

Replaces March 1998 version, DS4271-2.3

#### **APPLICATIONS**

- High Power Inverters And Choppers
- UPS
- Railway Traction
- Induction Heating
- AC Motor Drives
- Cycloconverters

#### **FEATURES**

- Double Side Cooling
- High Surge Capability
- High Voltage

#### **VOLTAGE RATINGS**

Type Number	Repetitive Peak Voltages V <sub>DRM</sub> V <sub>RRM</sub>	Conditions
TF219 20B	2000	$V_{RSM} = V_{RRM} + 100V$
TF219 18B	1800	
TF219 16B	1600	$I_{DRM} = I_{RRM} = 15 \text{mA}$
TF219 14B	1400	
		at $V_{_{\rm RRM}}$ or $V_{_{ m DRM}}$ & $T_{_{ m vj}}$

Lower voltage grades available.

#### **CURRENT RATINGS**

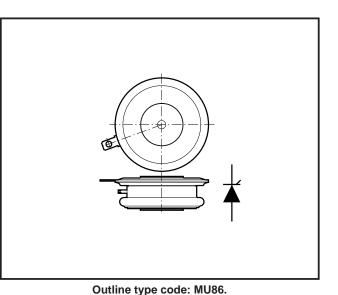
Symbol	Parameter	Conditions	Max.	Units
I <sub>T(AV)</sub>	Mean on-state current	Half sinewave, 50Hz, $T_{case} = 80^{\circ}C$	120	А
I <sub>T(RMS)</sub>	RMS value	Half sinewave, 50Hz, T <sub>case</sub> = 80°C	190	А

**TF219..B** 

# **Fast Switching Thyristor**

tq

#### **KEY PARAMETERS** $V_{\text{DRM}}$ 2000V 190A T(RMS) 1200A I<sub>TSM</sub> dV/dt 200V/µs dl/dt 500A/µs **40**µs



See Package Details for further information.

### TF219..B

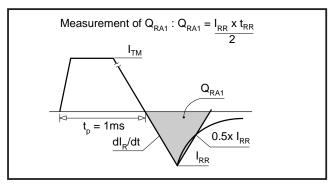
#### SURGE RATINGS

Symbol	Parameter	Conditions	Max.	Units
I <sub>TSM</sub>	Surge (non-repetitive) on-state current	10ms half sine; $V_{R} = 0\% V_{RRM}$ , $T_{j} = 125^{\circ}C$	1.2	kA
l²t	I <sup>2</sup> t for fusing	10ms half sine; $V_{R} = 0\% V_{RRM}$ , $T_{j} = 125^{\circ}C$	7.2	A²s

#### THERMAL AND MECHANICAL DATA

Symbol	Parameter	Conditions		Min.	Max.	Units
R <sub>th(j-c)</sub>	Thermal resistance - junction to case	Double side cooled	dc	-	0.1	°C/W
		Single side cooled	Anode dc	-	0.19	°C/W
			Cathode dc	-	0.24	°C/W
$R_{th(c-h)}$	Thermal resistance - case to heatsink	Clamping force 3.5kN with mounting compound	Double side	-	0.02	°C/W
			Single side	-	0.04	°C/W
$T_{vj}$	Virtual junction temperature	On-state (conducting)	On-state (conducting)		125	°C
		Reverse (blocking)		-	125	°C
T <sub>stg</sub>	Storage temperature range			-40	150	°C
-	Clamping force			3.3	3.6	kN

# **MEASUREMENT OF RECOVERED CHARGE - Q\_{RA1}**



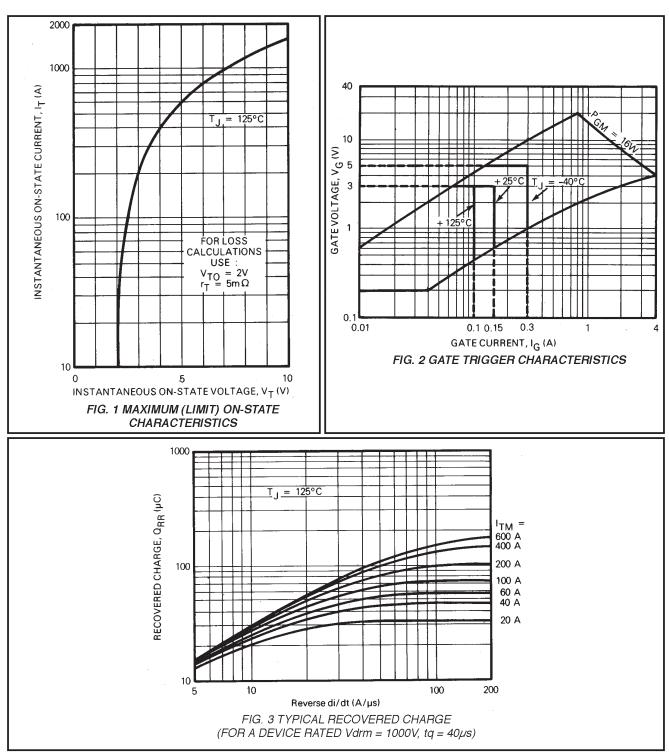
#### **DYNAMIC CHARACTERISTICS**

Symbol	Parameter	Conditions		Min.	Max.	Units
V <sub>TM</sub>	Maximum on-state voltage	At 150A peak, T <sub>case</sub> = 25°C		-	2.75	V
I <sub>RRM</sub> /I <sub>DRM</sub>	Peak reverse and off-state current	At V <sub>RRM</sub> /V <sub>DRM</sub> , T <sub>case</sub> = 125°C		-	15	mA
dV/dt	Maximum linear rate of rise of off-state voltage	Linear to 60% $V_{DRM} T_j = 125^{\circ}C$ , Gate open circuit		-	200	V/µs
		Gate source 20V, $20\Omega$	Repetitive 50Hz	-	500	A/μs
dl/dt	Rate of rise of on-state current	t <sub>r</sub> ≤ 0.5μs, T <sub>j</sub> = 125°C	Non-repetitive	-	800	A/μs
V <sub>T(TO)</sub>	Threshold voltage	At T <sub>vj</sub> = 125°C		-	2.0	V
r <sub>T</sub>	On-state slope resistance	At T <sub>vj</sub> = 125°C		-	5.0	mΩ
t <sub>gd</sub>	Delay time	$T_{j} = 25^{\circ}C, I_{T} = 100A,$		-	3*	μs
t <sub>(ON)TOT</sub>	Total turn-on time	$V_D = 50V, I_G = 1A,$ dl/dt = 50A/µs, dI <sub>G</sub> /dt = 1A/µs		-	1.5*	μs
I <sub>H</sub>	Holding current	T <sub>j</sub> = 25°C, I <sub>TM</sub> = 1A, V <sub>D</sub> = 12V		-	60	mA
t <sub>q</sub>	Turn-off time	$T_{i} = 125^{\circ}C$ , $I_{T} = 100A$ , $V_{R} = 50V$ dV/dt = 200V/ $\mu$ s (Linear to 60% dI <sub>R</sub> /dt = 30A/ $\mu$ s, Gate open circ	(, 6 V <sub>DRM</sub> ), t <sub>q</sub> code: B suit	-	40	μs

\*Typical value.

### GATE TRIGGER CHARACTERISTICS AND RATINGS

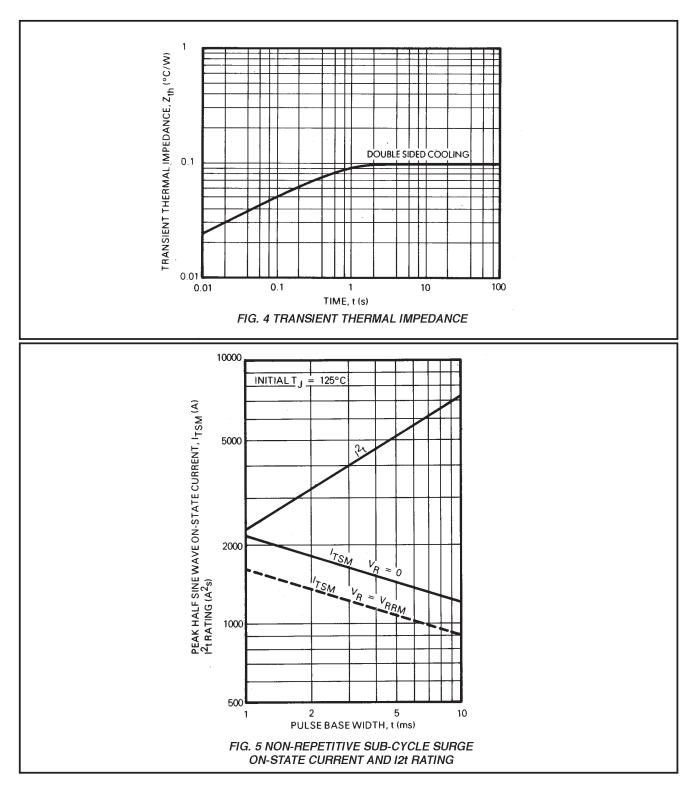
Symbol	Parameter	Conditions	Тур.	Max.	Units
V <sub>gt</sub>	Gate trigger voltage	$V_{\text{DRM}} = 12V, T_{\text{case}} = 25^{\circ}\text{C}, R_{\text{L}} = 6\Omega$	-	3.0	V
I <sub>GT</sub>	Gate trigger current	$V_{\text{DRM}} = 12V, \ T_{\text{case}} = 25^{\circ}\text{C}, \ R_{\text{L}} = 6\Omega$	-	200	mA
V <sub>gd</sub>	Gate non-trigger voltage	At $V_{DRM} T_{case} = 125^{\circ}C$ , $R_{L} = 1k\Omega$	-	0.2	V
V <sub>RGM</sub>	Peak reverse gate voltage		-	5.0	V
I <sub>FGM</sub>	Peak forward gate current	Anode positive with respect to cathode	-	4	А
P <sub>GM</sub>	Peak gate power		-	16	W
P <sub>G(AV)</sub>	Mean gate power		-	3	W



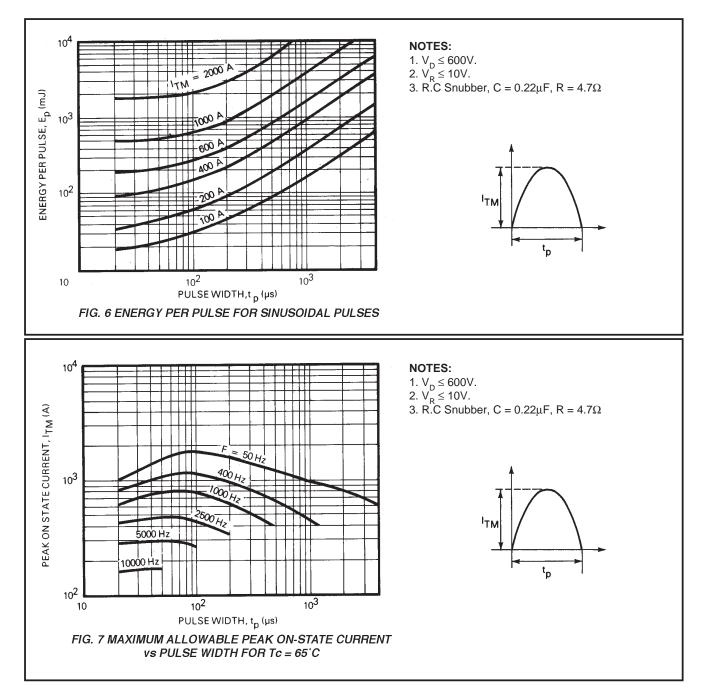
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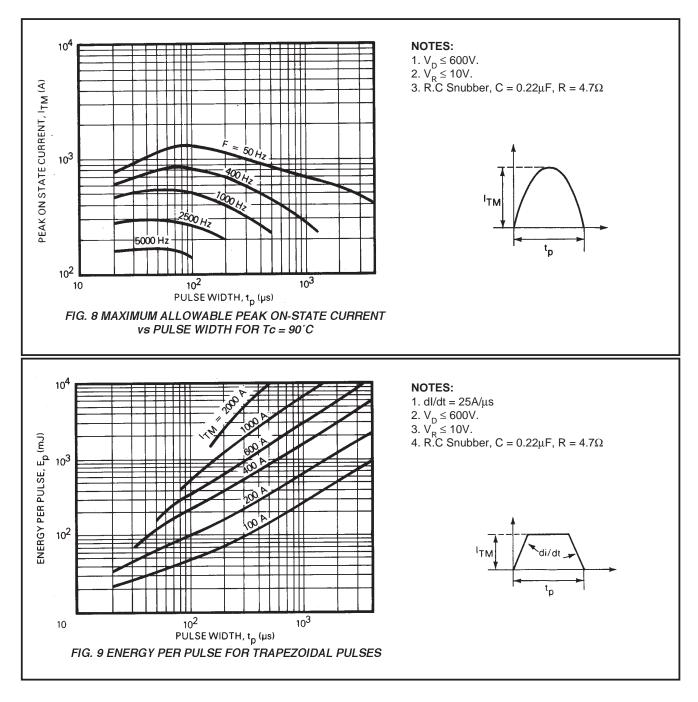
## CURVES

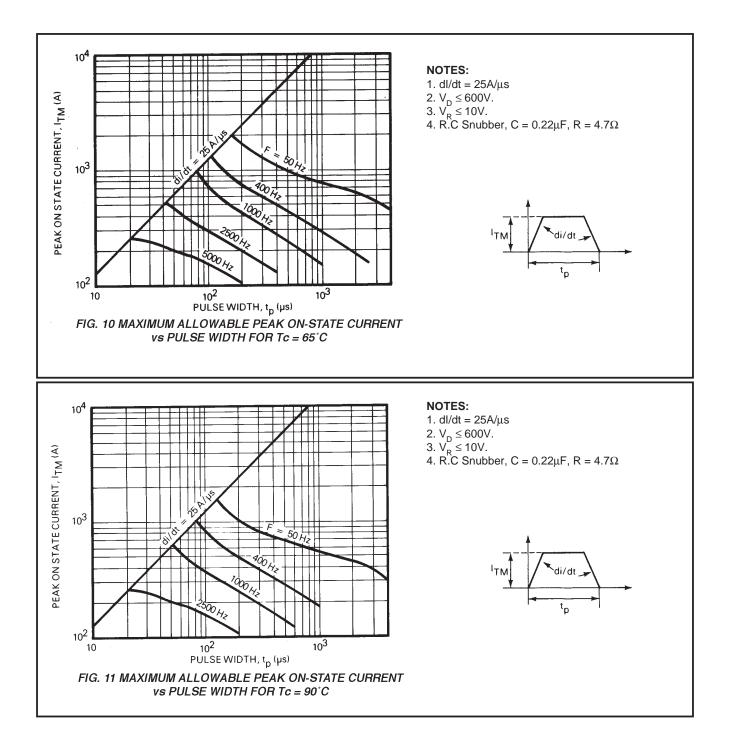
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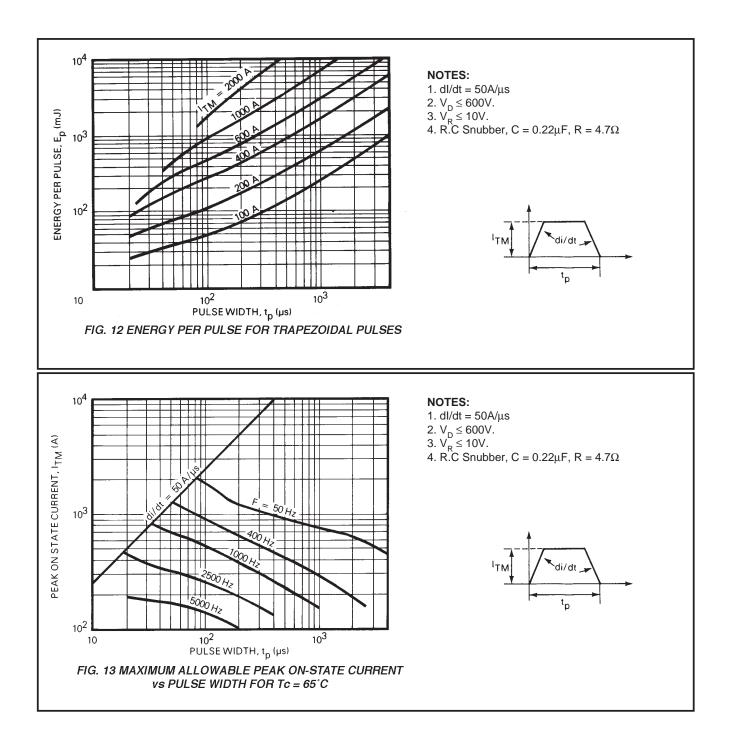


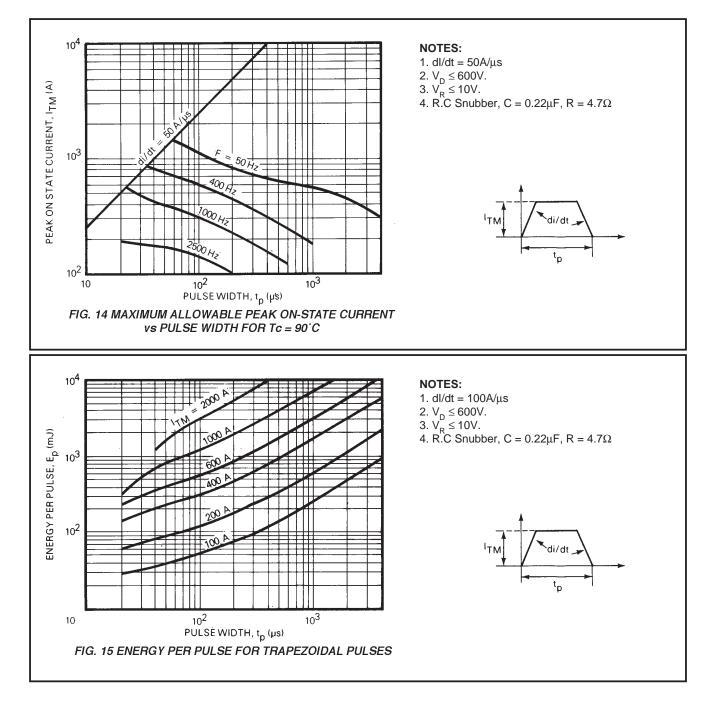
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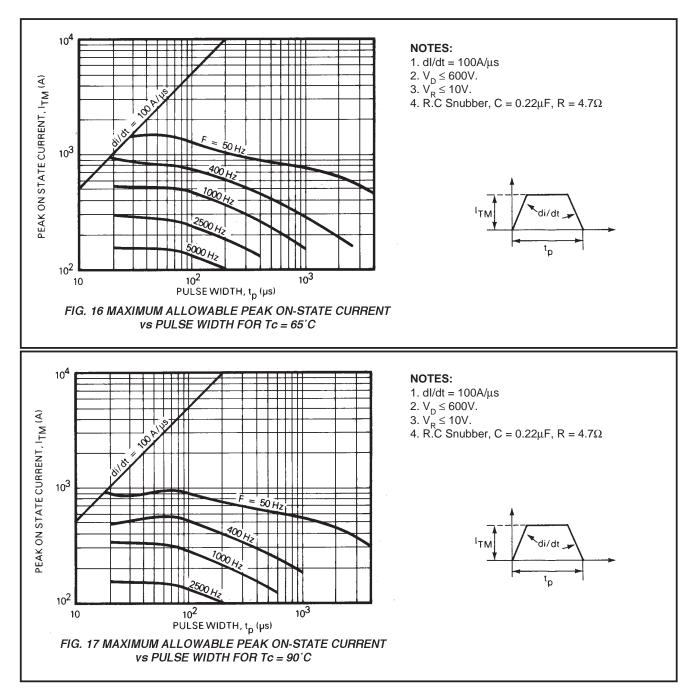






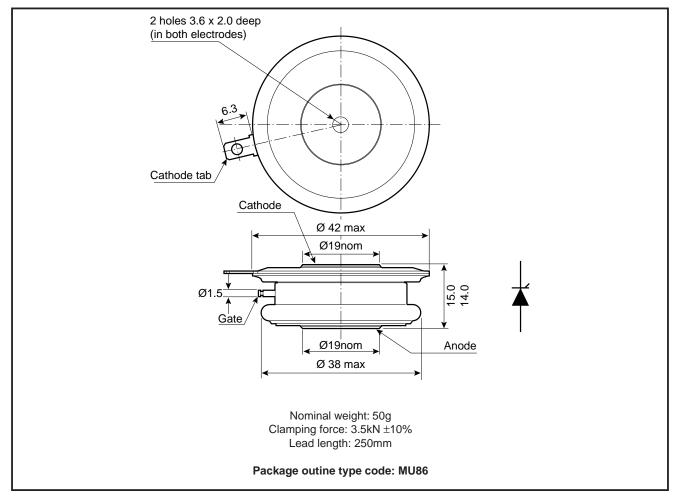






#### PACKAGE DETAILS

For further package information, please contact your local Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



#### **ASSOCIATED PUBLICATIONS**

Title	Application Note	
	Number	
Calculating the junction temperature or power semiconductors	AN4506	
Gate triggering and the use of gate characteristics	AN4840	
Recommendations for clamping power semiconductors	AN4839	
The effect of temperature on thyristor performance	AN4870	
Thyristor and diode measurement with a multi-meter	AN4853	
Turn-on performance of thyristors in parallel	AN4999	
Use of $V_{TO}$ , $r_{T}$ on-state characteristic	AN5001	

#### POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink / clamping systems in line with advances in device types and the voltage and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group continues to offer high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the up to date CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete solution (PACs).

#### **DEVICE CLAMPS**

Disc devices require the correct clamping force to ensure their safe operation. The PACs range offers a varied selection of preloaded clamps to suit all of our manufactured devices. This include cube clamps for single side cooling of 'T' 22mm

Clamps are available for single or double side cooling, with high insulation versions for high voltage assemblies.

Please refer to our application note on device clamping, AN4839

#### HEATSINKS

Power Assembly has it's own proprietary range of extruded aluminium heatsinks. They have been designed to optimise the performance or our semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest Sales Representative or the factory.



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Target Information: This is the most tentative form of information and represents a very preliminary specification. No actual design work on the product has been started.

Preliminary Information: The product is in design and development. The datasheet represents the product as it is understood but details may change.

Advance Information: The product design is complete and final characterisation for volume production is well in hand.

No Annotation: The product parameters are fixed and the product is available to datasheet specification.

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