

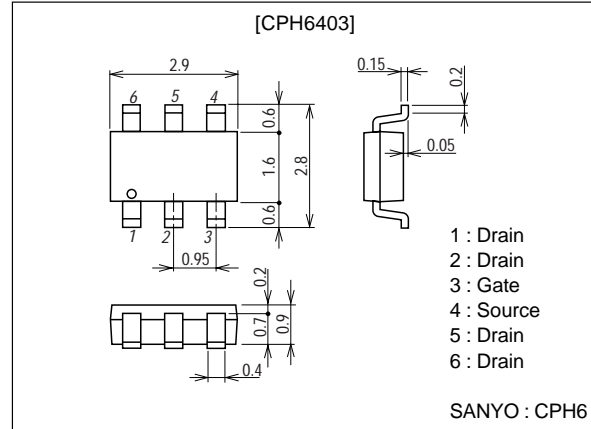
**CPH6403****Load Switching Applications****Features**

- Low ON-resistance.
- Ultrahigh-speed switching.
- 2.5V drive.

Package Dimensions

unit:mm

2151A

**Specifications****Absolute Maximum Ratings** at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		20	V
Gate-to-Source Voltage	V_{GSS}		± 10	V
Drain Current (DC)	I_D		6	A
Drain Current (pulse)	I_{DP}	$PW \leq 10\mu\text{s}$, duty cycle $\leq 1\%$	24	A
Allowable Power Dissipation	P_D	Mounted on a ceramic board (900mm ² ×0.8mm)	1.6	W
Channel Temperature	T_{ch}		150	$^\circ\text{C}$
Storage Temperature	T_{stg}		-55 to +150	$^\circ\text{C}$

Electrical Characteristics at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=1\text{mA}$, $V_{GS}=0$	20			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=20\text{V}$, $V_{GS}=0$			1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 8\text{V}$, $V_{DS}=0$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=10\text{V}$, $I_D=1\text{mA}$	0.5		1.3	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=10\text{V}$, $I_D=3\text{A}$	6.3	9		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D=3\text{A}$, $V_{GS}=4\text{V}$		28	38	$\text{m}\Omega$
	$R_{DS(on)2}$	$I_D=1\text{A}$, $V_{GS}=2.5\text{V}$		38	52	$\text{m}\Omega$
Input Capacitance	C_{iss}	$V_{DS}=10\text{V}$, $f=1\text{MHz}$		700		pF
Output Capacitance	C_{oss}	$V_{DS}=10\text{V}$, $f=1\text{MHz}$		200		pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS}=10\text{V}$, $f=1\text{MHz}$		150		pF

Marking : KC

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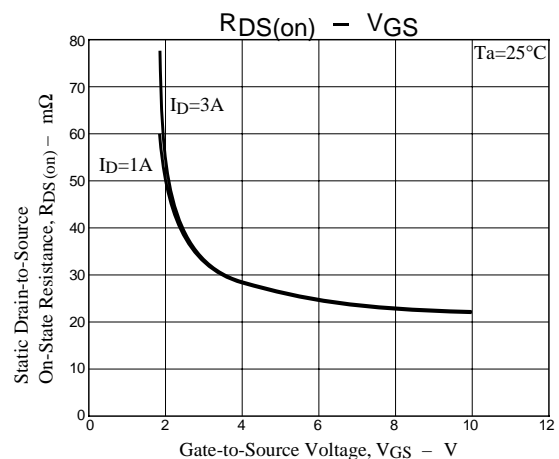
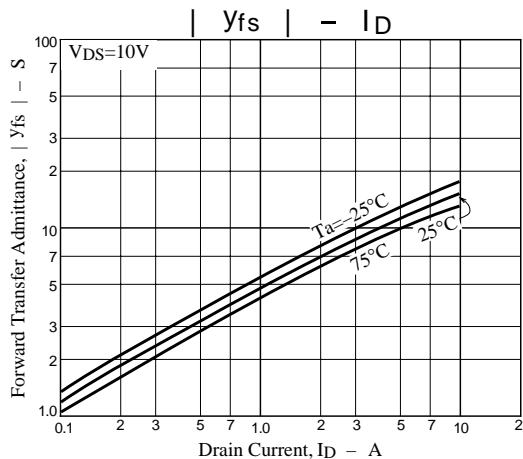
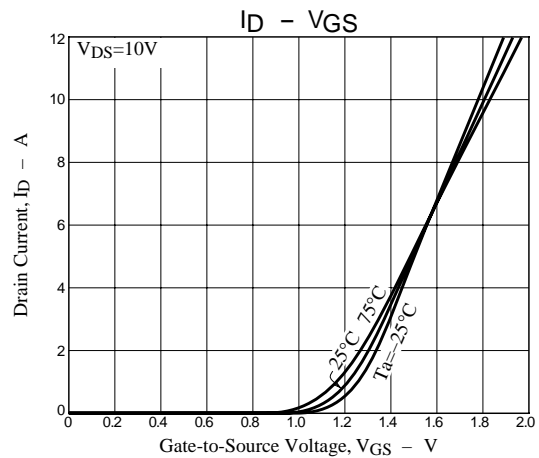
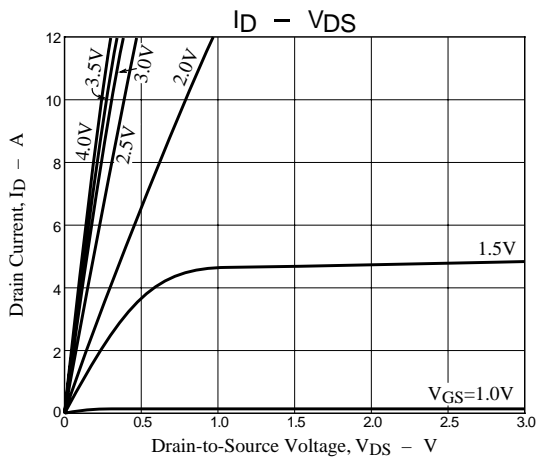
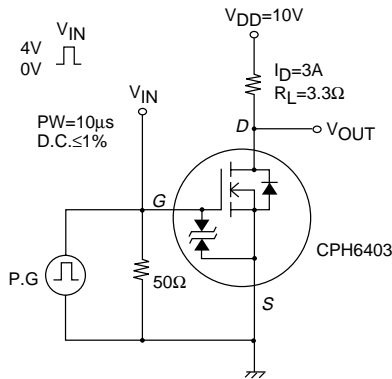
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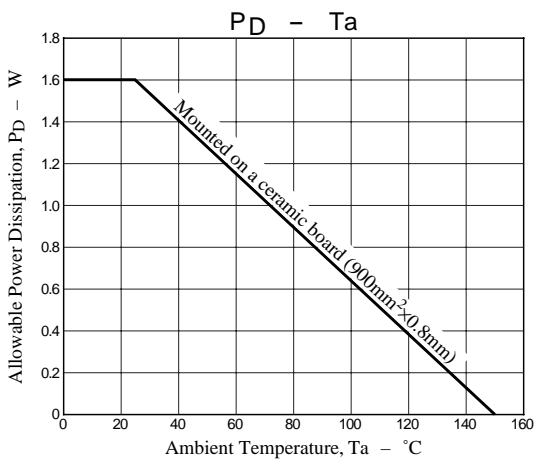
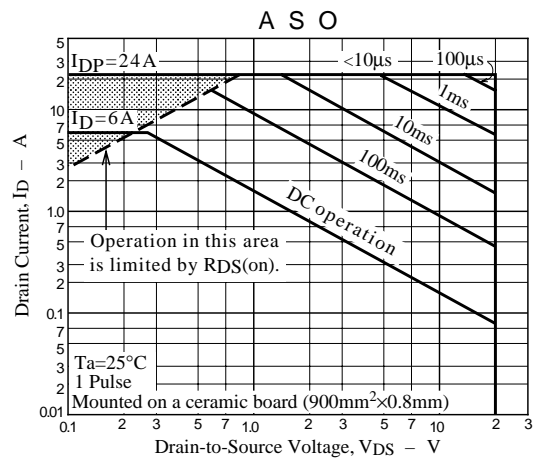
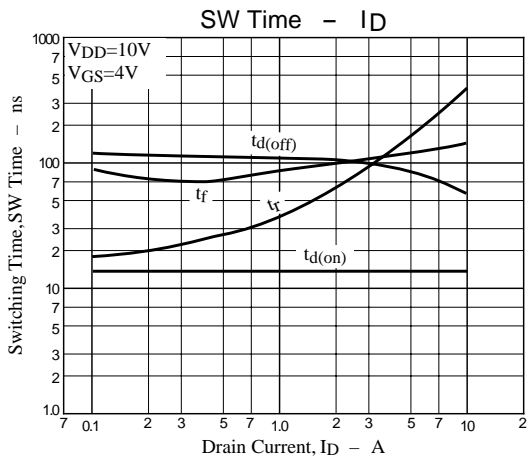
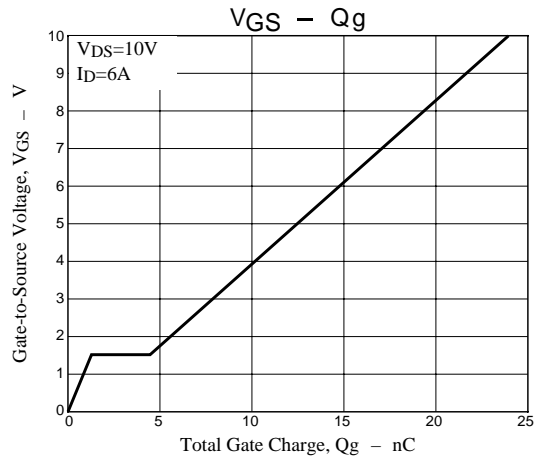
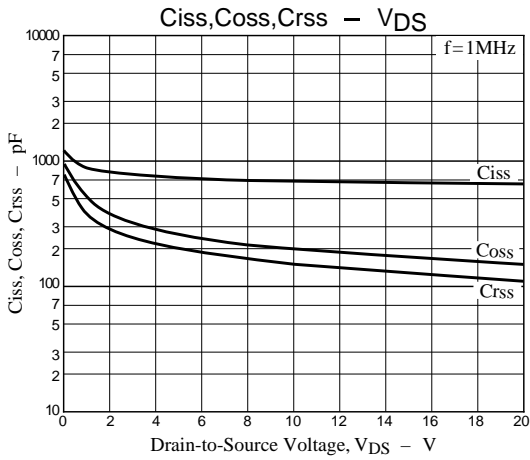
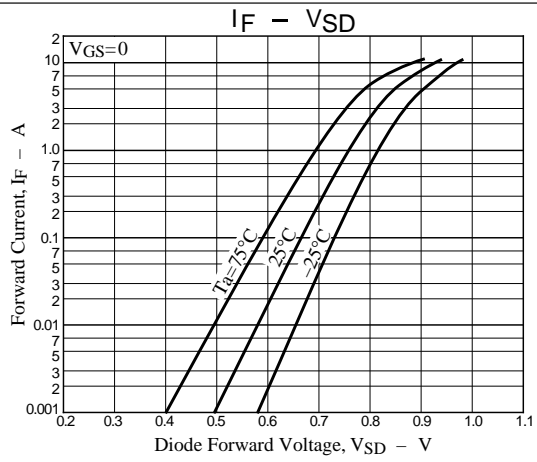
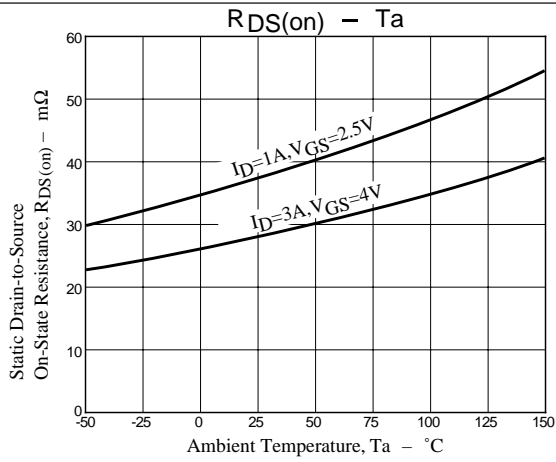
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit		14		ns
Rise Time	t_r	See specified Test Circuit		90		ns
Turn-OFF Delay Time	$t_{d(off)}$	See specified Test Circuit		90		ns
Fall Time	t_f	See specified Test Circuit		100		ns
Total Gate Charge	Qg	$V_{DS}=10V, V_{GS}=10V, I_D=6A$		24		nC
Gate-to-Source Charge	Qgs	$V_{DS}=10V, V_{GS}=10V, I_D=6A$		1.5		nC
Gate-to-Drain "Miller" Charge	Qgd	$V_{DS}=10V, V_{GS}=10V, I_D=6A$		3.2		nC
Diode Forward Voltage	V_{SD}	$I_S=6A, V_{GS}=0$		0.87	1.2	V

Switching Time Test Circuit



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