TOSHIBA BI-DIRECTIONAL TRIODE THYRISTOR SILICON PLANAR TYPE

SM6G45,SM6J45,SM6G45A,SM6J45A

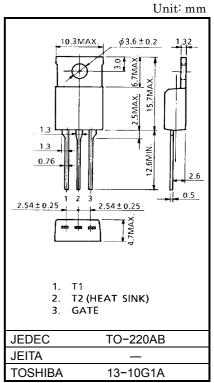
AC POWER CONTROL APPLICATIONS

Repetitive Peak Off-State Voltage : V_{DRM} = 400, 600V
 R.M.S ON-State Current : I_T (RMS) = 6A

• High Commutating (dv / dt)

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CHARACTERI	SYMBOL	RATING	UNIT		
Repetitive Peak Off-	SM6G45 SM6G45A	\/··	400	٧	
State Voltage	SM6J45 SM6J45A	V_{DRM}	600		
R.M.S On-State Current (Full Sine Waveform Tc	•	I _{T (RMS)}	6	Α	
Peak One Cycle Surge On-State Current (Non-Repetitive)		I _{TSM}	60 (50Hz)	Α	
			66 (60Hz)		
I ² t Limit Value		ı²t	18	A ² s	
Critical Rate of Rise of C Current	n-State	di / dt	50	A/μs	
Peak Gate Power Dissip	ation	P _{GM}	5	W	
Average Gate Power Dis	ssipation	P _{G (AV)}	0.5	W	
Peak Gate Voltage		V_{GM}	10	V	
Peak Gate Current		I _{GM}	2	Α	
Junction Temperature		Tj	-40~125	°C	
Storage Temperature Ra	ange	T _{stg}	-40~125	°C	



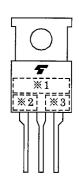
Weight: 2.0g



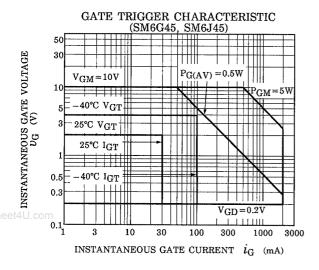
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

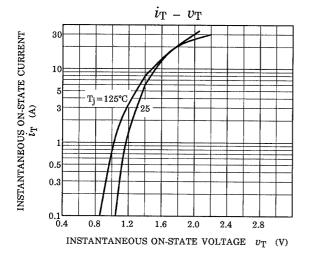
СН	ARACTERIS	TIC		SYMBOL	TEST CONDITION		MIN	TYP.	MAX	UNIT
Repetitive Pea Current	k Off-State			I _{DRM}	V _{DRM} = Rated, T _j = 125°C		_	_	2	mA
Gate Trigger Voltage		-	I	- V _{GT}	V _D = 12V R _L = 20Ω	T2 (+), Gate (+)	_	_	2	. v
	SM6G4		II			T2 (+), Gate (-)	_	_	2	
	SM6J45		III			T2 (-), Gate (-)	_	_	2	
			IV			T2 (-), Gate (+)	_	_	_	
						T2 (+), Gate (+)	_	_	1.5	
	SM6G4	-	II			T2 (+), Gate (-)	_	_	1.5	
	SM6J45		Ш			T2 (-), Gate (-)	_	_	1.5	
			IV			T2 (-), Gate (+)	_	_	_	
Gate Trigger Current			ı		$V_D = 12V$ $R_L = 20\Omega$	T2 (+), Gate (+)	_	_	30	- mA
	SM6G4	5	II			T2 (+), Gate (-)	_	_	30	
	SM6J45	,	III			T2 (-), Gate (-)	_	_	30	
			IV			T2 (-), Gate (+)	_	_	_	
			I	I _{GT}		T2 (+), Gate (+)	_	_	20	
	SM6G4	5A	II			T2 (+), Gate (-)	_	_	20	
	SM6J45	Ā	III			T2 (-), Gate (-)	_	_	20	
						T2 (-), Gate (+)	_	_	_	1
Peak On-State Voltage			V _{TM}	I _{TM} = 9A		_	_	1.5	V	
Gate Non-Trig	Gate Non-Trigger Voltage		V _{GD}	V _D = Rated, Tc = 125°C		0.2	_	_	V	
Holding Currer	ding Current I_H $V_D = 12V$, $I_{TM} = 1A$		_	_	50	mA				
Thermal Resis	nermal Resistance R _{th (j-c)} Junction to Case, AC		_	_	2.5	°C/W				
Critical Rate of Rise of Off- State Voltage at Commutation SM6G45 SM6J45 SM6G45A SM6J45A				V _{DRM} = 400V,		10	_	_	\/ / ···•	
				(dv / dt) c	(di / dt) c = -3.3A / ms T _j = 125°C		4		_	V / μs

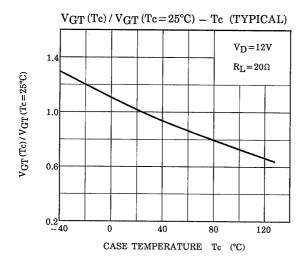
MARKING

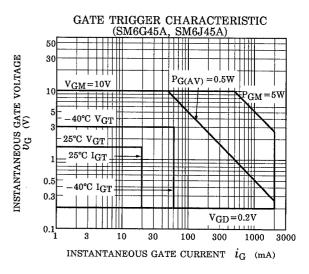


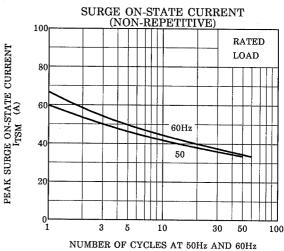
NUMBER	SYMBOL		MARK	
* 1		SM6G45, SM6G45A	M6G45	
'	TYPE	SM6J45, SM6J45A	M6J45	
* 2		SM6G45A, SM6J45A	A	
*3	Lot Number Month (Starting from Alphabet A) Year (Last Decimal Digit of the Current Year)		Example 8A : January 1998 8B : Febrary 1998 8L : December 1998	

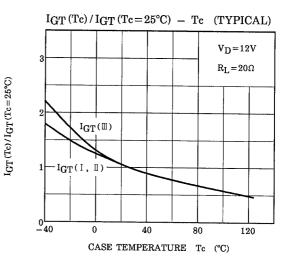


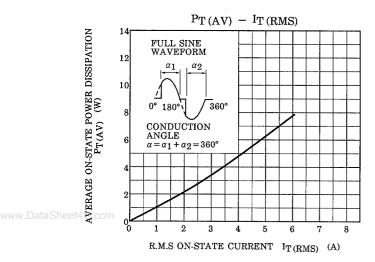


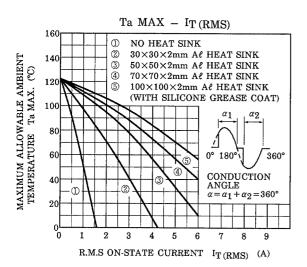


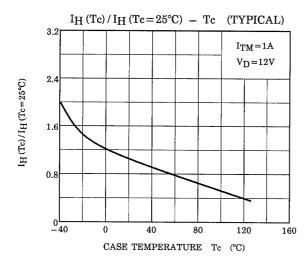


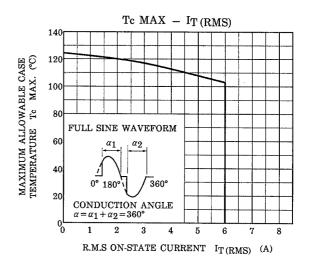


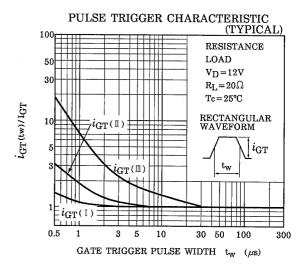


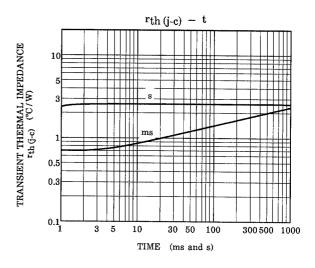












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