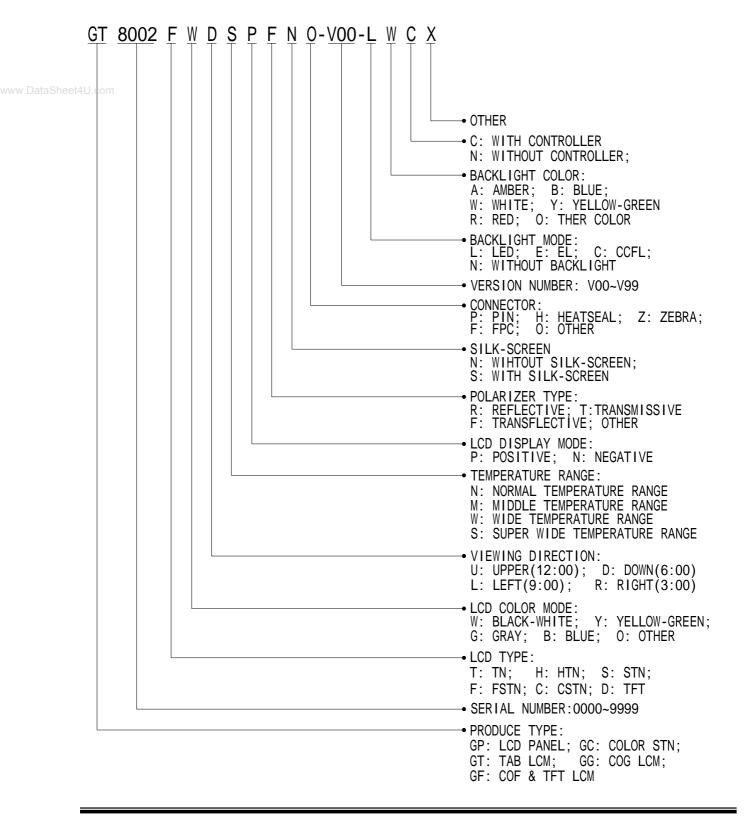
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LCM Number System



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1. GENERAL DESCRIPTION

The GT8002 is a 128 x 16 Dots Graphic LCD module. It has a FSTN panel composed of 128 segments and 16 commons. The LCM can be easily accessed by microcontroller via parallel 6800 timing interface.

2. FEATURES

Diaplay Mada	Transflective and positive
Display Mode	FSTN module
Display Format	Graphic 128x16 dots
Input Data	8 bit parallel input from MPU
Multiplexing Ratio	1/33 Duty
Bias	1/6 Bias
Viewing Direction	6 O'clock
Backlight	LED(White)

3. MECHANICAL SPECIFICATION

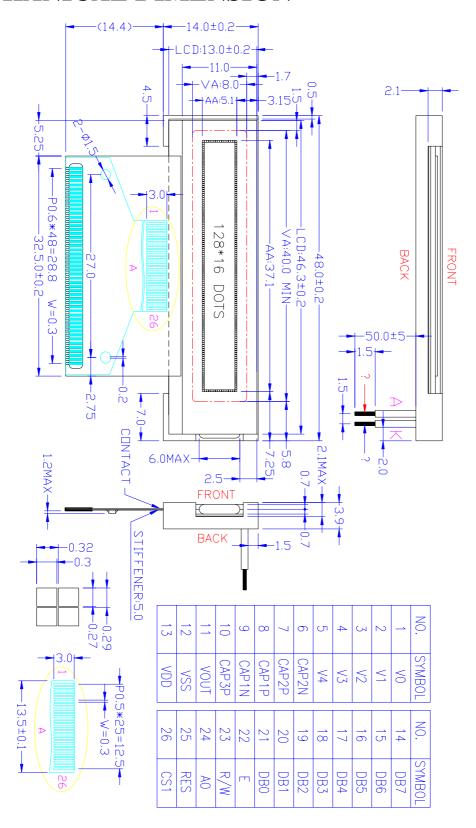
Item	Specifications	Unit
Dimensional outline	48.0(LED) x (14.0+14.4) x 3.9(max)	mm
Resolution	128segs x 16coms	dots
Active area	37.1(W) x 5.1(H)	mm
Dots pitch	0.29 (W)×0.32(H)	mm
Dots size	0.27(W)×0.3(H)	mm

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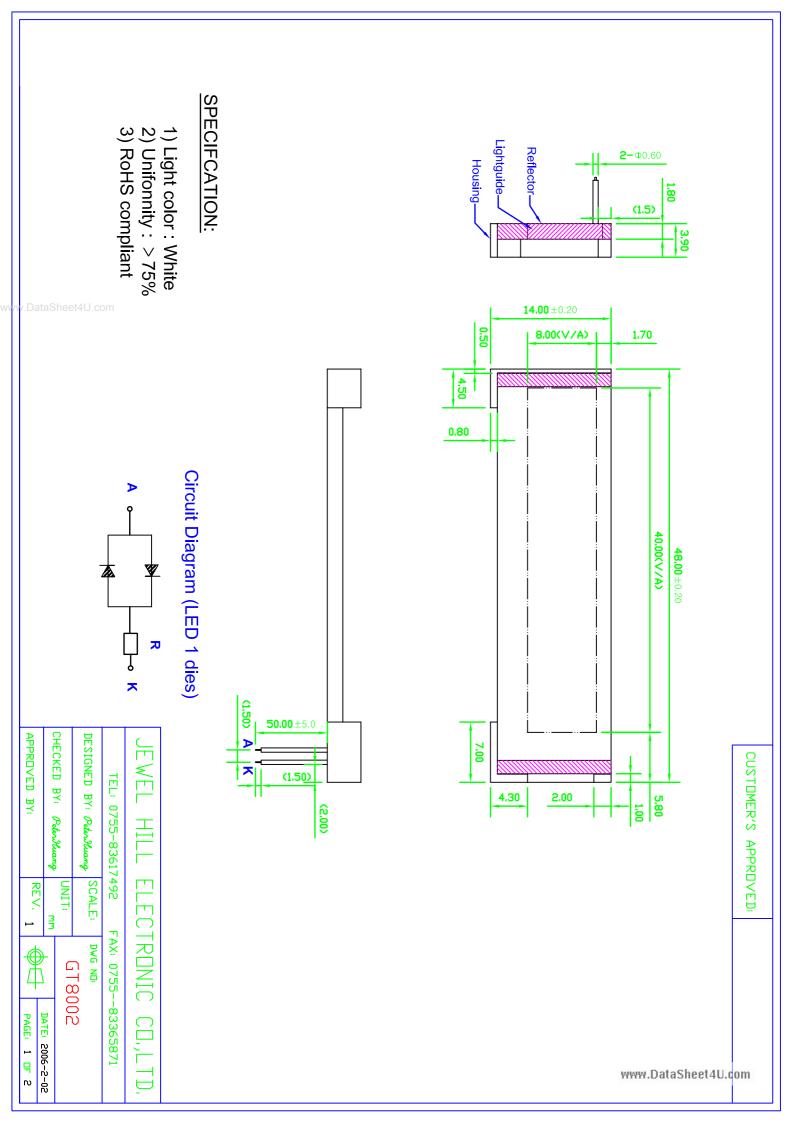


4. MECHANICAL DIMENSION

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JEWEL HILL ELECTRONICS LTD.

PRELIMINARY GT8002 LED BACKLIGHT FORLCD DISPLAY

1、极限参数 ABSOLUTE MAXIMUM RATINGE:

(除非特别说明,环境温度T=25° Unless specified, The Ambient temperature T=25°)

项目	符号	条件	值	单位
Item	Symbel	Conditions	Rating	Unit
* 极限直流正向电流 Absolute maximum forware current	Ifm		50	mA
* 脉冲驱动极限正向电流 Peak forward current	Ifp	I mseo plus10% Dutg Cyele	100	mA
反向电压 Reversr Voltage	Vr		0.8	V
* 极限功耗 Power dissipation	pd		60	mW
工作温度 Operating Temperature Range	Topr		- 20∼ +70	° C
贮存温度 Storage Temperature Range	Tstg		- 40 ∼ +85	• C

2、光电特性 ELECTRICAL-OPTICAL CHARACTERISTICS:

(除非特别说明,环境温度 T=25°C Unless specified, The Ambient tempetature T=25°C)

项目	符号	最小值	典型值	最大值	单位	条件
Item	Symbel	min.	typ.	max.	Unit.	Condition.
正向电压 Forward Voltage	Vf	3.2	3.5	3.8	V	If= 15 mA
反向电流 Reverse Current	Ir			30	mΑ	Vr= 0.8 V
Color Colorimeter	х	0.283		0.330		If= 15 mA
Color Colorimeter	У	0.276		0.338		
*亮度 Luminance	Lv	80	100		cd/m²	If= 15 mA

Design: 杨旭	Review:	COUNTERSIGH:	APPROVED:	
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5. MAXIMUM RATINGS

Item	Symbol	Min	Max	Unit	Note
G 1 1	V _{DD} - V _{SS}	-0.3	4.0	V	
Supply voltage	V_{LCD}	-0.3	18.0	V	
Input Voltage	V _{IN}	-0.3	V _{DD} +0.3	V	
Operating temperature	T_{OPR}	-25	+70		
Storage temperature	T_{STR}	-40	+85		
Humidity			90	%RH	

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6. ELECTRICAL CHARACTERISTICS

Item		Symbol	Condition	Min.	Тур.	Max.	Unit
Supply Voltage	Logic	$V_{\scriptscriptstyle DD}$		3.2	3.3	3.4	V
Innut Valtage	H level	$V_{\scriptscriptstyle \mathrm{IH}}$		$0.8V_{DD}$		$V_{\scriptscriptstyle m DD}$	V
Input Voltage	L level	$V_{\scriptscriptstyle \rm IL}$		V_{ss}		$0.2V_{DD}$	V
Current Consur (LCM:WITHOU	1	${ m I}_{ m DD}$	$V_{\text{DD}} = 3.3V;$ $V_{\text{LCD}} = 6.0V, T_{\text{amb}} = 25 ;$			1.2	mA
LCD Driving Voltage		$V_{\scriptscriptstyle LCD}$	Bias=1/6 VLCD=V0-Vss		6.0		V
Current Consumption (LCM:WITH LED)		$I_{ m LED}$	$\begin{array}{c} \text{VDD=3.3V} \\ \text{V}_{\text{LED}}\text{=}3.5\text{V}, \text{T}_{\text{amb}}\text{=}25 ; \end{array}$			TBD	mA

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7. MODULE FUNCTION DESCRIPTION

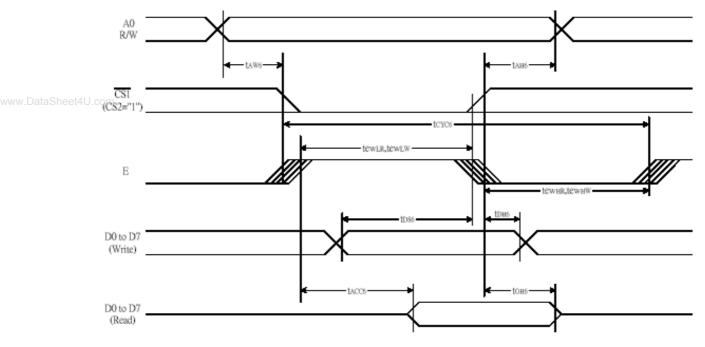
7.1. PIN DESCRIPTION

	Pin No.	Symbol	Description						
41	J.com 1	V0							
	2	V1	I CD Bigs Voltage:						
	3	V2	CD Bias Voltage; The Must Maintain the Relative Shown Below: V0 V1 V2 V3 V4 VSS						
	4	V3	V0 V1 V2 V3 V4 V8S						
	5	V4							
	6	CAP2N	DC DC Voltage Conventor Connect a Connector with Together						
	7	CAP2P	DC-DC Voltage Converter, Connect a Capacitor with Together						
	8	CAP1P	DC DC Valtage Conventor Connect a Conseitan with Tagether						
	9	CAP1N	DC-DC Voltage Converter, Connect a Capacitor with Together						
	10	CAP3P	DC-DC Voltage Converter, Connect to VOUT when Used 3-step Voltage Circuit						
	11	VOUT	DC-DC Voltage Output terminal						
	12	VSS	Power Supply for Ground(0V)						
	13	VDD	Power Supply for Positive(3.3V)						
	14-21	DB7-DB0	8-bit Bi-directional Data Bus for 6800-timing						
	22	E	Read/Write Enable Signal						
	23	R/W	Read/Write Selection Signal						
	24	A0	Data/Command Register Selection						
	25	/RES	Reset Signal						
	26	/CS1	Chip Selection Signal						

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7.2 TIMING CHARACTERISTICS

1.SYSTEM BUS READ/WRITE CHARACTERISTIC



System Bus Read/Write Characteristics 2 (For the 6800 Series MPU)

Figure 38

Table 27

(VDD = 3.3 V , Ta = 25°C)

14	Cinnal	Complete	Condition	Rat	Rating		
Item	Signal	Symbol	Condition	Min.	Max.	Units	
Address hold time		tan6		0	_		
Address setup time	A0	taw6		0	_		
System cycle time		tcyc6		240	_]	
Enable L pulse width (WRITE)	WR	tewLw		80	_		
Enable H pulse width (WRITE)	WE	tewnw		80	_		
Enable L pulse width (READ)	RD	tewlr		80	_	ns	
Enable H pulse width (READ)	KD.	tewhr		140]	
WRITE Data setup time		tosa		40	_		
WRITE Address hold time	DO to D7	tDH6		0	_		
READ access time		tacos	CL = 100 pF	_	70]	
READ Output disable time		tонs	CL = 100 pF	5	50		

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Table 28

(VDD = 2.7V , Ta =25°C)

	Item	Cianal	Symbol	Condition	Rati	Units	
	item	Signal	Symbol	Condition	Min.	Max.	Onits
	Address hold time		tans		0		
	Address setup time	A0	taw6		0	_	
	System cycle time		tcyc6		400	_	
	Enable L pulse width (WRITE)	WR .	tewLw		220	1	
www.DataSheet4U.co	Enable H pulse width (WRITE)		tewnw		180		
	Enable L pulse width (READ)	RD	tewlr		220		ns
	Enable H pulse width (READ)	2	tewhr		180	1	
	WRITE Data setup time		tose		40		
	WRITE Address hold time	D0 to D7	tDH6		0	_	
	READ access time		tacos	CL = 100 pF		140	
	READ Output disable time		ton6	CL = 100 pF	10	100	

Table 29

(VDD =1.8V , Ta =25°C)

	I			Rati		
Item	Signal	Symbol	Condition	Min.	Max.	Units
Address hold time		tan6		0	_	
Address setup time	A0	taw6		0	_]
System cycle time	1	tcyc6		640	_]
Enable L pulse width (WRITE)	WR	tewuw		360	_	1
Enable H pulse width (WRITE)	1 WK	tewnw		280	_	1
Enable L pulse width (READ)	RD	tewlr		360	_	ns
Enable H pulse width (READ)	1 80	tewhr		280	_	1
WRITE Data setup time		tose		80	_	1
WRITE Address hold time	D0 to D7	tDH6		0	_	1
READ access time	1 50000	tacos	CL = 100 pF	_	240	1
READ Output disable time	1	tons	CL = 100 pF	10	200	1

^{*1} The input signal rise time and fall time (tr, tf) is specified at 15 ns or less. When the system cycle time is extremely fast, (tr+tf) ≤ (tCYC6 - tEWLW - tEWHW) for (tr+tf) ≤ (tCYC6 - tEWHR - tEWHR) are specified.

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^{*2} All timing is specified using 20% and 80% of VDD as the reference.

^{*3} EWLW and tEWLR are specified as the overlap between CS1 being "L" (CS2 = "H") and E.

2. RESET TIMING



Figure 41

Table 36

(VDD = 3.3V , Ta = -40 to 85°C)

Item	Signal	Symbol	Condition		Units		
Itelli	Sigilal	Symbol	Collation	Min.	Тур.	Max.	Onits
Reset time		tr				1.0	us
Reset "L" pulse width	/RES	trw		1.0	_	_	us

Table 37

(VDD = 2.7V, Ta = -40 to 85°C)

Item	Signal	Symbol	Condition		Units			
itelli	Sigilal	Symbol	Condition	Min.	Тур.	Max.	Onits	
Reset time		tr				2.0	us	
Reset "L" pulse width	/RES	trw		2.0		-	us	

Table 38

(VDD = 1.8V, Ta = -40 to 85°C)

Item	Signal	Symbol	Condition	Condition			
item	olgilai	Symbol	Collation	Min.	Тур.	Max.	Units
Reset time		tr				3.0	us
Reset "L" pulse width	/RES	trw		3.0			us

^{*1} All timing is specified with 20% and 80% of VDD as the standard.

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7.3 APPLICATION OF LCM

■Reference circuit

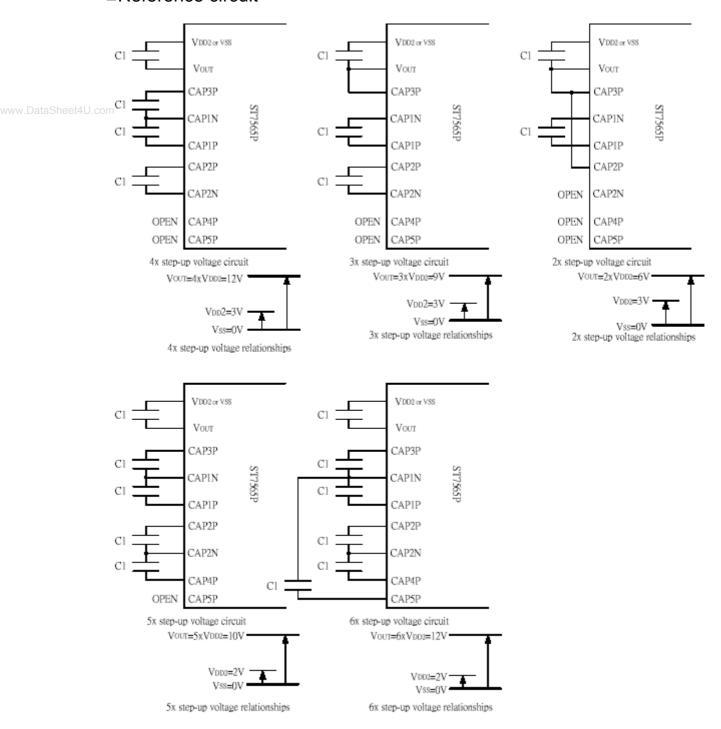


Figure 7

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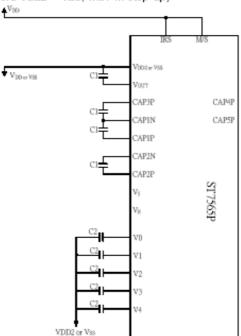
Reference Circuit Examples

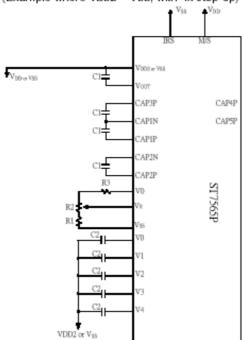
- 1. When used all of the step-up circuit, voltage regulating circuit and V/F circuit
- (1) When the voltage regulator internal resistor

(Example where VDD2 = VDD, with 4x step-up)

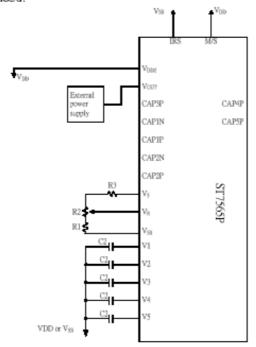
(2) When the voltage regulator internal resistor is not used.

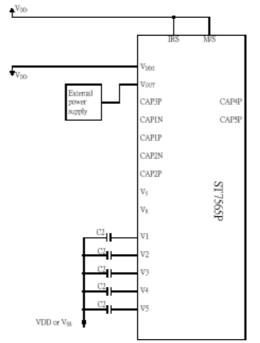
(Example where VDD2 = VDD, with 4x step-up)



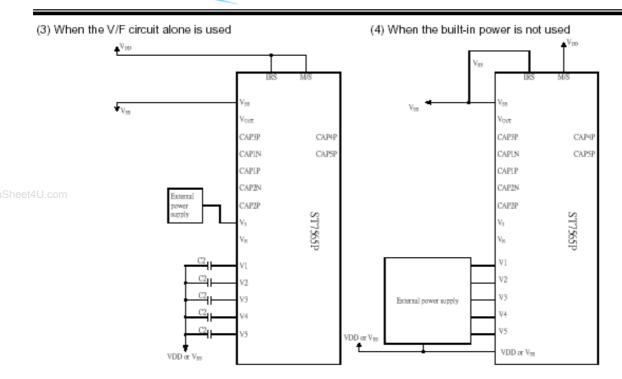


- 2. When the voltage regulator circuit and V/F circuit alone are used
- When the Vs voltage regulator internal resistor is not used.
- (2) When the Vs voltage regulator internal resistor is used.





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Item	Set value	units
c1	1.0 to 4.7	uF
c2	0.1 to 4.7	uF

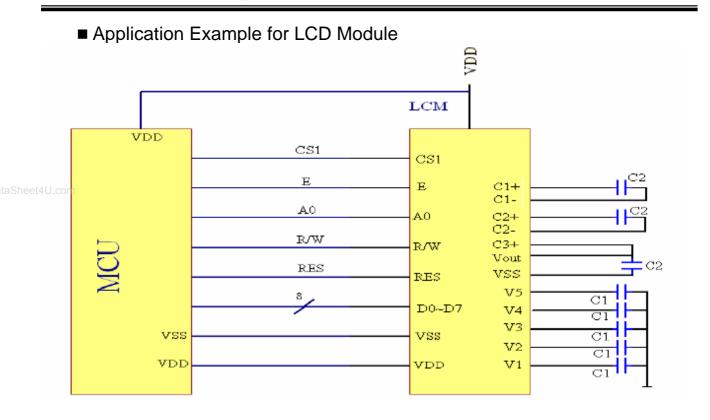
C1 and C2 are determined by the size of the LCD being driven

- *1. Because the VR terminal input impedance is high, use short leads and shielded lines.
- * 2. C1 and C2 are determined by the size of the LCD being driven. Select a value that will stabilize the liquid crystal drive voltage.

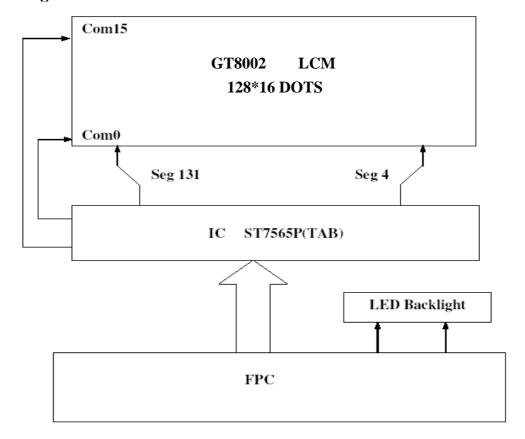
Example of the Process by which to Determine the Settings:

- · Turn the voltage regulator circuit and voltage follower circuit ON and supply a voltage to VOUT from the outside.
- Determine C2 by displaying an LCD pattern with a heavy load (such as horizontal stripes) and selecting a C2 that stabilizes
 the liquid crystal drive voltages (V1 to V5). Note that all C2 capacitors must have the same capacitance value.
- Next turn all the power supplies ON and determine C1.

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■ Block Diagram



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7.4 TABLE OF COMMAND

Table 16: Table of ST7565P Commands (Note) *: disabled data

	Command		Command Code				I Function						
	Command	Α0	/RD	MR	D7	D6	D5	D4	D3	D2	D1	טע	L
	(1) Display ON/OFF	0	1	0	1	0	1	0	1	1	1	0 1	LCD display ON/OFF 0: OFF, 1: ON
	(2) Display start line set	0	1	0	0	1	Di	spla	y sta	art a	ddre	ss	Sets the display RAM display start line address
١	(3) Page address set	0	1	0	1	0	1	1	Pa	ge a	d dre	ess	Sets the display RAM page address
CO	(4) Column address set	0	1	0	0	0	0	1				ant	Sets the most significant 4 bits of
	upper bit Column address set lower bit	0	1	0	0	0	0	0	Lea	st si	gnifi	ress cant ress	the display RAM column address. Sets the least significant 4 bits of the display RAM column address.
	(5) Status read	0	0	1		St	atus		0	0	0	0	Reads the status data
	(6) Display data write	1	1	0			١	Vrite	e dat	a			Writes to the display RAM
	(7) Display data read	1	0	1			F	Rea	d dat	a			Reads from the display RAM
	(8) ADC select	0	1	0	1	0	1	0	0	0	0	0 1	Sets the display RAM address SEG output correspondence 0: normal, 1: reverse
	(9) Display normal/ reverse	0	1	0	1	0	1	0	0	1	1	0 1	Sets the LCD display normal/ reverse 0: normal, 1: reverse
	(10) Display all points ON/OFF	0	1	0	1	0	1	0	0	1	0	0 1	Display all points 0: normal display 1: all points ON
	(11) LCD bias set	0	1	0	1	0	1	0	0	0	1	0 1	Sets the LCD drive voltage bias ratio 0: 1/9 bias, 1: 1/7 bias (ST7565P)
	(12) Read/modify/write	0	1	0	1	1	1	0	0	0	0	0	Column address increment At write: +1 At read: 0
1	(13) End	0	1	0	1	1	1	0	1	1	1	0	Clear read/modify/write
1	(14) Reset	0	1	0	1	1	1	0	0	0	1	0	Internal reset
	(15) Common output mode select	0	1	0	1	1	0	0	0 1	*	*	*	Select COM output scan direction 0: normal direction 1: reverse direction
	(16) Power control set	0	1	0	0	0	1	0	1		erat de	ing	Select internal power supply operating mode
	(17) V0 voltage regulator internal resistor ratio set	0	1	0	0	0	1	0	0		siste tio	or	Select internal resistor ratio(Rb/Ra) mode
	(18) Electronic volume mode set Electronic volume register set	0	1	0	1 0	0	0 Ele	0 ctro	0 nic v		_	1 alue	Set the V0 output voltage electronic volume register
	(19) Static indicator ON/OFF	0	1	0	1	0	1	0	1	1	0	0	0: OFF, 1: ON
	Static indicator register set	,		Ü	0	0	0	0	0	0	0	Mode	Set the flashing mode
	(20) Booster ratio set	0	1	0	1 0	1 0	1 0	1 0	1 0	0		0 o-up lue	select booster ratio 00: 2x,3x,4x 01: 5x 11: 6x
	(21) Power saver												Display OFF and display all points ON compound command
1	(22) NOP	0	1	0	1	1	1	0	0	0	1	1	Command for non-operation
	(23) Test	0	1	0	1	1	1	1	*	*	*	*	Command for IC test. Do not use this command

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8. ELECTRO-OPTICAL CHARACTERISTICS

Item	Symbol	Condition	Temp	Min	Тур.	Max	Units	Note		
L CD 11:			0°C		6.2					
LCD driving	VLCD	$\theta = \phi = 0$	25℃	5.7	6.0	6.3	V	NOTE1		
voltage			50°C		5.8					
	Rise Time (Tr)		0°C							
	Decay Time (Tf)	$\theta = \phi = 0$	0℃							
	Rise Time (Tr)		$\theta = \phi = 0$	$\theta = \phi = 0$	O.C.°C		225	340		
Response Time	Decay Time (Tf)				$\theta = \phi = 0$	$\theta = \phi = 0$	$\theta = \phi = 0$	25°C		240
	Rise Time (Tr)		50°C							
	Decay Time (Tf)		50°C							
Contrast Ratio	Cr	$\theta = \phi = 0$	25℃	5	10			NOTE4		

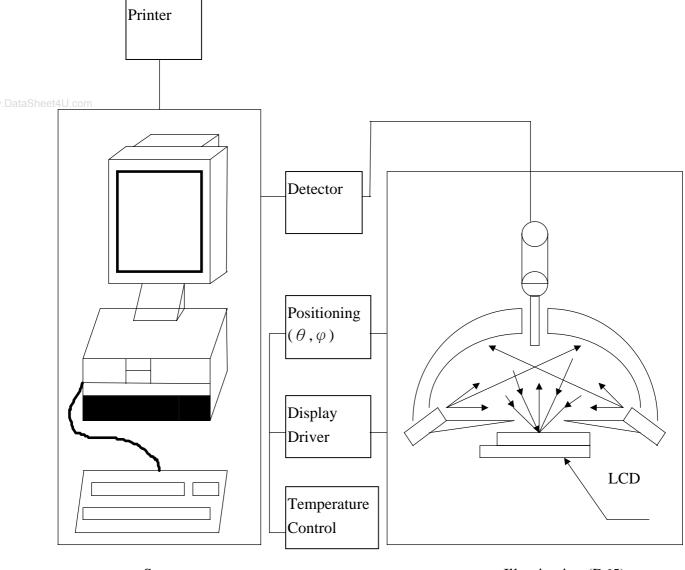
Viewing Angle Range	$\theta (\phi = 0^{\circ})$ (6")	$\phi = 90^{\circ}$ (3")	φ=180° (12")	φ=270° (9")	備註
θ (25°C) CR≥2	50	30	30	25	Deg NOTE3

For panel only

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• Electro-Optical Characteristics Measuring Equipment(DMS501)

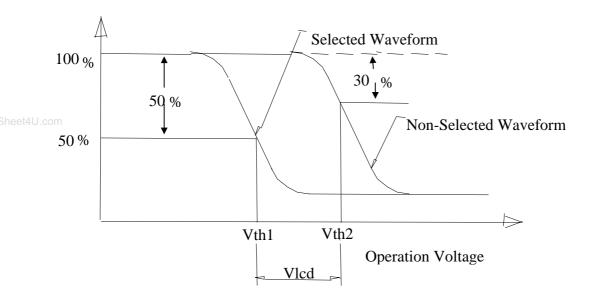


System Illumination (D65)

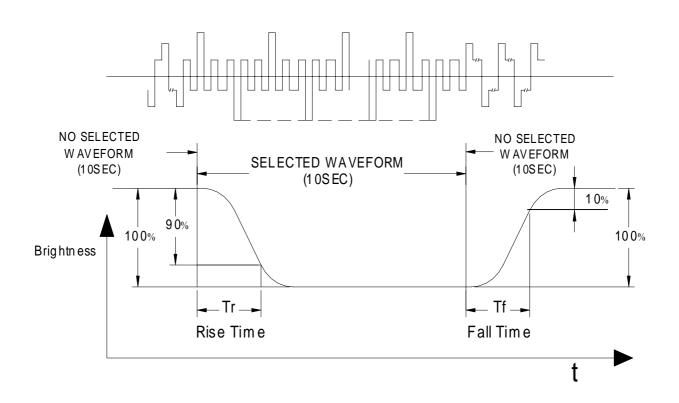
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• Note 1. Definition of Driving Voltage(Vlcd):



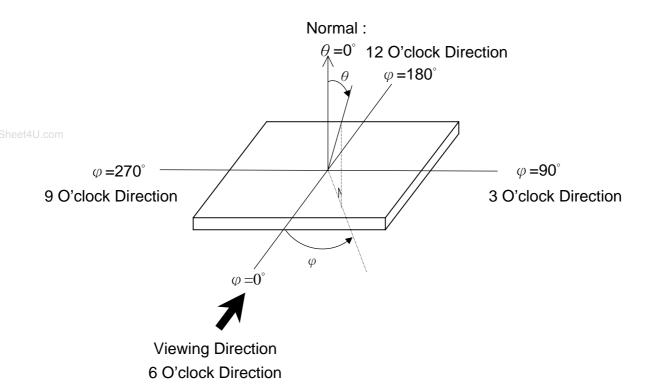
• Note 2. Definition of Optical Response Time :



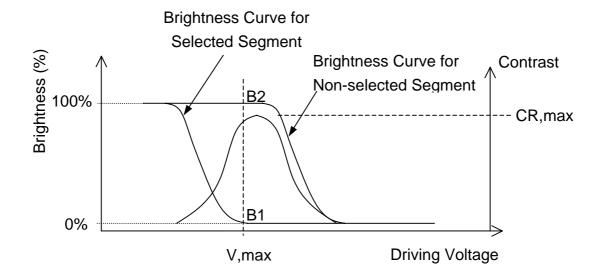
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• Note 3. Definition of Viewing Angle $\,\theta\,$ and $\,\phi\,$:



• Note 4. Definition of Contrast ratio(CR):



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9. RELIABILITY

9.1. MTBF

The LCD module shall be designed to meet a minimum MTBF value of 50000 hours with normal. (25°C in the room without sunlight)

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9.2. TESTS

NO.	ITEM	CONDITION	CRITERION
1	High Temperature Operating	70 120Hrs	No Defect OfOperational Function InRoom Temperature Are
2	Low Temperature Operating	-25 120Hrs	Allowable. o IDD of LCM in
3	High Temperature/ Humidity Non-Operating	60 ,90%RH ,120 Hrs	Pre-and post-test should follow specification
4	High Temperature Non-Operating	85 120Hrs	
5	Low Temperature Non-Operating	-40 120Hrs	
6	Temperature Cycling Non-Operating	-40 (30Min) ↔ 85 (30Min) 10 CYCLES	

Notes: Judgments should be mode after exposure in room temperature for two hours.

7	ESD Test	Contact Discharges: ±4KV; Air Discharges: ±8KV; 2 pcs	 When turning on power again, No abnormalities in functions
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10. PRECAUTIONS FOR USING LCD MODULES

10.1. HANDLING PRECAUTIONS

- (1) The display panel is made of glass. Do not subject it to a mechanical shock or impact by dropping it.
- (2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- (3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- (4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- (5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten a cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol
- (6) Solvents other than those above mentioned may damage the polarizer.

Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- (7) Extra care to minimize corrosion of the electrode. Water droplets, moisture condensation or a current flow in a high-humidity environment accelerates corrosion of the electrode.
- (8) Install the LCD Module by using the mounting holes. When mounting the LCD Module, make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- (9) Do not attempt to disassemble or process the LCD Module.
- (10) NC terminal should be open. Do not connect anything.
- (11) If the logic circuit power is off, do not apply the input signals.
- (12) To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - Be sure to ground the body when handling he LCD Module.
 - Tools required for assembling, such as soldering irons, must be properly grounded.
 - -To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.

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-The LCD Module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

10.2. STORAGE CONDITIONS

When storing, avoid the LCD module to be exposed to direct sunlight of fluorescent lamps. For stability, to keep it away form high temperature and high humidity environment (The best condition is : 23 ± 5 °C, 45 ± 20 %RH). ESD protection is necessary for long-term storage also.

10.3. OTHERS

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD Module have been operating for a long time showing the same display patterns the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be recovered by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD Module resulting from destruction caused by static electricity etc. exercise care to avoid holding the following sections when handling the modules.

- Exposed area of the printed circuit board.
- Terminal electrode sections.

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11. Using LCD modules

11.1 LIQUID CRYSTAL DISPLAY MODULES

LCD is composed of glass and polarizer. Pay attention to the following items when handling.

- (1) Please keep the temperature within specified range for use and storage. Polarization degradation, www.DataSheet4U.cbubble generation or polarizer peel-off may occur with high temperature and high humidity.
 - (2) Do not touch, push or rub the exposed polarizers with anything harder than a HB pencil lead (glass, tweezers, etc).
 - (3) N-hexane is recommended for cleaning the adhesives used to attach front/rear polarizers and reflectors made of organic substances, which will be damaged by chemicals such as acetone, toluene, ethanol and isopropyl alcohol.
 - (4) When the display surface becomes dusty, wipe gently with absorbent cotton or other soft material like chamois soaked in petroleum ether. Do not scrub hard to avoid damaging the display surface.
 - (5) Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading.
 - (6) Avoid contacting oil and fats.
 - (7) Condensation on the surface and contact with terminals due to cold will damage, stain or polarizers. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
 - (8) Do not put or attach anything on the display area to avoid leaving marks on.
 - (9) Do not touch the display with bare hands. This will stain the display area and degrade insulation between terminals (some cosmetics are determinate to the polarizers).
 - (10)As glass is fragile, it tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring.

11.2 INSTALLING LCD MODULE

Attend to the following items when installing the LCM.

- (1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.
- (2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be ± 0.1 mm.

11.3 ELECTRO-STATIC DISCHARGE CONTROL

Since this module uses a CMOS LSI, the same careful attention should be paid for electrostatic discharge as for an ordinary CMOS IC.

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- (1) Make certain that you are grounded when handing LCM.
- (2) Before removing LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential.
- (3) When soldering the terminal of LCM, make certain the AC power source for the soldering iron does not leak.
- (4) When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- (5) As far as possible, make the electric potential of your work clothes and that of the workbenches to the ground potential.
- (6) To reduce the generation of electro-static discharge, be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended.

11.4 PRECAUTIONS FOR OPERATION

- (1) Viewing angle varies with the change of liquid crystal driving voltage (Vo). Adjust Vo to show the best contrast.
- (2) Driving the LCD in the voltage above the limit will shorten its lifetime.
- (3) Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.
- (4) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then on.
- (5) Condensation on terminals can cause an electrochemical reaction disrupting the terminal circuit. Therefore, this product must be used and stored within the specified condition of 23±5°C, 45±20% RH.
- (6) When turning the power on, input each signal after the positive/negative voltage becomes stable.

11.5 SAFETY

- (1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- (2) If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

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12. REVISION HISTORY

Version	Revise record	Date
1.0	Original version	06-02-01
l.com		

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SAMPLE APPROVED REPORT

(样品确认单)

GT8002
FSTN/POSITIVE
6:00
1/33Duty, 1/6Bias
3.3V
6.0V
-25~70
-40~85
TRANSMISSIVE
TRANSFLECTIVE
ST7565P
LED/White
REV: 1 / BETB: 1
BETB: 1
DATE:
N.G.
□ N.G.
□ N.G.
□ N.G.
□ N.G.
oK □ N.G.
DATE (日期):

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