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DS75176B/DS75176BT Multipoint RS-485/RS-422 Transceivers

National Semiconductor

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General Description

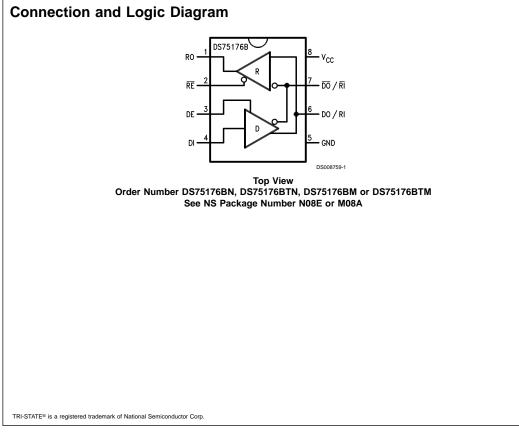
The DS75176B is a high speed differential TRI-STATE® bus/line transceiver designed to meet the requirements of EIA standard RS485 with extended common mode range (+12V to -7V), for multipoint data transmission. In addition, it is compatible with RS-422.

The driver and receiver outputs feature TRI-STATE capability, for the driver outputs over the entire common mode range of +12V to -7V. Bus contention or fault situations that cause excessive power dissipation within the device are handled by a thermal shutdown circuit, which forces the driver outputs into the high impedance state.

DC specifications are guaranteed over the 0 to 70°C temperature and 4.75V to 5.25V supply voltage range.

Features

- Meets EIA standard RS485 for multipoint bus transmission and is compatible with RS-422.
- Small Outline (SO) Package option available for
- minimum board space. 22 ns driver propagation delays.
- Single +5V supply.
- –7V to +12V bus common mode range permits ±7V ground difference between devices on the bus.
- Thermal shutdown protection.
- High impedance to bus with driver in TRI-STATE or with power off, over the entire common mode range allows the unused devices on the bus to be powered down.
- Pin out compatible with DS3695/A and SN75176A/B.
- Combined impedance of a driver output and receiver input is less than one RS485 unit load, allowing up to 32 transceivers on the bus.
- 70 mV typical receiver hysteresis.



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Absolute Maximum Ratings (Note 1)

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If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Supply Voltage, V _{CC}	7V
Control Input Voltages	7V
Driver Input Voltage	7V
Driver Output Voltages	+15V/ -10V
Receiver Input Voltages (DS75176B)	+15V/ -10V
Receiver Output Voltage	5.5V
Continuous Power Dissipation @ 25°C	
for M Package	675 mW (Note 5)
for N Package	900 mW (Note 4)
Storage Temperature Range	–65°C to +150°C
Lead Temperature (Soldering, 4 seconds)	260°C

Recommended Operating Conditions

	Min	Max	Units
Supply Voltage, V _{CC}	4.75	5.25	V
Voltage at Any Bus Terminal	-7	+12	V
(Separate or Common Mode)			
Operating Free Air Temperature T _A			
DS75176B	0	+70	°C
DS75176BT	-40	+85	°C
Differential Input Voltage,			
VID (Note 6)	-12	+12	V

Electrical Characteristics (Notes 2, 3)

 $0^{\circ}C \leq T_{A}\!\!\leq 70^{\circ}C,\,4.75V$ < $V_{CC}\!<$ 5.25V unless otherwise specified

Symbol	Paramet	ter	Conditions		Min	Тур	Max	Units
V _{OD1}	Differential Driver Outp	out	I _O = 0				5	V
	Voltage (Unloaded)							
V _{OD2}	Differential Driver Outp	out	(Figure 1)	R = 50Ω; (RS-422) (Note 7)	2			V
	Voltage (with Load)			R = 27Ω; (RS-485)	1.5			V
ΔV_{OD}	Change in Magnitude	of Driver						
	Differential Output Volt	tage For					0.2	V
	Complementary Output	t States						
V _{oc}	Driver Common Mode	Output	(Figure 1)	R = 27Ω			3.0	V
	Voltage							
$\Delta V_{OC} $	Change in Magnitude	of Driver	1					
	Common Mode Output	t Voltage					0.2	V
	For Complementary O	utput						
	States							
VIH	Input High Voltage				2			V
VIL	Input Low Voltage	(DI, DE,				0.8	
V _{CL}	Input Clamp Voltage	 i	RE, E	I _{IN} = -18 mA			-1.5	
I _{IL}	Input Low Current			$V_{IL} = 0.4V$			-200	μA
I _{IH}	Input High Current			V _{IH} = 2.4V			20	μA
I _{IN}	Input	DO/RI, DO/RI	V _{CC} = 0V or 5.25V	V _{IN} = 12V			+1.0	mA
	Current		DE = 0V	$V_{IN} = -7V$			-0.8	mA
V _{TH}	Differential Input Three	shold	$-7V \le V_{CM} \le + 12V$		-0.2		+0.2	V
	Voltage for Receiver							
ΔV_{TH}	Receiver Input Hystere	esis	$V_{CM} = 0V$			70		m٧
V _{он}	Receiver Output High	Voltage	$I_{OH} = -400 \ \mu A$		2.7			V
V _{OL}	Output Low Voltage	RO	I _{OL} = 16 mA (Note 7))			0.5	V
I _{OZR}	OFF-State (High Impe	dance)	V _{CC} = Max				±20	μA
	Output Current at Rec	eiver	$0.4V \le V_O \le 2.4V$					
R _{IN}	Receiver Input Resista	ince	$-7V \le V_{CM} \le +12V$		12			kΩ
I _{cc}	Supply Current		No Load	Driver Outputs Enabled			55	mA
			(Note 7)	Driver Outputs Disabled			35	mA
IOSD	Driver Short-Circuit		$V_{O} = -7V$ (Note 7)				-250	mA
	Output Current		$V_{0} = +12V$ (Note 7)				+250	mA

	trical Characteristics (Notes					
0 C ≤ 1 ₄ Symbol	∠≤ 70°C, 4.75V < V _{CC} < 5.25V unless oth Parameter	Conditions	Min	Тур	Max	Units
	Receiver Short-Circuit		-15	Typ	-85	mA
COR	Output Current					
	Absolute Maximum Ratings" are those beyond which ed at these limits. The tables of "Electrical Characteri	the safety of the device cannot be guaranteed. They are not meant t stics" provide conditions for actual device operation.	o imply	that the	device	should
Note 2: A	Il currents into device pins are positive; all currents ou	t of device pins are negative. All voltages are referenced to device grou	ind unle	ss other	wise spe	cified.

Note 3: All typicals are given for V_{CC} = 5V and T_A = 25°C.

Note 4: Derate linearly at 5.56 mW/°C to 650 mW at 70°C.

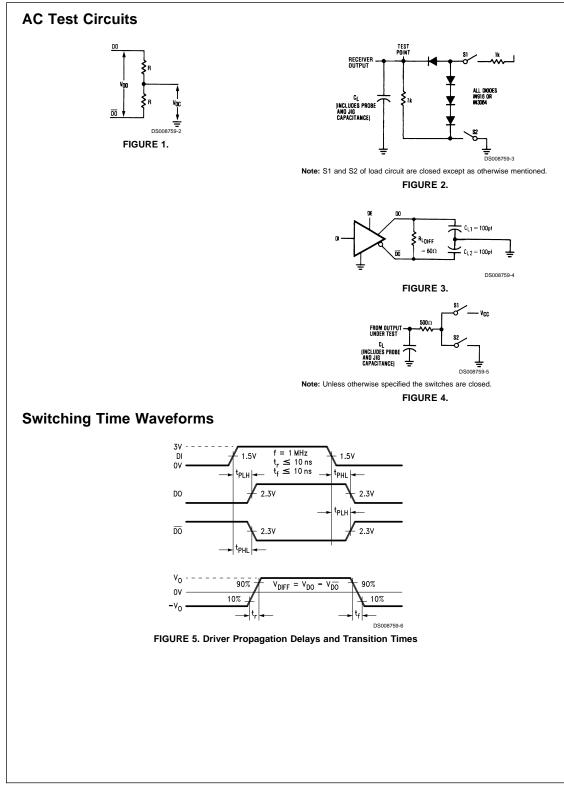
Note 5: Derate linearly 6.11 mW/°C to 400 mW at 70°C.

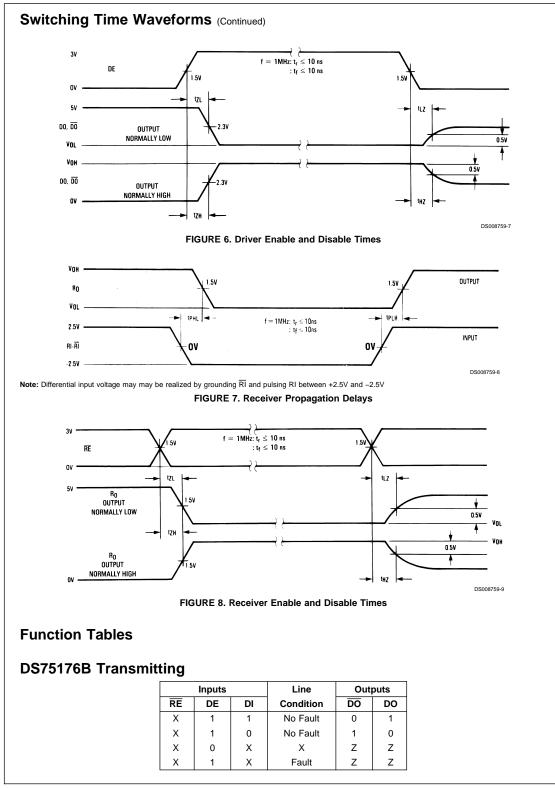
Note 6: Differential - Input/Output bus voltage is measured at the noninverting terminal A with respect to the inverting terminal B.

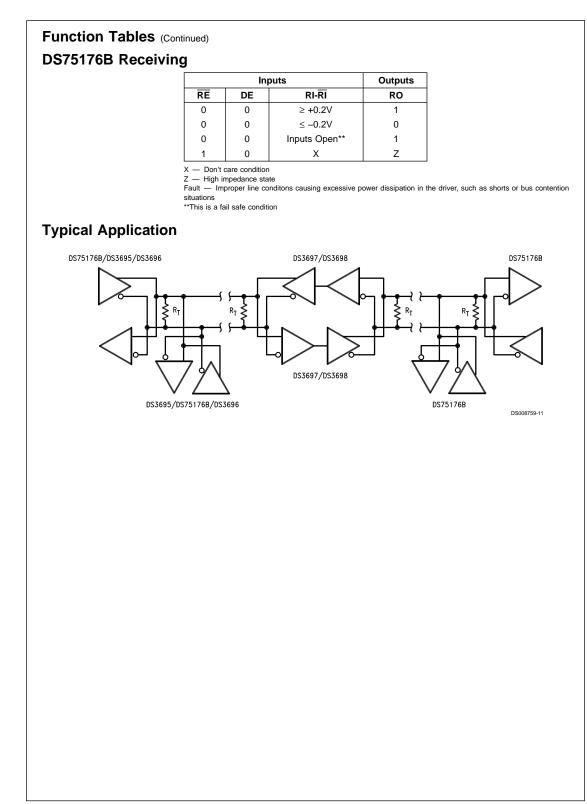
Note 7: All worst case parameters for which note 7 is applied, must be increased by 10% for DS75176BT. The other parameters remain valid for $-40^{\circ}C < T_A < +85^{\circ}C$.

Switching Characteristics V_{CC} = 5.0V, T_A = 25°C

Symbol	Parameter	Conditions	Min	Тур	Max	Units
t _{PLH}	Driver Input to Output	$R_{LDIFF} = 60\Omega$		12	22	ns
t _{PHL}	Driver Input to Output	$C_{L1} = C_{L2} = 100 \text{ pF}$		17	22	ns
t _r	Driver Rise Time	$R_{LDIFF} = 60\Omega$			18	ns
t _f	Driver Fall Time	$C_{L1} = C_{L2} = 100 \text{ pF}$			18	ns
		(Figure 3 and Figure 5)				
t _{zH}	Driver Enable to Output High	$C_L = 100 \text{ pF}$ (<i>Figure 4</i> and <i>Figure 6</i>) S1 Open		29	100	ns
t _{ZL}	Driver Enable to Output Low	$C_{L} = 100 \text{ pF}$ (<i>Figure 4</i> and <i>Figure 6</i>) S2 Open		31	60	ns
t _{LZ}	Driver Disable Time from Low	$C_{L} = 15 \text{ pF} (Figure 4 \text{ and } Figure 6) \text{ S2}$ Open		13	30	ns
t _{HZ}	Driver Disable Time from High	$C_{L} = 15 \text{ pF} (Figure 4 \text{ and } Figure 6) \text{ S1}$ Open		19	200	ns
t _{PLH}	Receiver Input to Output	$C_L = 15 \text{ pF} (Figure 2 \text{ and } Figure 7)$		30	37	ns
t _{PHL}	Receiver Input to Output	S1 and S2 Closed		32	37	ns
t _{ZL}	Receiver Enable to Output Low	$C_L = 15 \text{ pF}$ (<i>Figure 2</i> and <i>Figure 8</i>) S2 Open		15	20	ns
t _{zH}	Receiver Enable to Output High	$C_L = 15 \text{ pF}$ (<i>Figure 2</i> and <i>Figure 8</i>) S1 Open		11	20	ns
t_{LZ}	Receiver Disable from Low	$C_{L} = 15 \text{ pF} (Figure 2 \text{ and } Figure 8) \text{ S2}$ Open		28	32	ns
t _{HZ}	Receiver Disable from High	$C_L = 15 \text{ pF}$ (<i>Figure 2</i> and <i>Figure 8</i>) S1 Open		13	35	ns



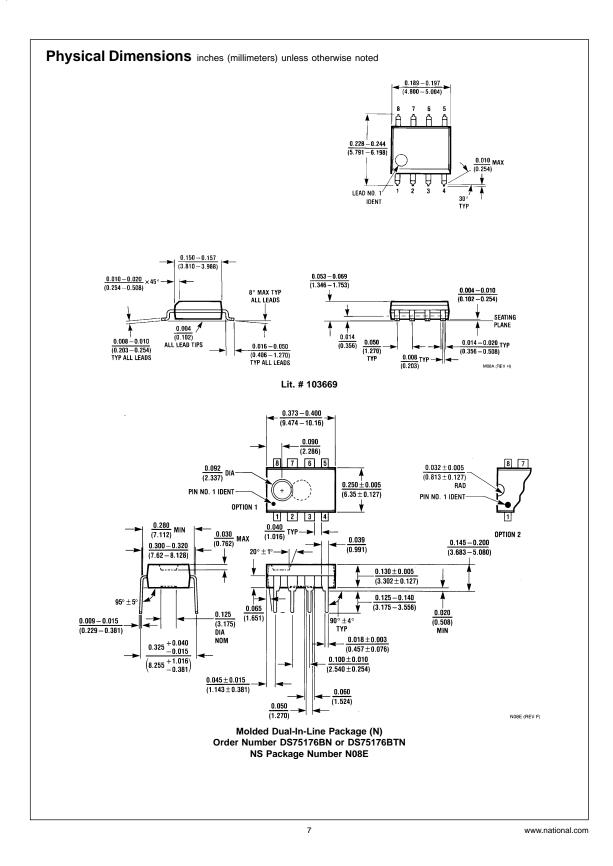




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