

# MOS INTEGRATED CIRCUITS

## Shift registers

TYPE	MODE(Static, Dynamic)	DESCRIPTION	TECHNOLOGY							POWER CONS. (mW)	TEMP. RANGE	PACKAGE		
			V <sub>CC</sub> (V)	V <sub>DD</sub> (V)	V <sub>GG</sub> (V)	f max (MHz)	V <sub>IH</sub> (V)	V <sub>OH</sub> (V)	I <sub>DD</sub> (mA)				I <sub>CC</sub> (mA)	
M 142	S	Quad 80 bit single phase	2	5	-	-	3	2	2.4	-	50	280	0-70	DIP K, M
M 142A	S	Quad 80 bit single phase	2	5	-	-	5	2	2.4	-	50	280	0-70	DIP K, M

## TV and radio circuits

TYPE	DESCRIPTION	TECHNOLOGY	V <sub>CC</sub> (V)	POWER CONS. (mW)	TEMP. RANGE (°C)	PACKAGE
M 055	1 of 16 decoder (open drain)	4	18	450	0-70	DIP U
M 106	TV and Radio microprocessor interface	4	5	-	0-70	DIP T
M 190	Keyboard encoder	4	13	240	0-70	DIP G
M 191	Tuning scale display	4	13	240	0-70	DIP K
M 192	Program display	CMOS	13	25	0-70	DIP K
M 193/A	Electronic Program Memory	6	13	700	0-70	DIP I
M 1024	30 channel TV remote control transmitter	CMOS	9	20	-25-70	DIP K
M 1025	30 channel TV remote control receiver	1	18	400	-25-70	DIP K
M 1124	30 channel TV remote control transmitter	CMOS	9	20	0-70	DIP K
M 1130	30 channel TV remote control receiver	1	18	450	0-70	DIP G
H 580	Quad analog switch	Bipolar	30	180	0-70	DIP G
H 590	Quad analog switch	Bipolar	30	180	0-70	DIP G
H 770	Analog switch binary input	Bipolar	15	110	0-70	DIP K
H 771	Analog switch binary input	Bipolar	15	110	0-70	DIP K
H 772	Analog switch binary input	Bipolar	15	110	0-70	DIP K
H 773	Analog switch binary input	Bipolar	15	110	0-70	DIP K