

SAW Components

Data Sheet B3550





Data Sheet

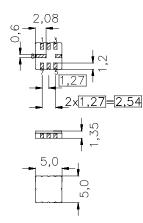
Features

- RF low-loss filter for remote control receivers
- Package for Surface Mounted Technology (SMT)
- Balanced and unbalanced operation possible

Terminals

■ Ni, gold plated

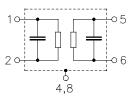
Ceramic package QCC8C



typ. dimensions in mm, approx. weight 0,1 g

Pin configuration

- 1 Input Ground
- 2 Input
- 5 Output
- 6 Output Ground
- 4,8 Case Ground
- 3,7 to be grounded



Туре	Ordering code	Marking and package according to	Packing according to		
B3550	B39431-B3550-U310	C61157-A7-A56	F61074-V8070-Z000		

Electrostactic Sensitive Device (ESD)

Maximum ratings

Operable temperature range	T_{A}	-45/+120	°C	
Storage temperature range	$T_{\rm stg}$	-45/+120	°C	
DC voltage	$V_{\rm DC}$	0	V	
Source power	P_S	10	dBm	source impedance 50 Ω



SAW Components B3550 433,92 MHz **Low-loss Filter**

Data Sheet

Characteristics

Reference temperature:

 $T_{\rm A} = 25~^{\circ}{\rm C}$ $Z_{\rm S} = 50~\Omega$ and matching network t. b. d. $Z_{\rm L} = 50~\Omega$ and matching network t. b. d. Terminating source impedance: Terminating load impedance:

		min.	typ.	max.	
Center frequency	f_C	_	433,96	_	MHz
(center frequency between 3 dB points)					
Minimum insertion attenuation					
433,80 434,12 MHz		_	2,0	3,5	dB
Pass band (relative to α_{min})					
433,76 434,16 MHz		_	1,0	2,0	dB
433,74 434,18 MHz		_	1,0	3,0	dB
433,68 434,24 MHz		_	1,5	6,0	dB
Relative attenuation (relative to α_{min})					
10,00 414,00 MHz		45	50	_	dB
414,00 428,00 MHz		40	45	_	dB
428,00 432,92 MHz		15	20	_	dB
434,92 442,00 MHz		10	15	_	dB
442,00 550,00 MHz		35	40	_	dB
550,001000,00 MHz		45	50	_	dB
Impedance for pass band matching ²⁾					
Input: $Z_{IN} = R_{IN} \parallel C_{IN}$		_	270 2,67	_	$\Omega \parallel pF$
Output: $Z_{OUT} = R_{OUT} \parallel C_{OUT}$		_	250 3,20	_	Ω pF
Temperature coefficient of frequency 1)	TC_{f}	_	-0,03	_	ppm/K ²
Frequency inversion point	T_{0}	10	_	30	°C

¹⁾Temperature dependence of f_C : $f_C(T_A) = f_C(T_0) (1 + TC_f(T_A - T_0)^2)$

The conjugate complex value of these characteristic impedances are the input and output impedances for flat passband. For more details, we refer to EPCOS application note #4.

²⁾ Impedance for passband matching bases on an ideal, perfect matching of the SAW filter to source- and to load impedance (here 50 Ohm). After the SAW filter is removed and input impedance into the input matching / output matching network is calculated.



SAW Components B3550 433,92 MHz **Low-loss Filter**

Data Sheet

Characteristics

Reference temperature:

 $T_{\rm A} = -45 \dots 95 ^{\circ} {\rm C}$ $Z_{\rm S} = 50 \Omega$ and matching network $Z_{\rm L} = 50 \Omega$ and matching network Terminating source impedance: Terminating load impedance:

		min.	typ.	max.	
Center frequency f ₀	С	_	433,92	_	MHz
(center frequency between 3 dB points)					
Minimum insertion attenuation					
433,80 434,12 MHz		_	2,0	4,0	dB
Pass band (relative to α_{min})					
433,76 434,08 MHz		_	1,0	2,0	dB
433,74 434,10 MHz		_	1,0	3,0	dB
433,68 434,16 MHz		_	1,5	6,0	dB
Pass bandwidth					
α _{rel} ≤ 3 dB		0,67	0,73	0,79	MHz
Relative attenuation (relative to α_{min})	λ _{rel}				
10,00 414,00 MHz		45	50	_	dB
414,00 428,00 MHz		40	45	_	dB
428,00 432,84 MHz		15	20	_	dB
434,92 442,00 MHz		10	15	_	dB
442,00 550,00 MHz		35	40	_	dB
550,001000,00 MHz		45	50	_	dB
Impedance for pass band matching ²⁾					
Input: $Z_{IN} = R_{IN} C_{IN}$		_	270 2,67	_	Ω pF
Output: $Z_{OUT} = R_{OUT} C_{OUT}$		_	250 3,20	_	Ω pF

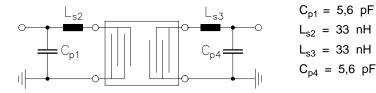
²⁾ Impedance for passband matching bases on an ideal, perfect matching of the SAW filter to source- and to load impedance (here 50 Ohm). After the SAW filter is removed and input impedance into the input matching / output matching network is calculated.

The conjugate complex value of these characteristic impedances are the input and output impedances for flat passband. For more details, we refer to EPCOS application note #4.



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Matching network to 50 Ω (element values depend on pcb layout and equivalent circuit)



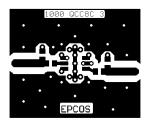
Minimising the crosstalk

For a good ultimate rejection a low crosstalk is necessary. Low crosstalk can be realised with a good RF layout. The major crosstalk mechanism is caused by the "ground-loop" problem.

Grounding loops are created if input-and output transducer GND are connected on the top-side of the PCB and fed to the system grounding plane by a common via hole. To avoid the common ground path, the ground pin of the input- and output transducer are fed to the system ground plane (bottom PCB plane) by their own via hole. The transducers' grounding pins should be isolated from the upper grounding plane.

A common GND inductivity of 0.5nH degrades the ultimate rejection (crosstalk) by 20dB.

The optimised PCB layout, including matching network for transformation to 50 Ohm, is shown here. In this PCB layout the grounding loops are minimised to realise good ultimate rejection.



Optimised PCB layout for SAW filters in QCC8C package, pinning 2,5 (top side, scale 1:1)

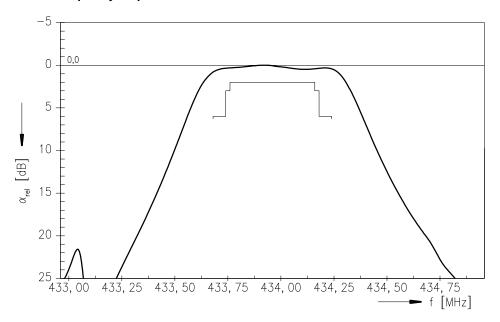
The bottom side is a copper plane (system ground area). The input and output grounding pins are isolated and connected to the common ground by separated via holes.

For good contact of the upper grounding area with the lower side it is necessary to place enough via holes.

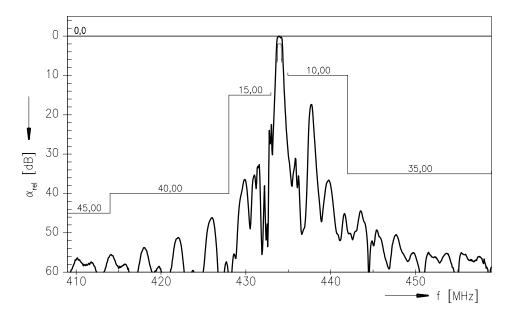


Data Sheet

Normalized frequency response



Normalized frequency response (wideband)





Data Sheet

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