

Low Power Quad Operational Amplifiers

LM324/LM2902

Description

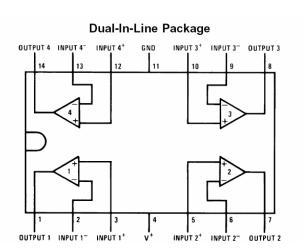
The LM324 series consists of four independent, high gain, internally frequency compensated operational amplifiers which were designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage.

Application areas include transducer amplifiers, DC gain blocks and all the conventional op amp circuits which now can be more easily implemented in single power supply systems. For example, the LM324 series can be directly operated off of the standard + 5V power supply voltage which is used in digital systems and will easily provide the required interface electronics without requiring the additional - 15V power supplies.

Features

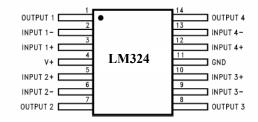
- Internally frequency compensated for unity gain
- Large DC voltage gain 100 dB
- Wide bandwidth (unity gain) 1 MHz (temperature compensated)
- Wide power supply range: Single supply 3V to 32V or dual supplies 1.5V to 16V
- Very low supply current drain (700 á A) essentially independent of supply voltage
- Low input biasing current 45 nA (temperature compensated)
- Low input offset voltage 2 mV and offset current: 5 nA
- Input common- mode voltage range includes ground
- Differential input voltage range equal to the power supply voltage
- Large output voltage swing 0V to V+ 1.5V

Pin Connection



Ordering Information

Devices	Package	Temp.
LM324M	SO-14	0 °C to 70 °C
LM324P	14-DIP	0 °C to 70 °C



Absolute Maximum Rating

Parameter	LM324	Unit	
Supply Voltage	32V	V	
Differential Input Voltage	32	V	
Input Voltage	-0.3 to 32	V	
Input Current	50	mA	
Storage Temperature	0 to 70	°C	
Lead Temperature (solder 10 Second)	260	°C	
ESD	250	V	

Electrical Characteristics

 $(V_{CC} = 5V; T_J = 25^{\circ}C$, unless otherwise specified)

PARAMETER	TEST CONDITIONS*		LM324			UNIT
			MIN	ТҮР	MAX	
V _{IO}	Vcc =5V to MAX,	25 °C		3	7	mV
Input offset voltage	$V_{IC} = V_{ICR}$ min, $V_{O}=1.4V$	Full range			9	
αVIO Average temperature coefficient of input offset voltage		Full range		7		µV/°C
lio	Vo=1.4V	25 ° C		2	50	nA
Input offset current		Full range			150	1
αllO Average temperature coefficient of input offset current		Full range		10		pA/°C
liB	Vo=1.4V	25 °C		-20	-250	nA
Input bias current		Full range			-500	
VICR	Vcc = 5V to MAX	25 °C	0 to Vcc-1.5			V
Common-mode input voltage range		Full range	0 to Vcc - 2	İ	İ	1
Vон	$RL \ge 2 k\Omega$	25 °C	Vcc-1.5			V
High-level output voltage	Vcc = MAX, $R_L = 2k\Omega$	Full range	26			1
	Vcc = MAX, RL \geq 10 k Ω	Full range	27	28		
V _{OL} Low-level output voltage	RL≥ 10 kΩ	Full range		5	20	mV
A _{VD}	Vcc = 15 V,	25 °C	25	100		V/mV
Large-signal differential voltage amplification	Vo=1V to 11 V, R _L \geq 2 k Ω	Full range	15			
CMRR Common-mode rejection ratio	$V_{IC} = 5V$ to MAX, $V_{IC} = V_{ICR}$ min	25 °C	65	80		dB
k _{SVR} Supply voltage rejection ratio (ΔVcc/ΔV _{IO})	Vcc = 5V to MAX	25 °C	65	100		dB
Vo1/Vo2 Crosstalk attenuation	f=1kHz to 20 kHz	25 °C		120		dB
lo	Vcc = 15 V,	25 °C	-20	-30		mA
Output current	$V_{ID}=1V, V_{O}=0$	Full range	-10]
	$V_{LD} = 15 V,$ $V_{LD} = -1V, V_{O} = 15V$	25 °C	10	20		
		Full range	5			
	V _{ID} = -1V, Vo - 200 mV	25 °C	12	30		μA
los Short-circuit output current	Vcc at 5 V, GND at -5V,Vo=0	25 °C		±40	±60	mA
lcc	Vo = 2.5 V, No load	Full range		0.7	1.2	mA
Supply current (four amplifiers)	Vcc = MAX, Vo = 0.5Vcc, No load	Full range		1.1	3]

* All characteristics are measured under open loop conditions with zero common-mode input voltage unless otherwise specified. "MAX" Vcc for testing purposes is 30 V. Full range is 0 °C to 70 °C

Advance Information- These data sheets contain descriptions of products that are in development. The specifications are based on the engineering calculations, computer simulations and/ or initial prototype evaluation.

Preliminary Information- These data sheets contain minimum and maximum specifications that are based on the initial device characterizations. These limits are subject to change upon the completion of the full characterization over the specified temperature and supply voltage ranges.

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