

GENERAL PURPOSE AMPLIFIER

RF2311

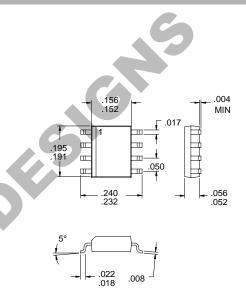
Typical Applications

- General Purpose High Bandwidth Gain Blocks
- IF or RF Buffer Amplifiers

- Broadband Test Equipment
- Final PA for Medium Power Applications
- Driver Stage for Power Amplifiers

Product Description

The RF2311 is a general purpose, low cost low power RF amplifier IC. The device is manufactured on an advanced Gallium Arsenide Heterojunction Bipolar Transistor (HBT) process, and has been designed for use as an easily cascadable 50Ω gain block. Applications include IF and RF amplification in wireless voice and data communication products operating in frequency bands up to 1600MHz. The gain flatness and high bandwidth make the device suitable for many other applications as well. The device is self-contained with 50Ω input and output impedances, and no external DC biasing elements are required to operate as specified.



Optimum Technology Matching® Applied Si BJT GaAs HBT GaAs MESFET Si Bi-CMOS SiGe HBT Si CMOS VCC 1 GND 2 GND 3 RF IN 4 Si GaAs HBT GaAs MESFET Si CMOS 8 RF OUT 7 GND 6 GND 5 GND

Functional Block Diagram

Package Style: SOP-8

Features

- DC to well over 1600MHz Operation
- Internally Matched Input and Output
- 14dB Small Signal Gain
- 4.2dB Noise Figure
- +9dBm Output Power
- Single 2.7V to 6V Positive Power Supply

 Ordering Information

 RF2311
 General Purpose Amplifier

 RF2311 PCBA
 Fully Assembled Evaluation Board

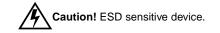
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 http://www.rfmd.com

Absolute Maximum Ratings

Parameter	Rating	Unit
Supply Voltage	-0.5 to +6	V _{DC}
Input RF Power	+10	dBm
Operating Ambient Temperature	-40 to +85	°C
Storage Temperature	-40 to +150	°C



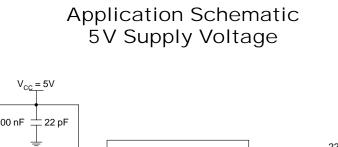
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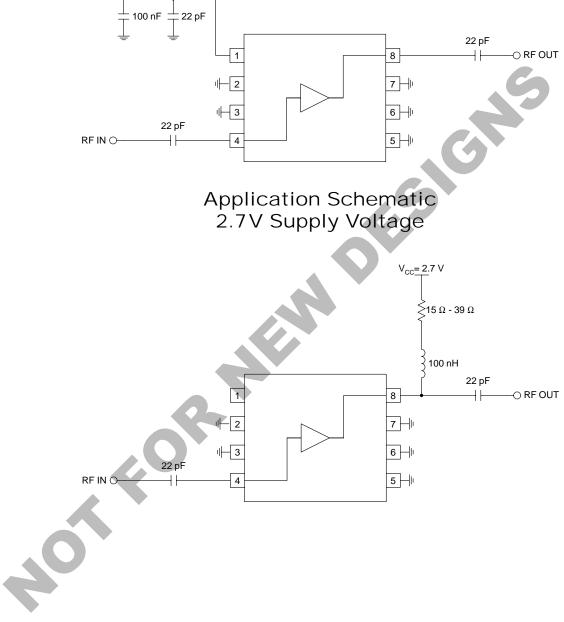
Parameter	Specification			Unit	Condition	
r ai airietei	Min.	Тур.	Max.	Onit	condition	
Overall					T=25 °C, V _{CC} =5V, Freq=900MHz	
Frequency Range		DC to 1600		MHz	3dB Bandwidth	
Gain	12	14	16	dB	V _{CC} =5V	
		13		dB	V _{CC} =3.6V	
Noise Figure		4.2		dB	V _{CC} =5V	
		4.0		dB	V _{CC} =3.6V	
Input VSWR		<1.5:1				
Output VSWR		<1.3:1			300MHz to 1200MHz	
Output IP ₃	+14	+16		dBm	900MHz, V _{CC} =5V	
		+8		dBm	900MHz, V _{CC} =3.6V	
Saturated Output Power	+8	+9		dBm	900MHz, V _{CC} =5∨	
		+1		dBm	900MHz, V _{CC} =3.6V	
Reverse Isolation		20		dB		
Power Supply						
Operating Voltage		2.7 to 6		V		
Operating Current Range	12	17	21	mA	V _{CC} =5V V _{CC} =3.6V	
		8	11	mA	V _{CC} =3.6V	

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1	Function	Description	Interface Schemati
	VCC	Power supply pin. An external bypass capacitor is recommended. The total supply current is shared between this pin and pin 8 (through the inductor).	See pin 8.
2	GND	Ground connection. Keep traces physically short and connect immedi- ately to ground plane for best performance.	
3	GND	Same as pin 2.	
4	RF IN	RF input pin. This pin is NOT internally DC blocked. A DC blocking capacitor, suitable for the frequency of operation, should be used in most applications. DC coupling of the input is not allowed, because this will override the internal feedback loop and cause temperature instability.	See pin 8.
5	GND	Same as pin 2.	
6	GND	Same as pin 2.	
7	GND	Same as pin 2.	
		itor, suitable for the frequency of operation, should be used in most applications. The DC voltage on this pin is typically 2.4V. Alternatively, power supply may be connected to this pin. A series resistor and inductor should be used, in which case the L should be large enough to present a high impedance at the lowest operating frequency, and the R should be (V_{CC} - 2.4) / I, where I is the desired device current (between 8mA and 20mA).	

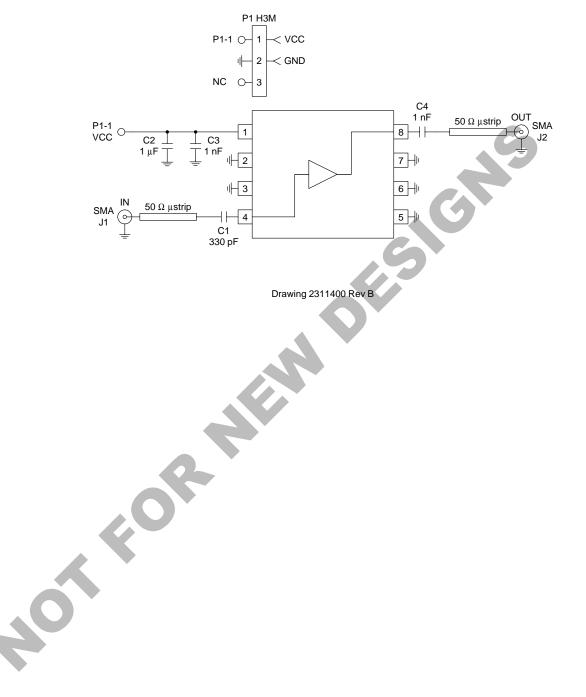
Rev C3 010228





Evaluation Board Schematic

(Download Bill of Materials from www.rfmd.com.)



Evaluation Board Layout 2" x 2"

