WESTCODE

Date:- 14 Jun, 2001

Data Sheet Issue:- 1

Distributed Gate Thyristor
Types R1271NS10x to R1271NS12x

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{DRM}	Repetitive peak off-state voltage, (note 1)	1000-1200	V
V_{DSM}	Non-repetitive peak off-state voltage, (note 1)	1000-1200	V
V_{RRM}	Repetitive peak reverse voltage, (note 1)	1000-1200	V
V_{RSM}	Non-repetitive peak reverse voltage, (note 1)	1100-1300	V

	OTHER RATINGS	MAXIMUM LIMITS	UNITS
$I_{T(AV)}$	Mean on-state current, T_{sink}=55°C, (note 2)	1271	A
$I_{T(AV)}$	Mean on-state current. T_{sink}=85°C, (note 2)	821	A
$I_{T(AV)}$	Mean on-state current. T_{sink}=85°C, (note 3)	458	A
$I_{T(RMS)}$	Nominal RMS on-state current, T_{sink}=25°C, (note 2)	2599	A
$I_{T(d.c.)}$	D.C. on-state current, T_{sink}=25°C, (note 4)	2050	A
I_{TSM}	Peak non-repetitive surge t_p=10ms, $V_{RM}=0.6V_{RRM}$, (note 5)	18.0	kA
I_{TSM2}	Peak non-repetitive surge t_p=10ms, $V_{RM}\leq 10V$, (note 5)	19.8	kA
I^2t	I^2t capacity for fusing t_p=10ms, $V_{RM}=0.6V_{RRM}$, (note 5)	1.62×10^6	A^2s
I^2t	I^2t capacity for fusing t_p=10ms, $V_{RM}\leq 10V$, (note 5)	1.96×10^6	A^2s
di_T/dt	Maximum rate of rise of on-state current (repetitive), (Note 6)	1000	A/µs
	Maximum rate of rise of on-state current (non-repetitive), (Note 6)	1500	A/µs
V_{RGM}	Peak reverse gate voltage	5	V
$P_{G(AV)}$	Mean forward gate power	2	W
P_{GM}	Peak forward gate power	30	W
V_{GD}	Non-trigger gate voltage, (Note 7)	0.25	V
T_{HS}	Operating temperature range	-40 to +125	°C
T_{stg}	Storage temperature range	-40 to +150	°C

Notes:-
1) De-rating factor of 0.13% per °C is applicable for T_j below 25°C.
2) Double side cooled, single phase; 50Hz, 180° half-sinewave.
3) Single side cooled, single phase; 50Hz, 180° half-sinewave.
4) Double side cooled.
5) Half-sinewave, 125°C T_j initial.
6) V_D=67% V_{DRM}, I_{FG}=2A, $t_r\leq 0.5\mu s$, T_{case}=125°C.
7) Rated V_{DRM}.

Characteristics

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS
V_{TM}	Maximum peak on-state voltage	-	-	2.02	I_{TM}=2000A	V
V_0	Threshold voltage	-	-	1.547		V
r_S	Slope resistance	-	-	0.237		mΩ
dv/dt	Critical rate of rise of off-state voltage	200	-	-	V_D=80% V_{DRM}, Linear ramp	V/µs
I_{DRM}	Peak off-state current	-	-	150	Rated V_{DRM}	mA
I_{RRM}	Peak reverse current	-	-	150	Rated V_{RRM}	
V_{GT}	Gate trigger voltage	-	-	3.0	T_j=25°C	V
I_{GT}	Gate trigger current	-	-	300	T_j=25°C V_D=10V, I_T=2A	mA
I_H	Holding current	-	-	1000	T_j=25°C	mA
t_{gd}	Gate controlled turn-on delay time	-	0.5	1.0	V_D=67%V_{DRM}, I_{TM}=2000A, di/dt=60A/µs, I_{FG}=2A, t_r≤0.5µs, T_{case}=25°C	µs
t_{gt}	Turn-on time	-	1.0	2.0		
Q_{rr}	Recovered charge	-	200	-	I_{TM}=1000A, t_p=1000µs, di/dt=60A/µs, V_r=50V	µC
Q_{ra}	Recovered charge, 50% Chord	-	120	150		µC
I_{rm}	Reverse recovery current	-	85	-		A
t_{rr}	Reverse recovery time, 50% chord	-	2.0	-		µs
t_q	Turn-off time (note 2)	-	-	22	I_{TM}=1000A, t_p=1000µs, di/dt=60A/µs, V_r=50V, V_{dr}=80%V_{DRM}, dV_{dr}/dt=20V/µs	µs
		20	-	25	I_{TM}=1000A, t_p=1000µs, di/dt=60A/µs, V_r=50V, V_{dr}=80%V_{DRM}, dV_{dr}/dt=200V/µs	
$R_{th(j-hs)}$	Thermal resistance, junction to heatsink	-	-	0.024	Double side cooled	K/W
		-	-	0.048	Single side cooled	
F	Mounting force	19	-	26		kN
W_t	Weight	-	510	-		g

Notes:-
1) Unless otherwise indicated T_j=125°C.
2) The required t_q (specified with dV_{dr}/dt=200V/µs) is represented by 'x' in the device part number. See ordering information for details of t_q codes.

Introduction

The R1271 series of Distributed Gate thyristors have fast switching characteristics provided by a regenerative, interdigitated gate. They also exhibit low switching losses and are therefore suitable for medium current, medium frequency applications.

Notes on Ratings and Characteristics

1.0 Voltage Grade Table

Voltage Grade	V_{DRM} V_{DSM} V_{RRM} V	V_{RSM} V	V_D V_R DC V
10	1000	1100	700
12	1200	1300	810

2.0 Extension of Voltage Grades

This report is applicable to other and higher voltage grades when supply has been agreed by Sales/Production.

3.0 Extension of Turn-off Time

This Report is applicable to other t_q/re-applied dv/dt combinations when supply has been agreed by Sales/Production.

4.0 Repetitive dv/dt

Higher dv/dt selections are available up to 1000V/µs on request.

5.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for T_j below 25°C.

6.0 Rate of rise of on-state current

The maximum un-primed rate of rise of on-state current must not exceed 1500A/µs at any time during turn-on on a non-repetitive basis. For repetitive performance, the on-state rate of rise of current must not exceed 1000A/µs at any time during turn-on. Note that these values of rate of rise of current apply to the total device current including that from any local snubber network.

7.0 Square wave ratings

These ratings are given for load component rate of rise of forward current of 100 and 500A/µs.

8.0 Duty cycle lines

The 100% duty cycle is represented on all the ratings by a straight line. Other duties can be included as parallel to the first.

9.0 Maximum Operating Frequency

The maximum operating frequency is set by the on-state duty, the time required for the thyristor to turn off (t_q) and for the off-state voltage to reach full value (t_v), i.e.

$$f_{max} = \frac{1}{t_{pulse} + t_q + t_v}$$

10.0 On-State Energy per Pulse Characteristics

These curves enable rapid estimation of device dissipation to be obtained for conditions not covered by the frequency ratings.

Let E_p be the Energy per pulse for a given current and pulse width, in joules
Let $R_{th(J-Hs)}$ be the steady-state d.c. thermal resistance (junction to sink)
and T_{SINK} be the heat sink temperature.

Then the average dissipation will be:

$$W_{AV} = E_P \cdot f \text{ and } T_{SINK(\max.)} = 125 - \left(W_{AV} \cdot R_{th(J-Hs)}\right)$$

11.0 Reverse recovery ratings

(i) Q_{ra} is based on 50% I_{rm} chord as shown in Fig. 1 below.

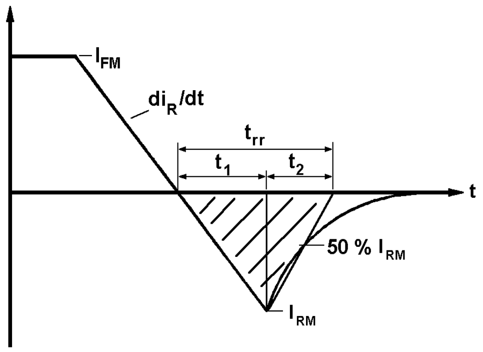


Fig. 1

(ii) Q_{rr} is based on a 150µs integration time.

i.e.

$$Q_{rr} = \int_0^{150\mu s} i_{rr}.dt$$

(iii)

$$K\ Factor = \frac{t1}{t2}$$

12.0 Reverse Recovery Loss

12.1 Determination by Measurement

From waveforms of recovery current obtained from a high frequency shunt (see Note 1, Page 5) and reverse voltage present during recovery, an instantaneous reverse recovery loss waveform must be constructed. Let the area under this waveform be E joules per pulse. A new heat sink temperature can then be evaluated from the following:

$$T_{SINK(new)} = T_{SINK(original)} - E \cdot \left(k + f \cdot R_{th(J-Hs)}\right)$$

where k = 0.227 (°C/W)/s

E = Area under reverse loss waveform per pulse in joules (W.s)
f = Rated frequency (in Hz) at the original heat sink temperature
$R_{th(J-Hs)}$ = D.C. thermal resistance (°C/W)

The total dissipation is now given by:

$$W_{(TOT)} = W_{(original)} + E \cdot f$$

12.2 Determination without Measurement

In circumstances where it is not possible to measure voltage and current conditions, or for design purposes, the additional losses E in joules may be estimated as follows.

Let E be the value of energy per reverse cycle in joules (curves in Figure 9).
Let f be the operating frequency in Hz

$$T_{SINK(new)} = T_{SINK(original)} - \left(E \cdot R_{th} \cdot f\right)$$

Where $T_{SINK\ (new)}$ is the required maximum heat sink temperature and $T_{SINK\ (original)}$ is the heat sink temperature given with the frequency ratings.

A suitable R-C snubber network is connected across the thyristor to restrict the transient reverse voltage to a peak value (V_{rm}) of 67% of the maximum grade. If a different grade is being used or V_{rm} is other than 67% of Grade, the reverse loss may be approximated by a pro rata adjustment of the maximum value obtained from the curves.

NOTE 1- Reverse Recovery Loss by Measurement

This thyristor has a low reverse recovered charge and peak reverse recovery current. When measuring the charge care must be taken to ensure that:

(a) a.c. coupled devices such as current transformers are not affected by prior passage of high amplitude forward current.
(b) A suitable, polarised, clipping circuit must be connected to the input of the measuring oscilloscope to avoid overloading the internal amplifiers by the relatively high amplitude forward current signal
(c) Measurement of reverse recovery waveform should be carried out with an appropriate critically damped snubber, connected across diode anode to cathode. The formula used for the calculation of this snubber is shown below:

$$R^2 = 4 \cdot \frac{V_r}{C_S \cdot {}^{di}\!/_{dt}}$$

Where: V_r = Commutating source voltage
C_S = Snubber capacitance
R = Snubber resistance

13.0 Gate Drive

The recommended pulse gate drive is 30V, 15Ω with a short-circuit current rise time of not more than 0.5µs. This gate drive must be applied when using the full di/dt capability of the device.

The duration of pulse may need to be configured with respect to the application but should be no shorter than 20µs, otherwise an increase in pulse current could be needed to supply the necessary charge to trigger the device.

14.0 Computer Modelling Parameters

14.1 Calculating V_T using ABCD Coefficients

The on-state characteristic I_T vs. V_T, on page 7 is represented in two ways;

(i) the well established V_0 and r_s tangent used for rating purposes and

(ii) a set of constants A, B, C, D, forming the coefficients of the representative equation for V_T in terms of I_T given below:

$$V_T = A + B \cdot \ln(I_T) + C \cdot I_T + D \cdot \sqrt{I_T}$$

The constants, derived by curve fitting software, are given in this report for hot characteristics where possible. The resulting values for V_T agree with the true device characteristic over a current range, which is limited to that plotted.

125°C Coefficients	
A	1.520747638
B	8.42314×10^{-3}
C	2.47082×10^{-4}
D	-1.30505×10^{-3}

14.2 D.C. Thermal Impedance Calculation

$$r_t = \sum_{p=1}^{p=n} r_p \cdot \left(1 - e^{\frac{-t}{\tau_p}}\right)$$

Where *p* = *1* to *n, n* is the number of terms in the series.

- t = Duration of heating pulse in seconds.
- r_t = Thermal resistance at time t.
- r_p = Amplitude of p_{th} term.
- τ_p = Time Constant of r_{th} term.

D.C. Double Side Cooled					
Term	1	2	3	4	5
r_p	0.01249139	6.316833×10^{-3}	1.850855×10^{-3}	1.922045×10^{-3}	6.135330×10^{-4}
τ_p	0.8840810	0.1215195	0.03400152	6.742908×10^{-3}	1.326292×10^{-3}

D.C. Single Side Cooled						
Term	1	2	3	4	5	6
r_p	0.02919832	4.863568×10^{-3}	3.744798×10^{-3}	6.818034×10^{-3}	2.183558×10^{-3}	1.848294×10^{-3}
τ_p	6.298105	3.286174	0.5359179	0.1186897	0.02404574	3.379476×10^{-3}

Curves

Figure 1 - On-state characteristics of Limit device

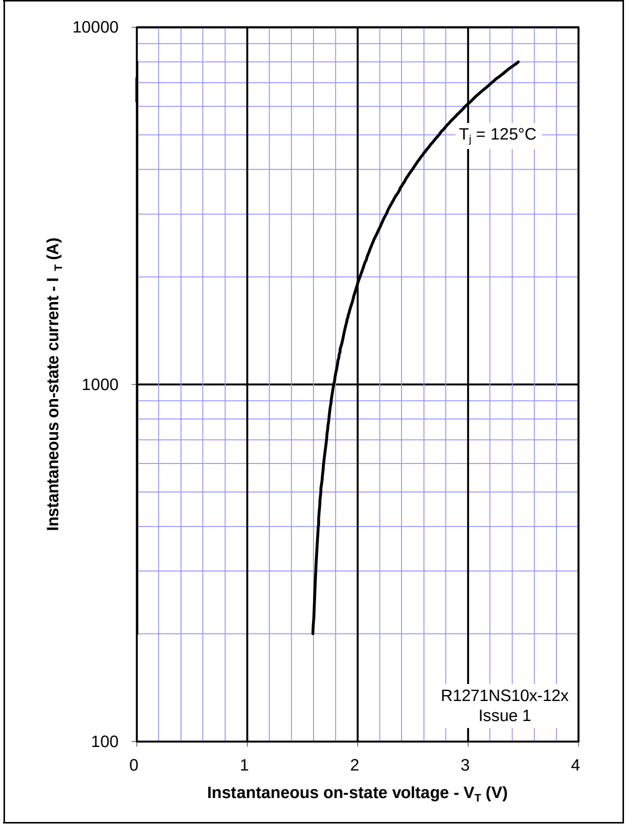


Figure 2 - Transient thermal impedance

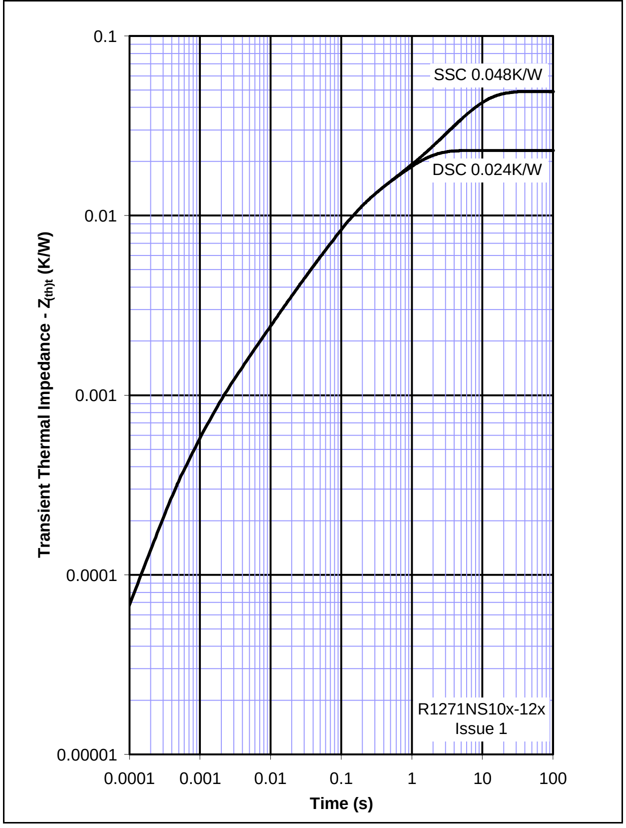


Figure 3 - Gate characteristics - Trigger limits

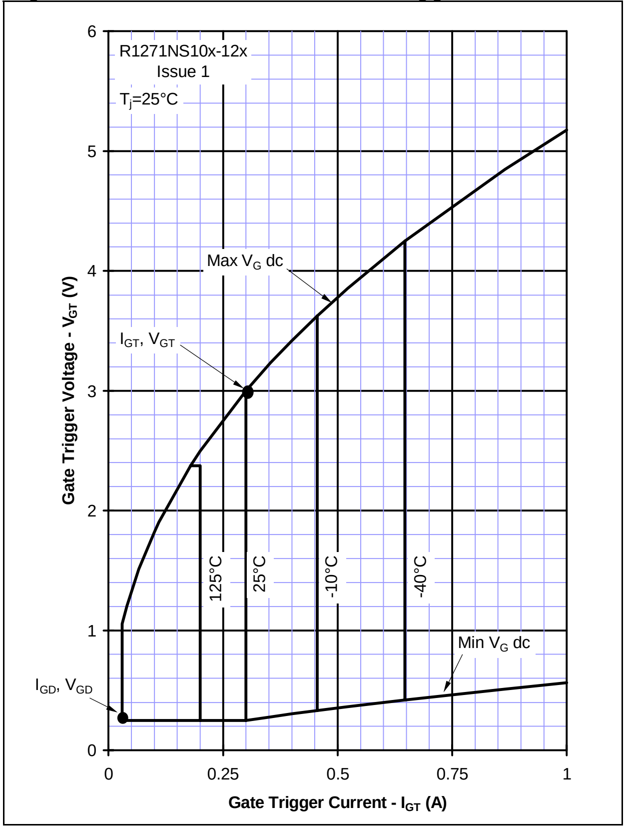


Figure 4 - Gate characteristics - Power curves

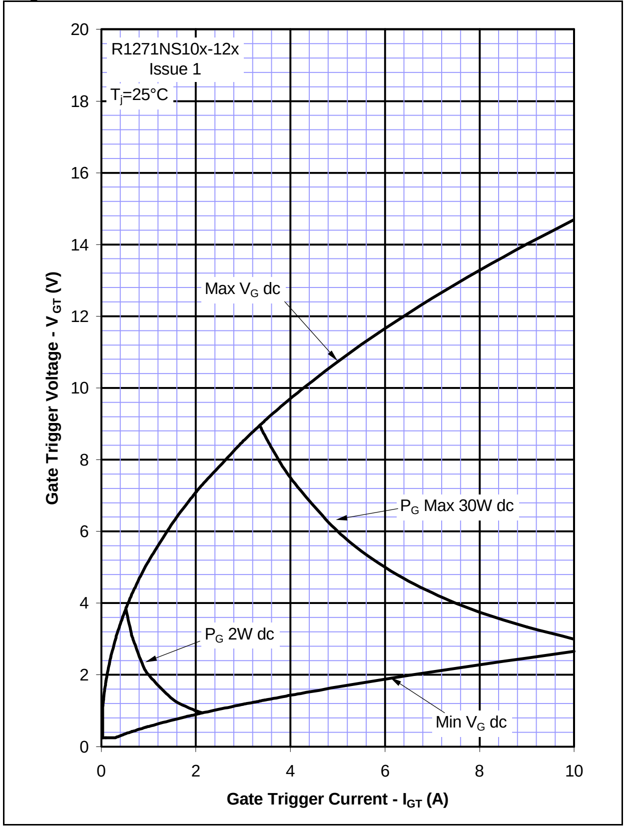


Figure 5 - Total recovered charge, Q_{rr}

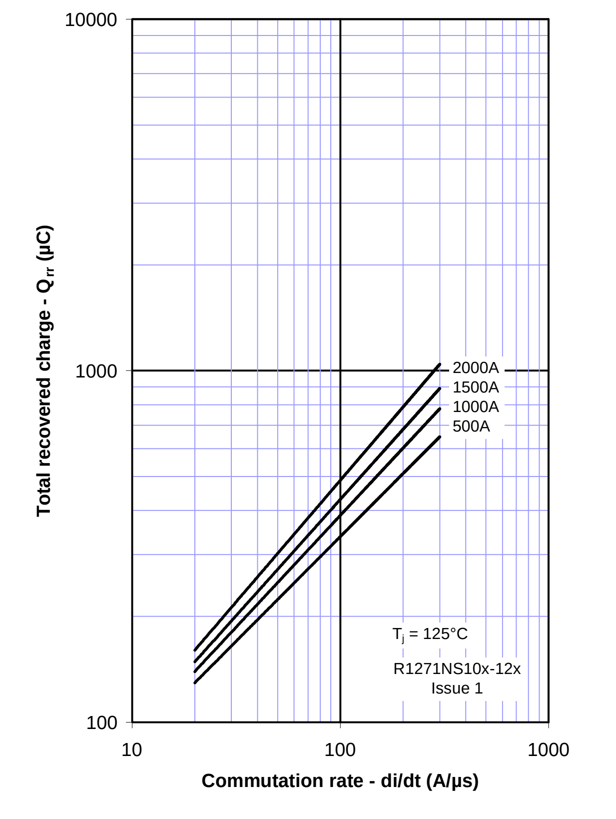


Figure 6 - Recovered charge, Q_{ra} (50% chord)

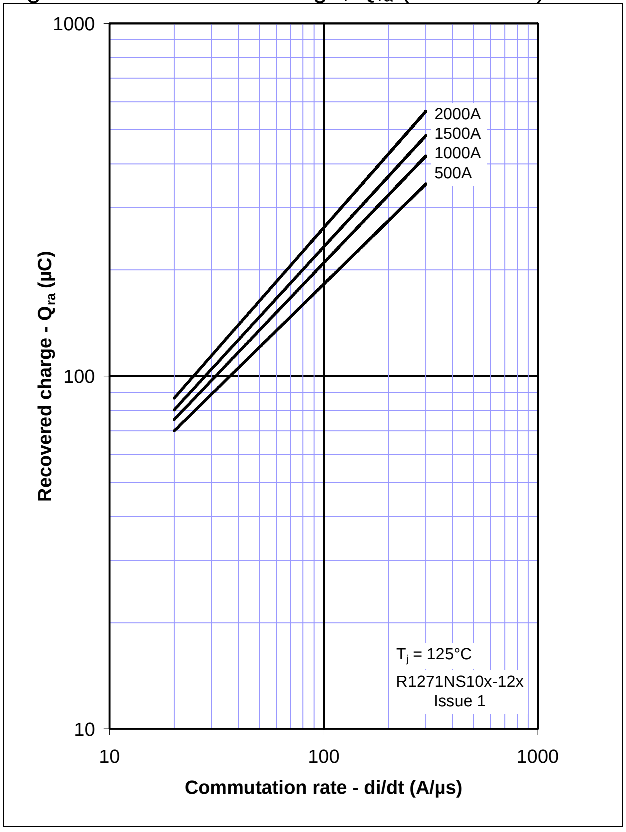


Figure 7 - Peak reverse recovery current, I_{rm}

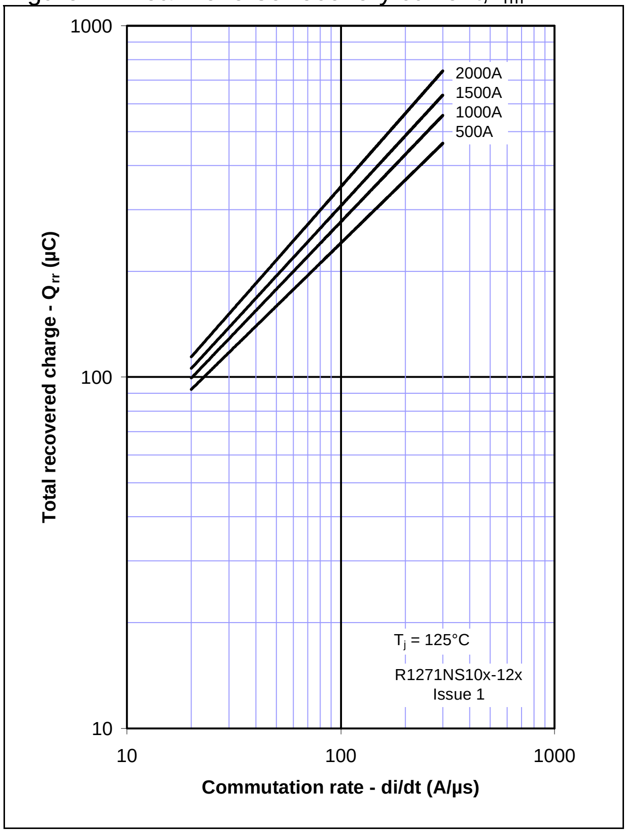


Figure 8 - Maximum recovery time, t_{rr} (50% chord)

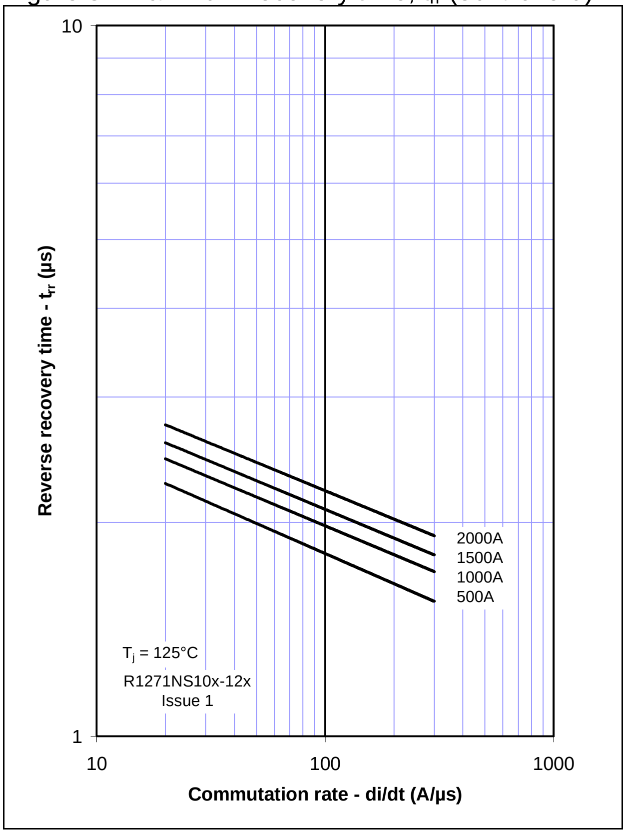


Figure 9 - Reverse recovery energy per pulse

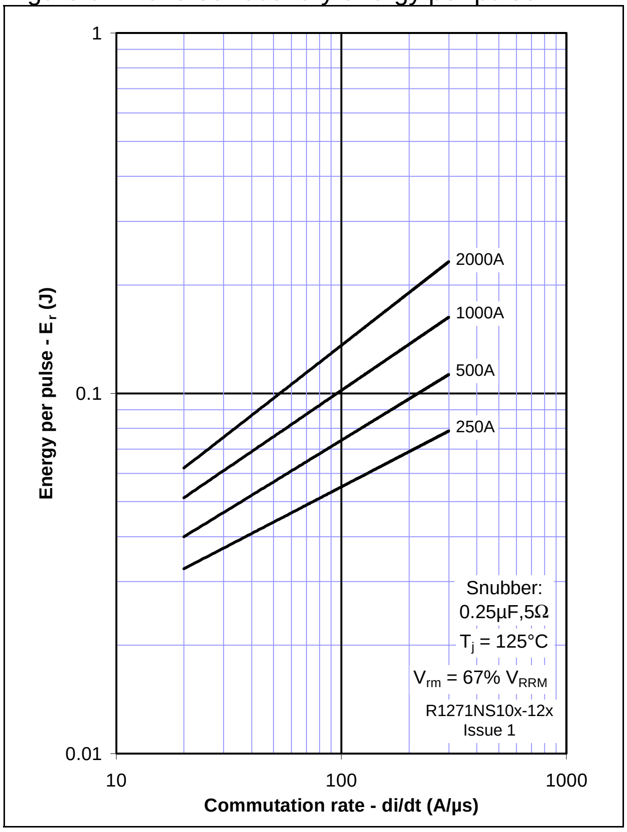


Figure 10 - Sine wave energy per pulse

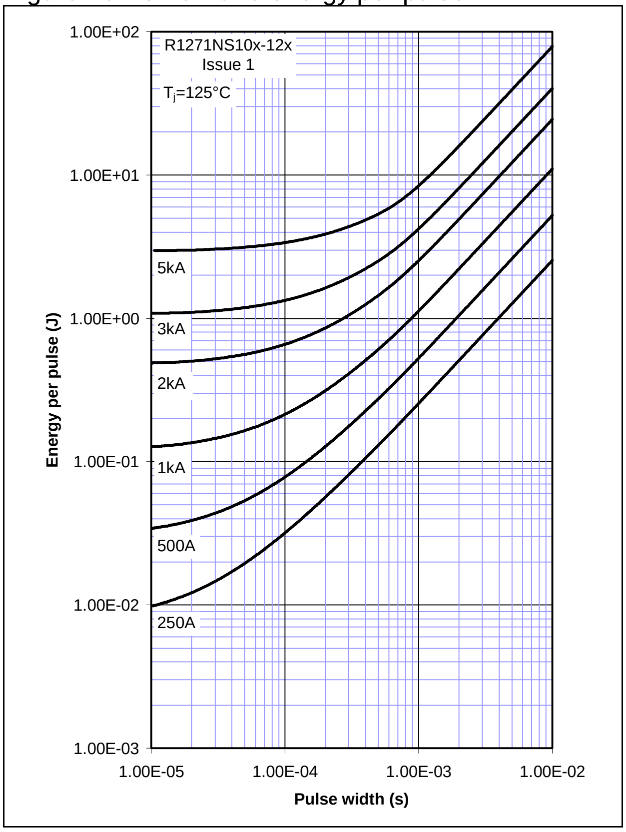


Figure 11 - Sine wave frequency ratings

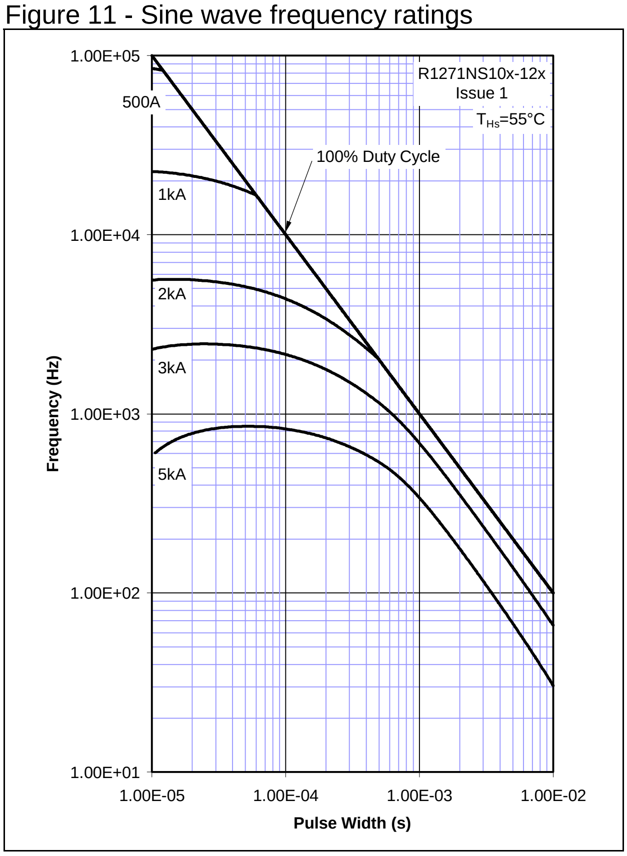


Figure 12 - Sine wave frequency ratings

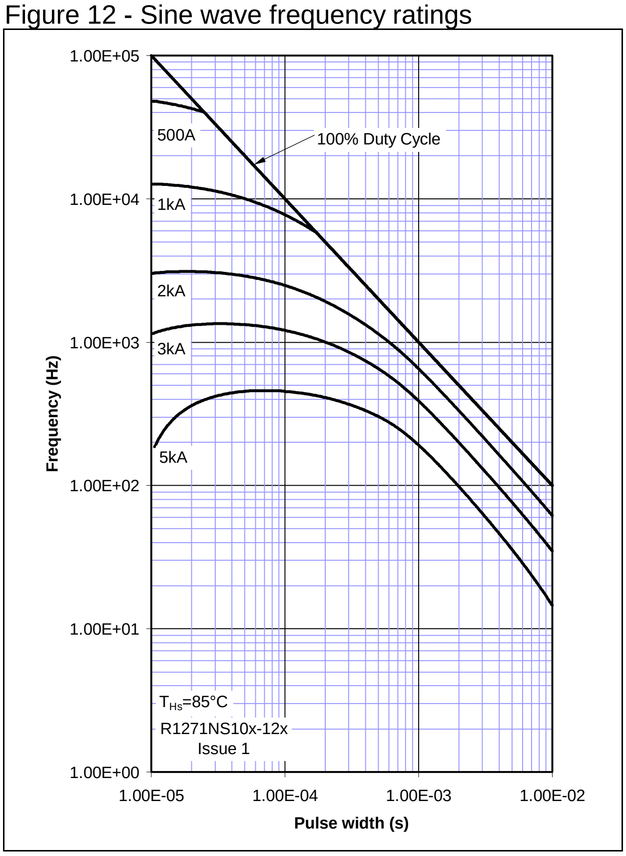


Figure 13 - Square wave frequency ratings

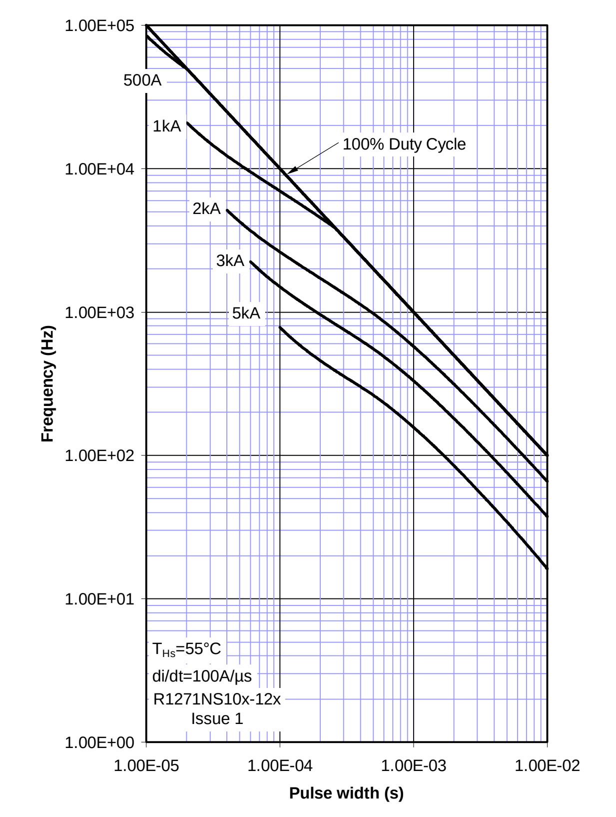


Figure 14 - Square wave frequency ratings

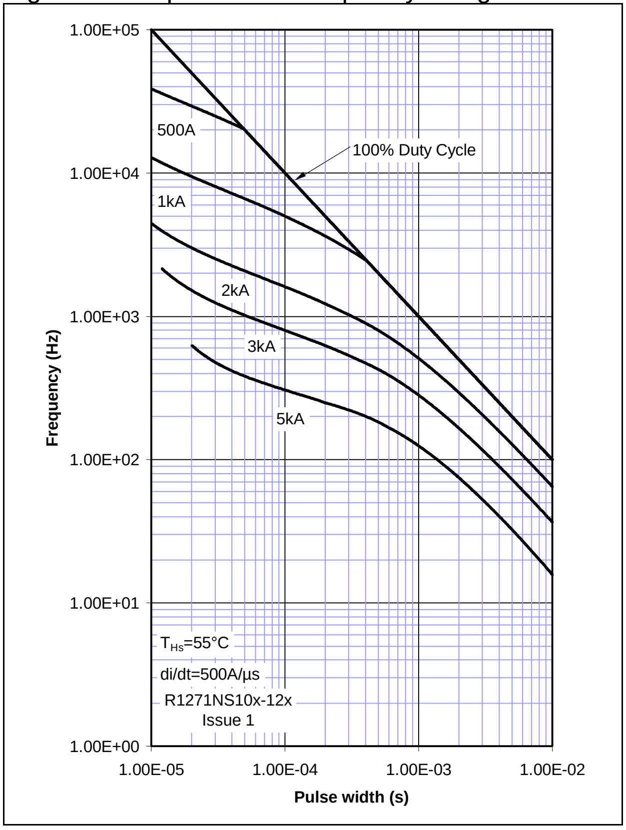


Figure 15 - Square wave frequency ratings

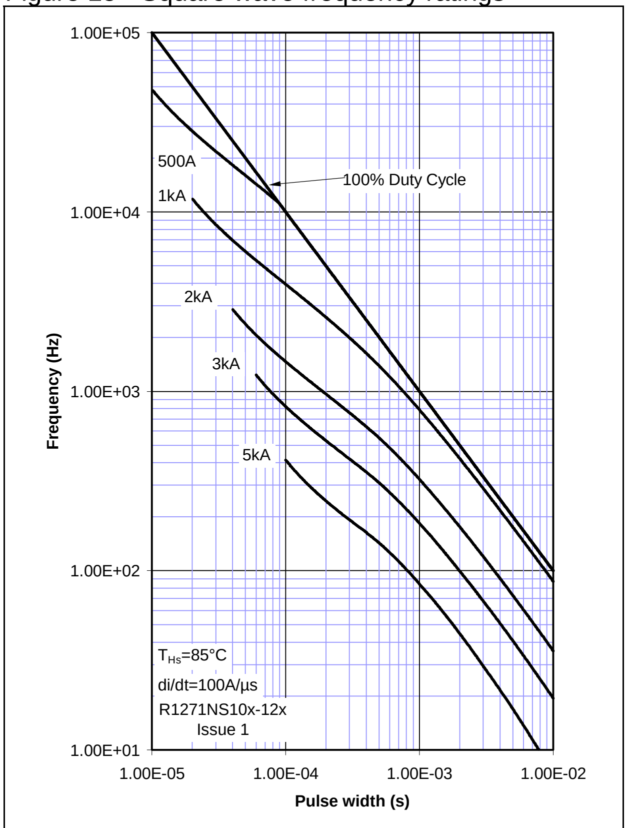


Figure 16 - Square wave frequency ratings

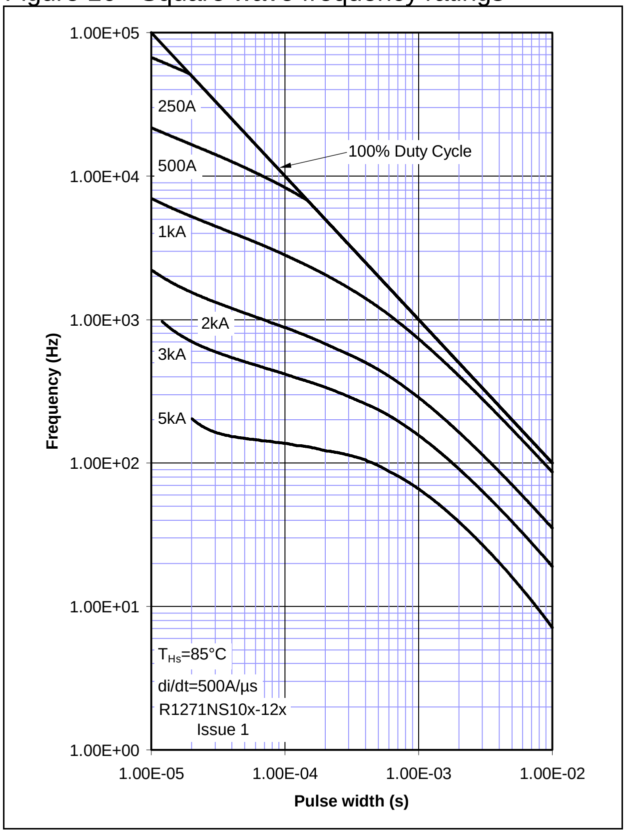


Figure 17 - Square wave energy per pulse

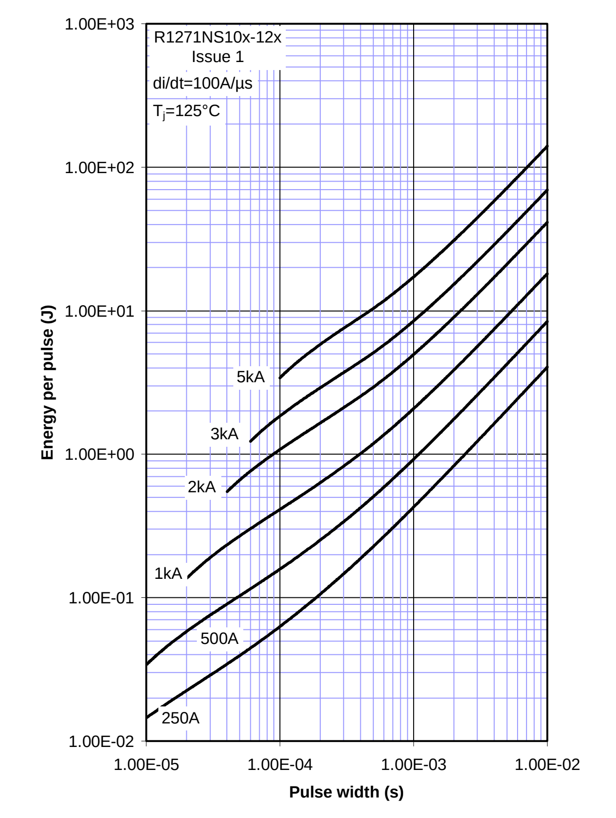


Figure 18 - Square wave energy per pulse

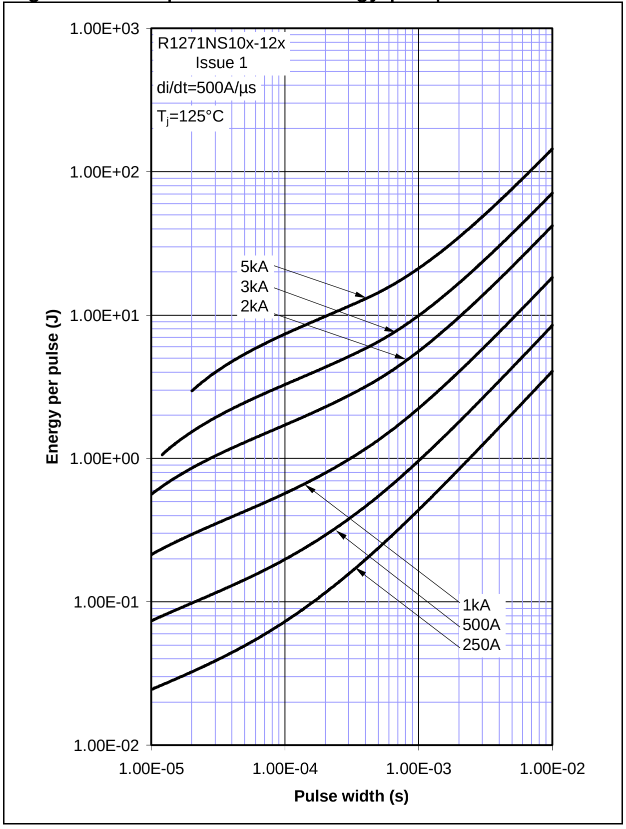
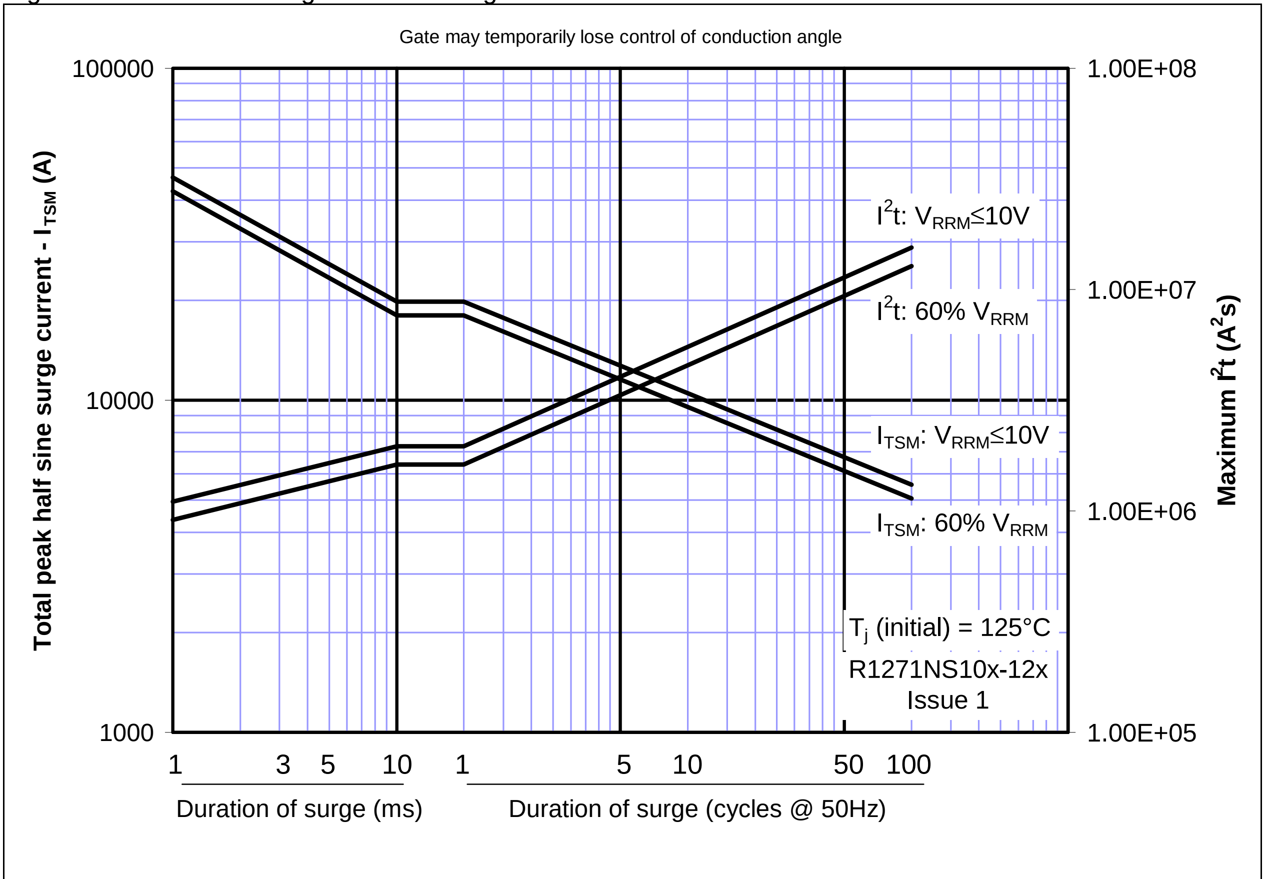
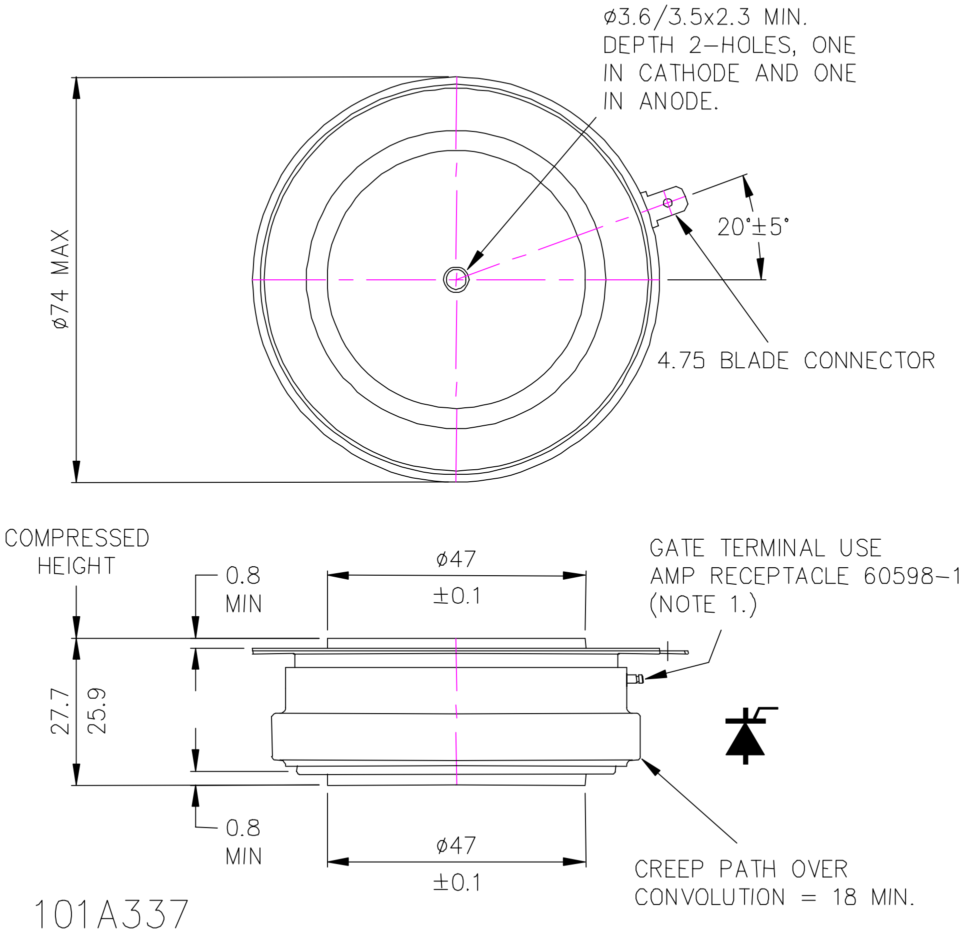


Figure 19 - Maximum surge and I^2t Ratings



Outline Drawing & Ordering Information



ORDERING INFORMATION (Please quote 10 digit code as below)

R1271	NS	♦ ♦	♦
Fixed Type Code	Fixed Outline Code	Off-state Voltage Code V_{DRM}/100 10-12	t_q Code (@ 200V/µs) D=20µs, E=25µs

Typical order code: R1271NS12D – 1200V V_{DRM}, 20µs t_q, 27.7mm clamp height capsule.

WESTCODE

UK: Westcode Semiconductors Ltd.
P.O. Box 57, Chippenham, Wiltshire, England. SN15 1JL.
Tel: +44 (0) 1249 444524 Fax: +44 (0) 1249 659448
E-Mail: WSL.sales@westcode.com

USA: Westcode Semiconductors Inc.
3270 Cherry Avenue, Long Beach, California 90807
Tel: 562 595 6971 Fax: 562 595 8182
E-Mail: WSI.sales@westcode.com

Internet: http://www.westcode.com

Devices with a suffix code (2-letter or letter/digit/letter combination) added to their generic code are not necessarily subject to the conditions and limits contained in this report.