TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOS Type (U-MOS

TPCP8002

Notebook PC Applications Portable Equipment Applications

- Lead (Pb)-Free
- Small footprint due to small and thin package
- Low drain-source ON-resistance
 - $: R_{DS}(ON) = 7 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance
 - $|Y_{fs}| = 36 \text{ S (typ.)}$
- Low leakage current
 - $I_{DSS} = 10 \, \mu A \, (V_{DS} = 20 \, V)$
- Enhancement mode
 - : $V_{th} = 0.5 \text{ to } 1.2 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 0.2 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

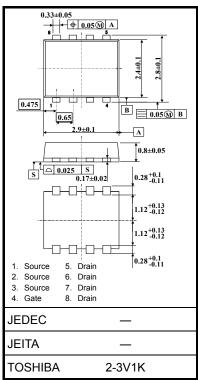
Characteristic			Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	20	V		
Drain-gate vol	tage (R _G	s = 20 kΩ)	V_{DGR}	20	V	
Gate-source v	oltage		V _{GSS}	±12	V	
Drain current	DC	(Note 1)	ID	9.1	Α	
Diain current	Pulse	(Note 1)	I _{DP}	36.4	_ ^	
Drain power dissipation (t = 5 s) (Note 2a)			P _D	1.68	W	
Drain power dissipation (t = 5 s) (Note 2b)			P _D	0.84	VV	
Single pulse avalanche energy (Note 3)		E _{AS}	21.5	mJ		
Avalanche current		I _{AR}	9.1	Α		
Repetitive avalanche energy (Note 4)		E _{AR}	0.168	mJ		
Channel temperature			T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C		

Note: For Notes 1 to 5, refer to the next page.

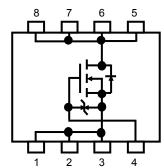
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.

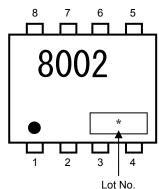
Unit: mm



Circuit Configuration



Marking (Note 5)



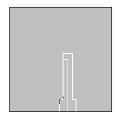
Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 5 s) (Note 2a)	R _{th (ch-a)}	74.4	°C/W
Thermal resistance, channel to ambient (t = 5 s) (Note 2b)	R _{th (ch-a)}	148.8	°C/W

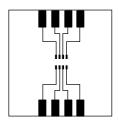
Note 1: The channel temperature should not exceed 150°C during use.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)



 $\begin{aligned} & \text{FR-4} \\ 25.4 \times 25.4 \times 0.8t \\ & \text{Unit: (mm)} \end{aligned}$



FR-4 $25.4 \times 25.4 \times 0.8t$ Unit: (mm)

Note 3: V_{DD} = 16 V, T_{ch} = 25°C (initial), L = 0.2 mH, R_G = 25 Ω , I_{AR} = 9.1 A

Note 4: Repetitive rating: pulse width limited by maximum channel temperature.

Note 5: • on the lower left of the marking indicates Pin 1.

* Weekly code (3 digits):



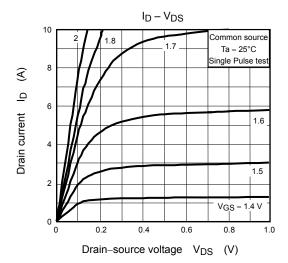
Electrical Characteristics (Ta = 25°C)

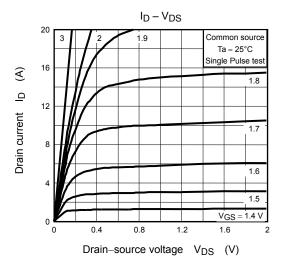
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	V _{GS} = ±10 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-off current		I _{DSS}	V _{DS} = 20 V, V _{GS} = 0 V	_	_	10	μΑ
Drain-source breakdown voltage		V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	20	_	_	V
		V (BR) DSX	I _D = 10 mA, V _{GS} = -12 V	8	_	_	V
Gate threshold vo	oltage	V _{th}	V _{DS} = 10 V, I _D = 0.2 mA	0.5	_	1.2	V
Drain sauras ON			V _{GS} = 2.5 V, I _D = 4.5 A	_	10	13.7	- mΩ
Drain-source ON-resistance		R _{DS} (ON)	V _{GS} = 4.5 V, I _D = 4.5 A	_	7	10	
Forward transfer admittance		Y _{fs}	V _{DS} = 10 V, I _D = 4.5 A	18	36	_	S
Input capacitance		C _{iss}			3700	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		400	_	
Output capacitance		C _{oss}			450	_	
Switching time	Rise time	t _r	$V_{GS} \stackrel{5 \text{ V}}{\underset{0 \text{ V}}{\text{V}}} \stackrel{I_D = 4.5 \text{ A}}{\underset{0 \text{ V}}{\text{C}}} \stackrel{O}{\underset{0 \text{ V}}{\text{C}}} \stackrel{O}{\underset{0 \text{ V}}{\text{V}}} \stackrel{O}{\underset{0 \text{ V}}{\text{U}}} \stackrel{O}{\underset{0 \text{ V}}{\text{V}}} \stackrel{O}{\underset{0 \text{ V}}} \stackrel{O}{0 $	_	14	_	- ns
	Turn-on time	t _{on}		_	24	_	
	Fall time	t _f		_	30	_	
	Turn-off time	t _{off}		_	110	_	
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \approx 16 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 9.1 \text{ A}$		48		_
Gate-source charge 1		Q _{gs1}		_	8		nC
Gate-drain ("Miller") charge		Q _{gd}		_	12	_	

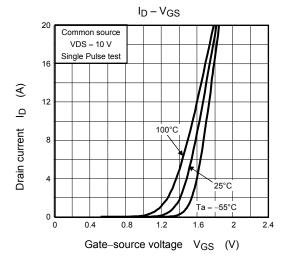
Source-Drain Ratings and Characteristics (Ta = 25°C)

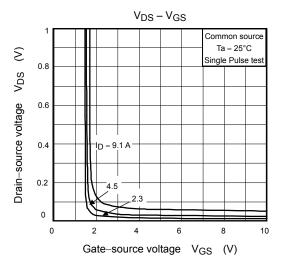
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse (Note 1)	I _{DRP}	_	_	_	36.4	Α
Forward voltage (diode)		V_{DSF}	I _{DR} = 9.1 A, V _{GS} = 0 V	_	_	-1.2	V

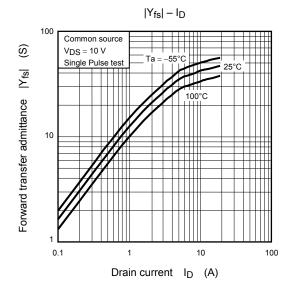
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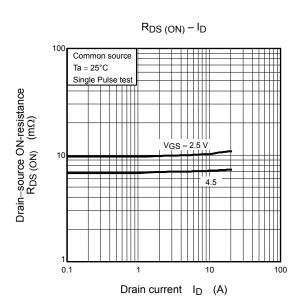


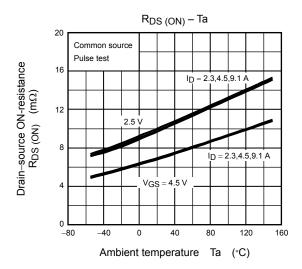


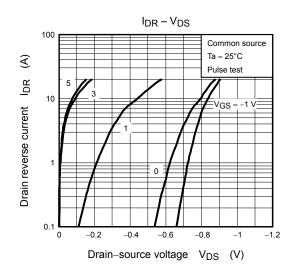


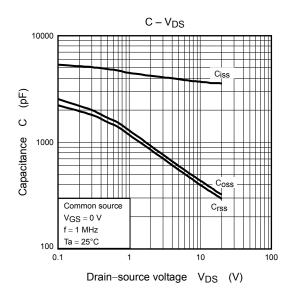


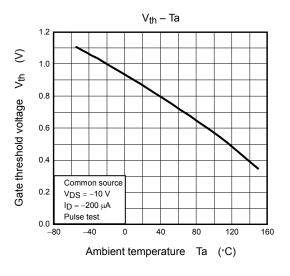


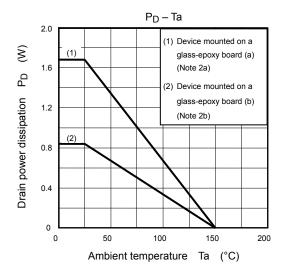


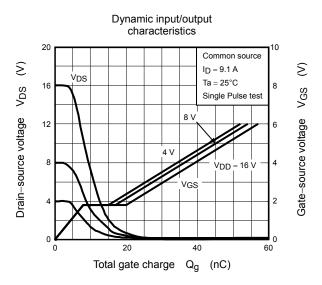


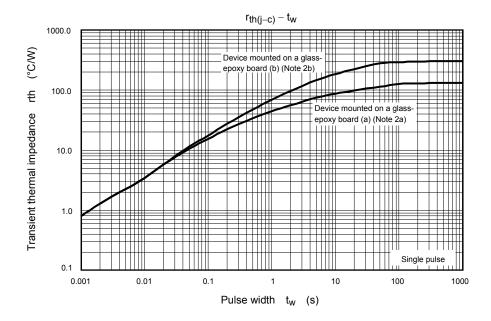




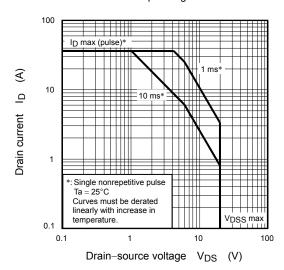








Safe operating area



TOSHIBA TPCP8002

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