TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (MACH II π -MOS V)

TPCA8009-H

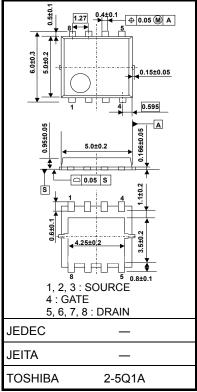
High Speed Switching Applications Switching Regulator Applications DC/DC Converter Applications

- Small footprint due to a small and thin package
- High-speed switching
- Small gate charge: Qsw = 3.7 nC (typ.)
- Low drain-source ON-resistance: RDS (ON) = 0.23Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 4.5S$ (typ.)
- Low leakage current: $I_{DSS} = 100 \mu A (max) (V_{DS} = 150 V)$
- Enhancement mode: $V_{th} = 2.0 \text{ to } 4.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

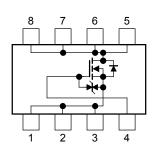
Characte	eristic	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	150	V
Drain-gate voltage (R	GS = 20 kΩ)	V_{DGR}	150	V
Gate-source voltage		V _{GSS}	±20	V
Drain current	DC (Note 1)	ID	7	Α
Drain current	Pulsed (Note 1)	I_{DP}	14	^
Drain power dissipati	on (Tc=25°C)	P _D	45	W
Drain power dissipati	on (t = 10 s) (Note 2a)	P _D	2.8	W
Drain power dissipati	on (t = 10 s) (Note 2b)	P _D	1.6	W
Single-pulse avalance	ne energy (Note 3)	E _{AS}	34	mJ
Avalanche current		I _{AR}	7	Α
Repetitive avalanche	energy c=25°C) (Note 4)	E _{AR}	1.5	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature	range	T _{stg}	-55 to 150	°C





Weight: 0.068 g (typ.)

Circuit Configuration



Note: For Notes 1 to 4, refer to the next page.

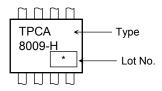
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case (Tc=25°C)	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	44.6	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	78.1	°C/W

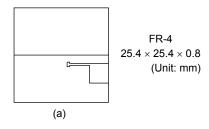
Marking (Note 5)

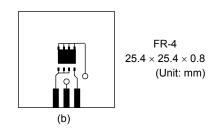


Note 1: The channel temperature should not exceed 150°C during use.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)

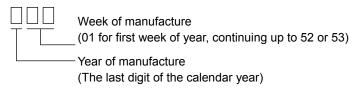




Note 3: $~V_{DD}=50~V,~T_{ch}=25^{\circ}C$ (initial), $L=1mH,~R_{G}=25~\Omega,~I_{AR}=7~A$

Note 4: Repetitive rating: pulse width limited by max channel temperature

Note 5: * Weekly code: (Three digits)



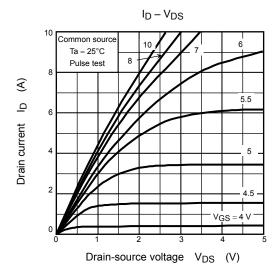


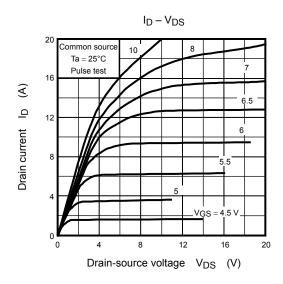
Electrical Characteristics (Ta = 25°C)

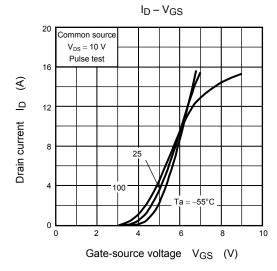
Ch	racteristic Symbol Test Condition Min Ty		Тур.	Max	Unit		
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Drain cutoff curre	nt	I _{DSS}	V _{DS} = 150 V, V _{GS} = 0 V	_	_	100	μА
		V _{(BR) DSS}	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	150	_	 ±10 100 - - - - - - 4.0 	
Drain-source breakdown voltage Gate threshold voltage Drain-source ON-resistance Forward transfer admittance Input capacitance Reverse transfer capacitance Output capacitance		V	$I_D = 10 \text{ mA}, V_{GS} = -5 \text{ V}$	150	_	_	V
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	100	100 — 2.0 — 4.0 — 0.23 2.1 4.5 — 600 — 20 — 220		
Gate threshold vo	Sate threshold voltage		$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	2.0	_	4.0	V
		R _{DS} (ON)	V _{GS} = 10 V, I _D = 3.5 A	_	0.23	0.35	Ω
		Y _{fs}	V _{DS} = 10 V, I _D = 3.5 A	2.1	4.5	_	S
Input capacitance	:	C _{iss}		_	600	_	
Reverse transfer	capacitance	C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	20	_	pF
Output capacitance		C _{oss}		_	220		
$V_{(BR)} DSX $	Rise time	t _r	V _{GS} 10 V	_	8	_	
	I. ▼⊥ I	_	17	_			
Switching time	Fall time	tf	7	_	— 100 — — — — — 4.0 0.23 0.35 4.5 — 600 — 20 — 220 — 8 — 17 — 13 — 70 —	_	ns
	Turn-OFF time	t _{off}		_	70	_	
				_	10	_	
, ,		Q _{gs}	$V_{DD} \simeq 120 \text{ V}, V_{GS} = 10 \text{ V},$	_	7.6	_	nC
		Q _{gd}	I _D = 7 A	_	2.4	_	
Gate switch charg	ge	Q _{sw}		_	3.7	_	

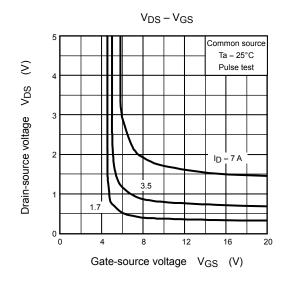
Source-Drain Ratings and Characteristics (Ta = 25°C)

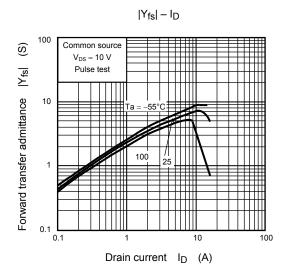
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I _{DRP}	_	_	_	14	Α
Forward voltage (diode)			V _{DSF}	I _{DR} = 7 A, V _{GS} = 0 V	_	_	-2.0	V

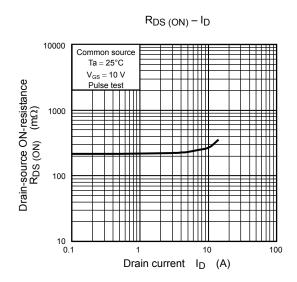


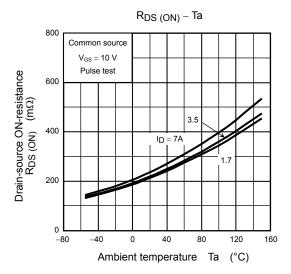


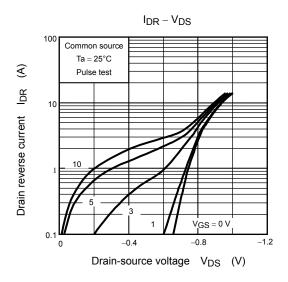


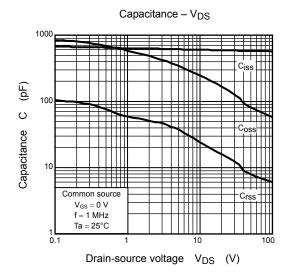


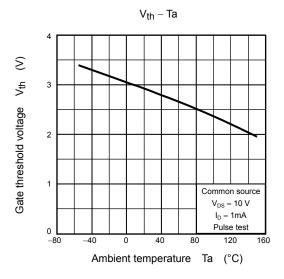


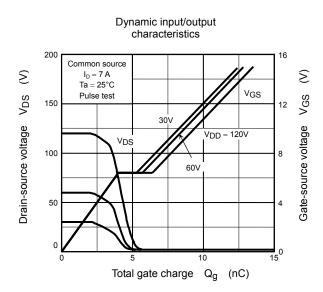


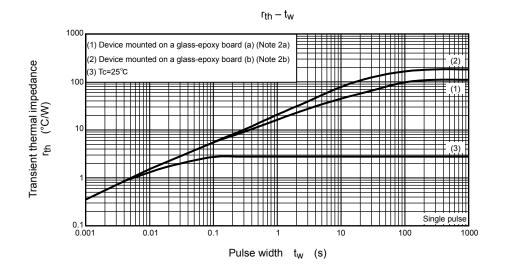


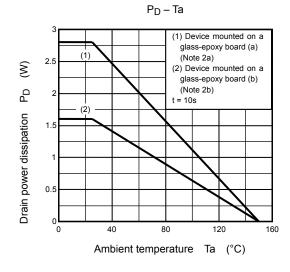


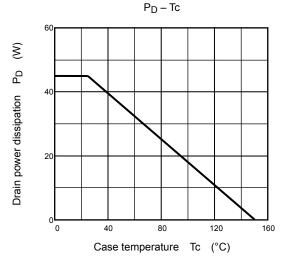


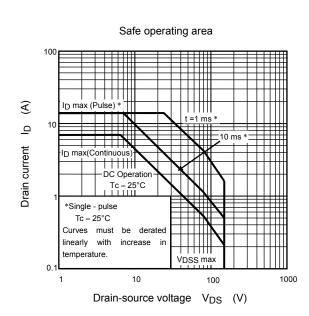












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