TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSIII)

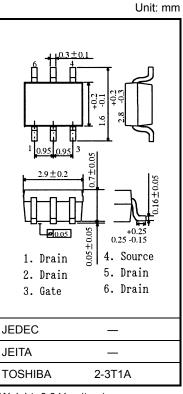
TPC6003

Notebook PC Applications Portable Equipment Applications

- Low drain-source ON resistance: RDS (ON) = 19 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 7 S$ (typ.)
- Low leakage current: $IDSS = 10 \mu A (max) (VDS = 30 V)$
- Enhancement mode: $V_{th} = 1.3 \text{ to } 2.5 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	30	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	30	V	
Gate-source voltage		V _{GSS}	±20	٧	
Drain current	DC (Note 1)	I _D	6	A	
Drain current	Pulse (Note 1)	I _{DP}	24		
Drain power dissipation	(t = 5 s) (Note 2a)	P_{D}	2.2	W	
Drain power dissipation	(t = 5 s) (Note 2b)	P _D	0.7	W	
Single pulse avalanche energy (Note 3)		E _{AS}	5.8	mJ	
Avalanche current	I _{AR}	3	Α		
Repetitive avalanche energy (Note 4)		E _{AR}	0.22	mJ	
Channel temperature	T _{ch}	150	°C		
Storage temperature range		T _{stg}	-55 to 150	°C	



Weight: 0.011 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

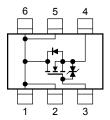
Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to ambient $(t=5\ s)$ (Note 2a)	R _{th (ch-a)}	56.8	°C/W	
Thermal resistance, channel to ambient (t = 5 s) (Note 2b)	R _{th (ch-a)}	178.5	°C/W	

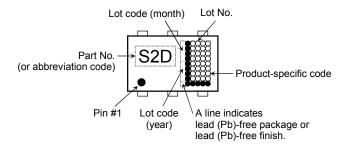
Note: (Note 1), (Note 2), (Note 3), (Note 4) and (Note 5): See the next page.

This transistor is an electrostatic-sensitive device. Please handle with caution.

Circuit Configuration



Marking (Note 5)



Electrical Characteristics (Ta = 25°C)

Ch	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cui	rent	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА	
Drain cut-OFF cu	ırrent	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	_	_	10	μА	
Drain-source breakdown voltage		V _{(BR)DSS}	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V	
		V _{(BR)DSX}	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$ 15		_		V	
Gate threshold v	oltage	V _{th}	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	1.3	_	2.5	٧	
Drain-source ON resistance		D	$V_{GS} = 4.5 \text{ V}, I_D = 3 \text{ A}$	_	25	32	- mΩ	
		R _{DS} (ON)	$V_{GS} = 10 \text{ V}, I_D = 3 \text{ A}$	-	19	24		
Forward transfer	admittance	mittance $ Y_{fs} $ $V_{DS} = 10 \text{ V}, I_D = 3 \text{ A}$		3.5	7		S	
Input capacitance		C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	1250	_	pF	
Reverse transfer capacitance		C _{rss}		_	155	_		
Output capacitance		C _{oss}		_	170	_		
Switching time	Rise time	t _r	V _{GS} 10 V	_	5	_	- ns	
	Turn-ON time	t _{on}		_	11	_		
	Fall time	t _f		_	9	_		
	Turn-OFF time	t _{off}	$V_{DD} \simeq 15 \text{ V}$ Duty \leq 1%, $t_W = 10 \mu\text{s}$	_	63	_		
Total gate charge (gate-source plus gate-drain)		Qg			25		nC	
Gate-source charge		Q _{gs}	$V_{DD} \simeq 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 6 \text{ A}$	_	20	_		
Gate-drain ("miller") charge		Q _{gd}			5			

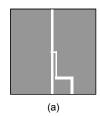
Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Pulse drain reverse current (No	e 1)	I _{DRP}	_	_	_	24	Α
Forward voltage (Diode)		V _{DSF}	I _{DR} = 6 A, V _{GS} = 0 V	_	_	-1.2	V

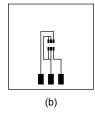
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)



FR-4 2510 ms*

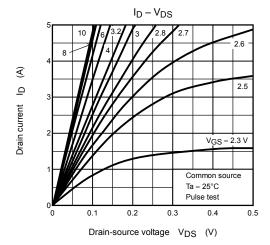


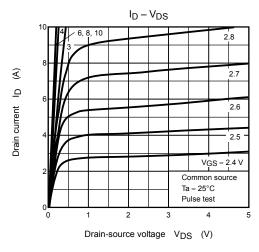
FR-4 $25.4 \times 25.4 \times 0.8$ Unit: (mm)

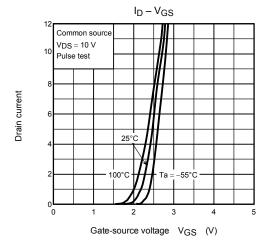
Note 3: $V_{DD} = 24~V,~T_{ch} = 25^{\circ}C$ (initial), L = 0.5 mH, R_G = 25 $\Omega,~I_{AR} = 3.0~A$

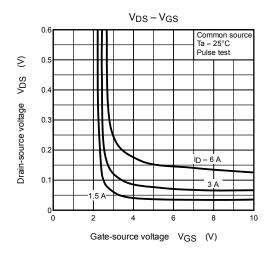
Note 4: Repetitive rating: pulse width limited by maximum channel temperature

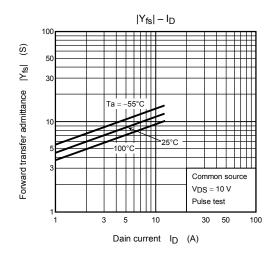
Note 5: • on lower left of the marking indicates Pin 1.

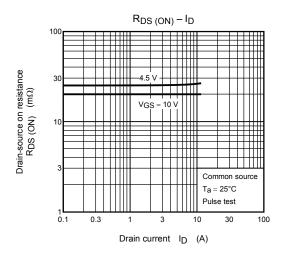


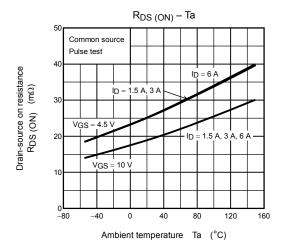


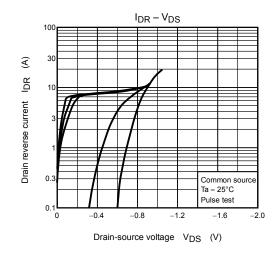


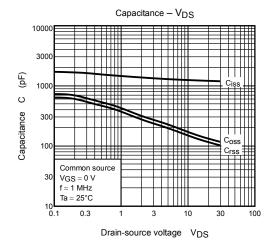


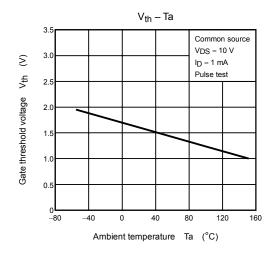


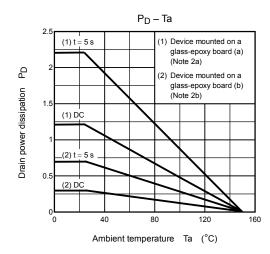


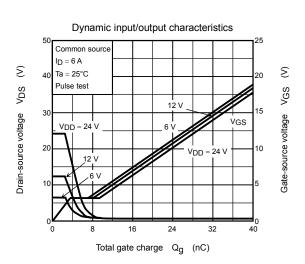


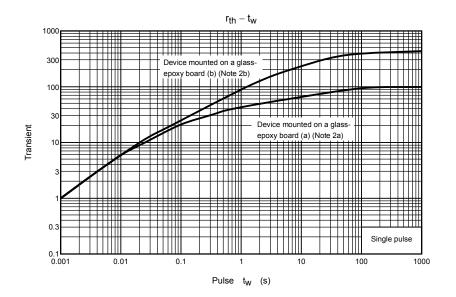


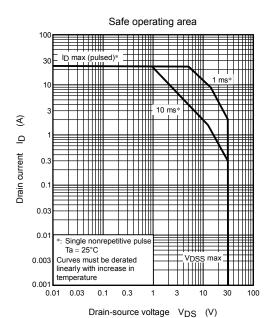












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