## Features

- Low Insertion Loss: 0.65 dB @ 2.4 GHz
- Isolation: 24 dB @ 2.4 GHz
- Low Current Consumption: <10 $\mu \mathrm{A} @-3 \mathrm{~V}$
- Lead-Free SOT-363 Plastic Package
- 100\% Matte Tin Plating over Copper
- Halogen-Free "Green" Mold Compound
- $260^{\circ} \mathrm{C}$ Reflow Compatible
- RoHS* Compliant Version of the SW-438


## Description

M/A-COM's MASWSS0151 is a GaAs MMIC SPDT switch in a low cost, lead-free SC70 (SOT-363) surface mount plastic package. The MASWSS0151 is ideally suited for applications that include transmit / receive switching for Bluetooth and WLAN equipment.

The MASWSS0151 can also be used in applications up to 500 mW in cellular, PCS, DCS1800, GSM, CDMA, and other analog and digital wireless communications systems.

The MASWSS0151 is fabricated using a 0.5 micron gate length GaAs PHEMT process. The process features full passivation for performance and reliability.

## Ordering Information ${ }^{1}$

| Part Number | Package |
| :---: | :---: |
| MASWSS0151 | Bulk Packaging |
| MASWSS0151TR-3000 | 3000 piece reel |
| MASWSS0151SMB | Sample Board <br> (Includes 5 Samples) |

1. Reference Application Note M513 for reel size information.

## Functional Schematic



## Pin Configuration

| Pin No. | Function | Description |
| :---: | :---: | :---: |
| 1 | RF1 | RF Input / Output |
| 2 | GND | RF Ground |
| 3 | RF2 | RF Input / Output |
| 4 | V2 | Control 2 Input |
| 5 | RFC | RF Common Input |
| 6 | V1 | Control 1 Input |

## Absolute Maximum Ratings ${ }^{\text {2,3 }}$

| Parameter | Absolute Maximum |
| :---: | :---: |
| Input Power $(1 \mathrm{GHz})$ | +32 dBm |
| Operating Voltage | +8 Volts |
| Operating Temperature | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| Storage Temperature | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |

2. Exceeding any one or combination of these limits may cause permanent damage to this device.
3. $M / A-C O M$ does not recommend sustained operation near these survivability limits.
[^0]Electrical Specifications: $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{C}}=0 \mathrm{~V} /-3.0 \mathrm{~V}$, Pin $=0 \mathrm{dBm}, \mathrm{Z}_{0}=50 \Omega^{4}$

| Parameter | Test Conditions | Units | Min. | Тур. | Max. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss | DC - 3.0 GHz | dB | - | 0.65 | 0.8 |
| Isolation | $\begin{aligned} & \mathrm{DC}-1.0 \mathrm{GHz} \\ & 1.0-2.0 \mathrm{GHz} \\ & 2.0-3.0 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \end{aligned}$ | $29$ <br> - | $\begin{aligned} & 31 \\ & 25 \\ & 21 \end{aligned}$ | - |
| Return Loss | $\begin{aligned} & \mathrm{DC}-1.5 \mathrm{GHz} \\ & 1.5-3.0 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \end{aligned}$ | — | $\begin{aligned} & 22 \\ & 20 \end{aligned}$ | — |
| P1dB | 1 GHz | dBm | - | 31 | - |
| IP2 | Two Tone, $10 \mathrm{dBm} /$ Tone, $900 \mathrm{MHz}, 5 \mathrm{MHz}$ Spacing | dBm | - | 81 | - |
| IP3 | Two Tone, $10 \mathrm{dBm} /$ Tone, $900 \mathrm{MHz}, 5 \mathrm{MHz}$ Spacing | dBm | - | 55 | - |
| 2nd Harmonic | 2.4 GHz, Pin $=+20 \mathrm{dBm}$ | dBc | - | -70 | - |
| 3rd Harmonic | 2.4 GHz, Pin $=+20 \mathrm{dBm}$ | dBc | - | -60 | - |
| Ton, Toff | 50\% control to 90\% RF, 50\% control to 10\% RF | nS | - | 20 | - |
| Trise, Tfall | 10\% to $90 \% \mathrm{RF}, 90 \%$ to $10 \% \mathrm{RF}$ | nS | - | 10 | - |
| Control Current |  | $\mu \mathrm{A}$ | - | 5 | 10 |

4. For positive voltage control, external DC blocking capacitors are required on all RF ports.

## Truth Table ${ }^{5,6}$

| V1 | V2 | RFC-RF1 | RFC-RF2 |
| :---: | :---: | :---: | :---: |
| 1 | 0 | Off | On |
| 0 | 1 | On | Off |

5. Differential voltage, V (state 1$)-\mathrm{V}$ (state 0 ), must be +2.1 V minimum and must not exceed 5 V .
6. $0=-5 \vee$ to $0 \vee, 1=-2.9 \vee$ to 5 V .

## Handling Procedures

Please observe the following precautions to avoid damage:

## Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Lead-Free SC70 (SOT-363) ${ }^{\dagger}$

$\dagger$ Reference Application Note M538 for lead-free solder reflow recommendations.

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- Asia/Pacific Tel: 81.44.844.8296 / Fax: 81.44.844.8298

Visit www.macom.com for additional data sheets and product information.

## Typical Performance Curves vs. Frequency over Temperature

## Insertion Loss



## Isolation



## Return Loss




[^0]:    * Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

