



GaAs SP4T 2.5 V High Power Switch DC - 3.0 GHz

MASWSS0118

Features

- Low Voltage Operation: 2.5 V
- Low Harmonics: < -65 dBc at +34 dBm & 1 GHz
- Low Insertion Loss: 0.65 dB at 1 GHz
- High Isolation: 23 dB at 2 GHz
- 4 mm 16-Lead PQFN Package
- 0.5 micron GaAs PHEMT Process

Description

M/A-COM's MASWSS0118 is a GaAs PHEMT MMIC single pole four throw (SP4T) high power switch in a low cost 4 mm 16-lead PQFN package. The MASWSS0118 is ideally suited for applications where high power, low control voltage, low insertion loss, high isolation, small size, and low cost are required.

Typical applications are for GSM and DCS handset systems that connect separate transmit and receive functions to a common antenna, as well as other handset and related applications. This part can be used in all systems operating up to 3.0 GHz requiring high power at low control voltage.

The MASWSS0118 is fabricated using a 0.5 micron gate length GaAs PHEMT process. The process features full passivation for performance and reliability.

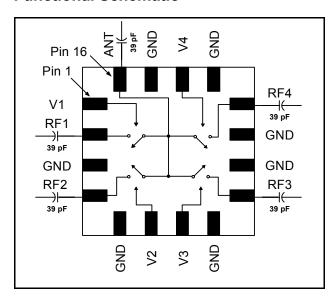
Ordering Information

information.

Part Number	Package
MASWSS0118TR	1000 piece reel
MASWSS0118TR-3000	3000 piece reel
MASWSS0118SMB	Sample Test Board

1. Reference Application Note M513 for reel size information.

Functional Schematic



Pin Configuration

scription	ıme	No. Pin	Pin No.
ontrol 1			1
F Port 1	l		2
Ground)	(3
F Port 2	2		4
Ground)	(5
ontrol 2			6
ontrol 3			7
Ground)	(8
F Port 3	3		9
Ground)) (10
Ground)	1 (11
F Port 4	ļ	2	12
Ground)	3 (13
ontrol 4		1	14
Ground)	5 (15
enna Port	Γ	3 /	16
Ground	e ²	7 Pa	17
		7 Pa	

2. The exposed pad centered on the package bottom must be connected to RF and DC ground.

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Electrical Specifications: $T_A = 25$ °C, $Z_0 = 50 \Omega^3$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Insertion Loss ⁴	DC – 1 GHz 1 – 2 GHz 2 - 3 GHz	dB dB dB		0.65 0.80 1.00	0.85 1.00 —
Isolation	DC – 1 GHz 1 – 2 GHz 2 - 3 GHz	1 – 2 GHz dB			
Return Loss	DC – 3 GHz	dB	_	20	_
IP3	Two Tone +26 dBm, 5 MHz Spacing, > 50 MHz V_C = 0 V / 2.5 V	dBm	_	57	_
IP2	Two Tone +26 dBm, 5 MHz Spacing, > 50 MHz V_C = 0 V / 2.5 V	dBm	_	81	_
P.1dB	V _C = 0 V / 2.5 V	dBm	_	38	_
2 nd Harmonic	1 GHz, P _{IN} = +34 dBm, V _C = 0 V / 2.5 V	dBc	_	-80	-71
3 rd Harmonic	1 GHz, P _{IN} = +34 dBm, V _C = 0 V / 2.5 V	dBc	_	-68	-65
Trise, Tfall	10% RF to 90% RF, 90% to 10% RF, V_C = 0 V / 2.5 V	μS	_	0.2	_
Ton, Toff	50% control to 90% RF, 50% control to 10% RF, V_C = 0 V / 2.5 V	μS	_	0.2	_
Transients		mV	_	35	_
Control Current	V _C = 0 V / 2.5 V, 34 dBm	μA	_	10	50

^{3.} External DC blocking capacitors are required on all RF ports.

Absolute Maximum Ratings 5,6

Parameter	Absolute Maximum		
Input Power (0.5 - 3.0 GHz, 2.5 V Control)	+38 dBm		
Voltage	± 8.5 volts		
Operating Temperature	-40°C to +85°C		
Storage Temperature	-65°C to +150°C		

^{5.} Exceeding any one or combination of these limits may cause permanent damage to this device.

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Truth Table 7,8

V1	V2	V3	V4	ANT- RF1	ANT- RF2	ANT- RF3	ANT- RF4
1	0	0	0	On	Off	Off	Off
0	1	0	0	Off	On	Off	Off
0	0	1	0	Off	Off	On	Off
0	0	0	1	Off	Off	Off	On

^{7.} Differential voltage, V (state 1) -V (state 2), must be 2.5 V minimum.

^{4.} Insertion Loss can be optimized by varying the DC blocking capacitor value, e.g. 1000 pF for 100 MHz - 500 MHz, 39 pF for 0.5 GHz - 3 GHz.

^{6.} M/A-COM does not recommend sustained operation near these survivability limits.

^{8.} 0 = -5 V to +2.5 V, 1 = -2.5 V to +5 V

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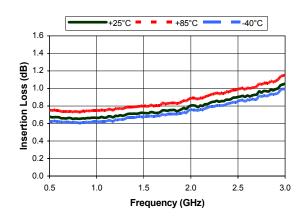


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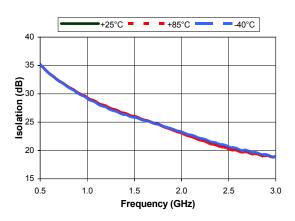
MASWSS0118

Typical Performance Curves

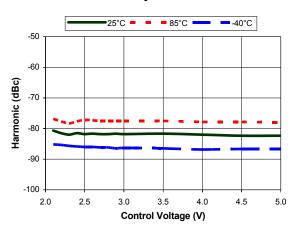
Insertion Loss vs. Temperature



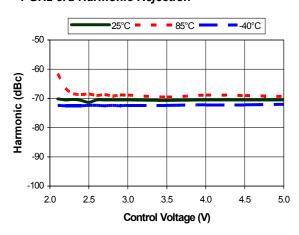
Isolation vs. Temperature



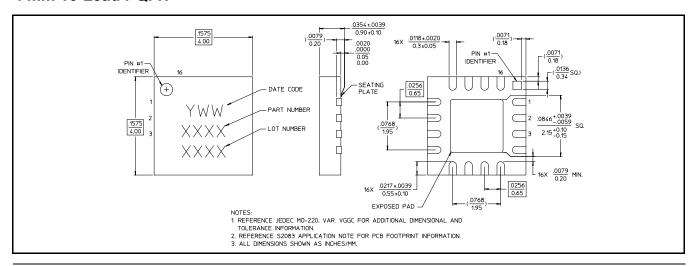
1 GHz 2nd Harmonic Rejection



1 GHz 3rd Harmonic Rejection



4 mm 16-Lead PQFN



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