## Features

- UNII, Hiperlan, and 802.11a Applications
- Low Insertion Loss: 1.0 dB Typical
- High Isolation: 21 dB Typical
- Low Leakage Current: < $1 \mu \mathrm{~A}$
- Fast Switching Speed
- 4 mm FQFP-N Plastic Surface Mount Package


## Description

The MASWSM0002 is a low loss RF switch mounted in a standard outline $4 \mathrm{~mm}, 16$ pin FQFP-N leadless plastic package. It is designed for U-NII, Hiperlan, and 802.11a applications. The MASWSM0002 features isolation greater than 20 dB , a P1dB point above 30 dBm , and switching speeds of 15 ns typical. Its small size and height make it ideal for PC card and access point applications.
The MASWSM0002 is fabricated using M/A-COM's 0.5 micron P-HEMT process to realize low loss and high linearity. The process features passivation for increased performance and reliability.

## Ordering Information

| Part Number | Description |
| :--- | :--- |
| MASWSM0002TR | 7 inch, 1000 piece reel |
| MASWSM0002TR-3000 | 13 inch, 3000 piece reel |
| MASWSM0002SMB | Sample Test Board |

## Functional Schematic

## Pin Configuration

| Pin | Function | Description |
| :---: | :---: | :--- |
| 1 | $\mathrm{~V}_{2}$ | Voltage Control Line 2 |
| 2 | $\mathrm{~N} / \mathrm{C}$ | Not Connected |
| 3 | $\mathrm{RF}_{1}$ | RF Port 1 |
| 4 | $\mathrm{~N} / \mathrm{C}$ | Not Connected |
| 5 | $\mathrm{~N} / \mathrm{C}$ | Not Connected |
| 6 | $\mathrm{~N} / \mathrm{C}$ | Not Connected |
| 7 | $\mathrm{~N} / \mathrm{C}$ | Not Connected |
| 8 | $\mathrm{~N} / \mathrm{C}$ | Not Connected |
| 9 | $\mathrm{~N} / \mathrm{C}$ | Not Connected |
| 10 | $\mathrm{RF} F_{2}$ | RF Port 2 |
| 11 | $\mathrm{~N} / \mathrm{C}$ | Not Connected |
| 12 | $\mathrm{~V}_{1}$ | Voltage Control Line 1 |
| 13 | $\mathrm{RF} \mathrm{F}_{\mathrm{C}}$ | Common RF Port |
| 14 | $\mathrm{~N} / \mathrm{C}$ | Not Connected |
| 15 | $\mathrm{~N} / \mathrm{C}$ | Not Connected |
| 16 | $\mathrm{~V}_{\mathrm{DD}}$ | DC Supply Voltage |
| Pad | GND | RF \& DC Ground |



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Electrical Specifications: $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}, \mathrm{Z}_{0}=50 \Omega^{1}$

| Characteristic | Test Conditions | Unit | Min | Typ | Max |
| :--- | :--- | :---: | :---: | :---: | :---: |
| Frequency |  | GHz | 5.15 |  | 5.35 |
| Insertion Loss | $\mathrm{F}=5.25 \mathrm{GHz}, \mathrm{P}_{\mathrm{IN}}=0 \mathrm{dBm}$ | dB |  | 1.0 | 1.4 |
| Isolation | $\mathrm{F}=5.25 \mathrm{GHz}, \mathrm{P}_{\mathrm{IN}}=0 \mathrm{dBm}$ | dB | 18 | 21 |  |
| Return Loss, $\mathrm{RF} \mathrm{F}_{\mathrm{C}}$ | $\mathrm{F}=5.25 \mathrm{GHz}, \mathrm{P}_{\mathrm{IN}}=0 \mathrm{dBm}$ | dB |  | 13 |  |
| 1 dB Compression Point | $\mathrm{F}=5.25 \mathrm{GHz}$ | dBm |  | 30.5 |  |
| Third Order Intercept Point | $\mathrm{F}=5.250 \mathrm{GHz}, \mathrm{P}_{\mathrm{IN}}=+20 \mathrm{dBm}$ <br> $\mathrm{F}=5.251 \mathrm{GHz}, \mathrm{P}_{\mathrm{IN}}=+20 \mathrm{dBm}$ | dBm |  | 51 |  |
| Leakage Current | $\mathrm{F}=5.25 \mathrm{GHz}, \mathrm{P}_{\mathrm{IN}}=+20 \mathrm{dBm}$ | AA |  | 1.0 |  |
| $\mathrm{~T}_{\text {RISE }}, \mathrm{T}_{\text {FALL }}$ | $10 \%$ to $90 \% \mathrm{RF}, 90 \%$ to $10 \% \mathrm{RF}$ | ns |  | 8 |  |
| $\mathrm{~T}_{\text {ON }}, T_{\text {OFF }}$ | $50 \%$ Control to $90 \% \mathrm{RF}$, <br> $50 \%$ Control to $10 \% \mathrm{RF}$ | ns |  | 15 |  |

1. Control voltages are 0 V and +3 V , and $\mathrm{V}_{\mathrm{DD}}$ is +3 V unless otherwise specified.

## Switch Logic Table ${ }^{1,2}$

| Insertion Loss Path | Isolated Path | $\mathbf{V}_{\mathbf{1}}$ | $\mathbf{V}_{\mathbf{2}}$ |
| :--- | :--- | :---: | :---: |
| $\mathrm{RF}_{1} / \mathrm{RF}_{\mathrm{C}}$ | $\mathrm{RF}_{2} / \mathrm{RF}_{\mathrm{C}}$ | 1 | 0 |
| $\mathrm{RF}_{2} / \mathrm{RF}_{\mathrm{C}}$ | $\mathrm{RF}_{1} / \mathrm{RF}_{\mathrm{C}}$ | 0 | 1 |

1. " 0 " $=0+/-0.2$ volts
2. " 1 " $=+2.7$ to +5 volts, equal to $V_{D D}$

## Absolute Maximum Ratings ${ }^{1}$

| Parameter | Absolute Maximum |
| :--- | :---: |
| Max Input Power | +33 dBm |
| Operating Voltage | +5.5 volts |
| Operating Temperature | $-40^{\circ} \mathrm{C}$ to $+100^{\circ} \mathrm{C}$ |
| Channel Temperature | $+150^{\circ} \mathrm{C}$ |
| Storage Temperature | $-40^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |

1. Exceeding any one or combination of these limits may cause permanent damage.

## Application Information

## Static Sensitivity

Gallium arsenide integrated circuits are ESD sensitive and can be damaged by static electricity. Use proper ESD precautions when handling these devices.


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## Typcial Performance

Insertion Loss vs. Frequency, over Temperature


Isolation vs. Frequency, over Temperature


Insertion Loss vs. Frequency, $\mathrm{T}_{\mathrm{A}}=+\mathbf{+ 2 5 ^ { \circ }} \mathrm{C}$


Return Loss vs. Frequency, over Temperature


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## 4 mm, 16 Pin FQFP-N Package

| Dimension | Measurement (mm) |  |  |
| :---: | :---: | :---: | :---: |
|  | Min. | Nom. | Max. |
| A | 0.80 | 0.90 | 1.00 |
| A1 | 0 | 0.02 | 0.05 |
| A2 | 0.80 | 0.88 | 1.00 |
| b | 0.23 | 0.30 | 0.38 |
| D |  | 4.00 |  |
| D2 | 2.05 | 2.15 | 2.25 |
| e |  | 0.65 |  |
| E |  | 4.00 |  |
| E2 | 2.00 | 2.15 | 2.25 |
| L | 0.40 | 0.55 | 0.65 |

Note: See JEDEC MO-220A VGGC Issue B for additional dimensional and tolerance information.


PIN \#1


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