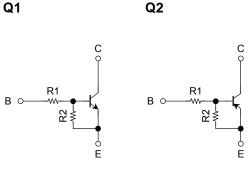
TOSHIBA Transistor Silicon NPN · PNP Epitaxial Type (PCT process) (Bias Resistor built-in Transistor)

RN4988FS

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Two devices are incorporated into a fine pitch Small Mold (6-pin) package.
- Incorporating a bias resistor into a transistor reduces parts count. Reducing the parts count enables the manufacture of ever more compact equipment and lowers assembly cost.

Equivalent Circuit and Bias Resistor Values

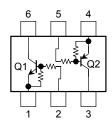


R1: 22 kΩ

R2: 47 kΩ

(Q1, Q2 common)

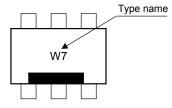
Equivalent Circuit (top view)



1.0±0.05 0.8±0.05 0.1±0.05 0.1±0.05 0.15±0.05 6 35 02 0.7±0.05 .0±0. Ö 35 2 Π 1±0.05 +0.02 48 1. EMITTER1 (E1) 2. BASE1 (B1) 3. COLLECTOR2 4. EMITTER2 (C.2 5. BASE2 6. COLEECTOR1 fS6 JEDEC _ JEITA _ TOSHIBA 2-1F1D

Weight:0.001g (typ.)

Marking



Unit: mm

Maximum Ratings (Ta = 25°C) (Q1)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	20	V
Collector-emitter voltage	V _{CEO}	20	V
Emitter-base voltage	V _{EBO}	7	V
Collector current	Ι _C	50	mA

Maximum Ratings (Ta = 25°C) (Q2)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	-20	V
Collector-emitter voltage	V _{CEO}	-20	V
Emitter-base voltage	V _{EBO}	-7	V
Collector current	Ι _C	-50	mA

Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Rating	Unit
Collector power dissipation	P _C (Note)	50	mW
Junction temperature	Тj	150	°C
Storage temperature range	T _{stg}	-55~150	°C

Note: Total rating

Electrical Characteristics (Ta = 25°C) (Q1)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	$V_{CB} = 20 V, I_E = 0$	_	_	100	nA
	I _{CEO}	$V_{CE} = 20 V, I_B = 0$	_	_	500	
Emitter cut-off current	I _{EBO}	$V_{EB}=7~V,~I_C=0$	0.085	_	0.126	mA
DC current gain	h _{FE}	$V_{CE} = 5 \text{ V}, I_{C} = 10 \text{ mA}$	120	_	_	
Collector-emitter saturation voltage	V _{CE (sat)}	$I_C=5\ m\text{A},\ I_B=0.25\ m\text{A}$	_	_	0.15	V
Input voltage (ON)	V _{I (ON)}	$V_{CE}=0.2~V,~I_C=5~mA$	0.8	_	2.2	V
Input voltage (OFF)	V _{I (OFF)}	$V_{CE} = 5 \text{ V}, I_{C} = 0.1 \text{ mA}$	0.6	_	1.1	V
Collector output capacitance	C _{ob}	$V_{CB}=10~V,~I_{E}=0,~f=1~MHz$	_	1.2		pF

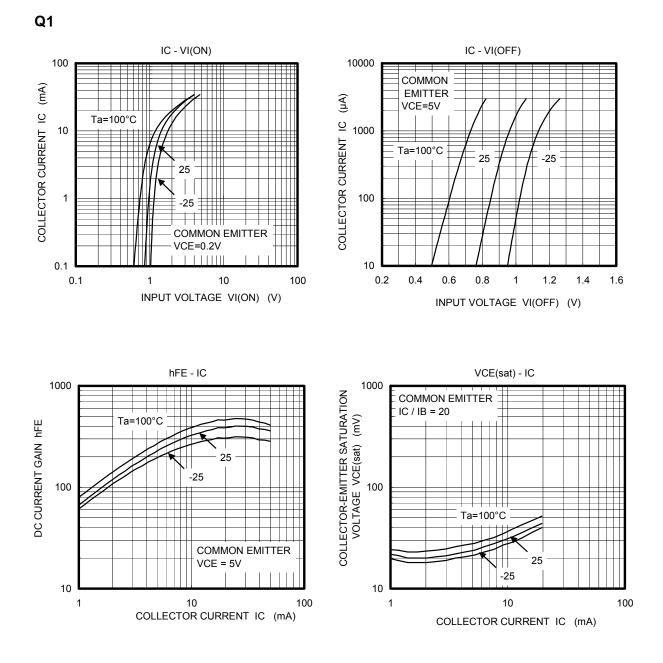
Electrical Characteristics (Ta = 25°C) (Q2)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	$V_{CB}=-20~V,~I_{E}=0$	—	_	-100	nA
	ICEO	$V_{CE}=-20~V,~I_B=0$	—		-500	
Emitter cut-off current	I _{EBO}	$V_{EB}=-7~V,~I_C=0$	-0.085	_	-0.126	mA
DC current gain	h _{FE}	$V_{CE} = -5 \text{ V}, \text{ I}_{C} = -10 \text{ mA}$	120	_	_	
Collector-emitter saturation voltage	V _{CE (sat)}	$I_C = -5 \text{ mA}, \ I_B = -0.25 \text{ mA}$	_	_	-0.15	V
Input voltage (ON)	V _{I (ON)}	$V_{CE} = -0.2 \text{ V}, \text{ I}_{C} = -5 \text{ mA}$	-0.8		-2.2	V
Input voltage (OFF)	V _{I (OFF)}	$V_{CE} = -5 \text{ V}, \text{ I}_{C} = -0.1 \text{ mA}$	-0.6	_	-1.1	V
Collector output capacitance	C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$	—	1.2	_	pF

Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

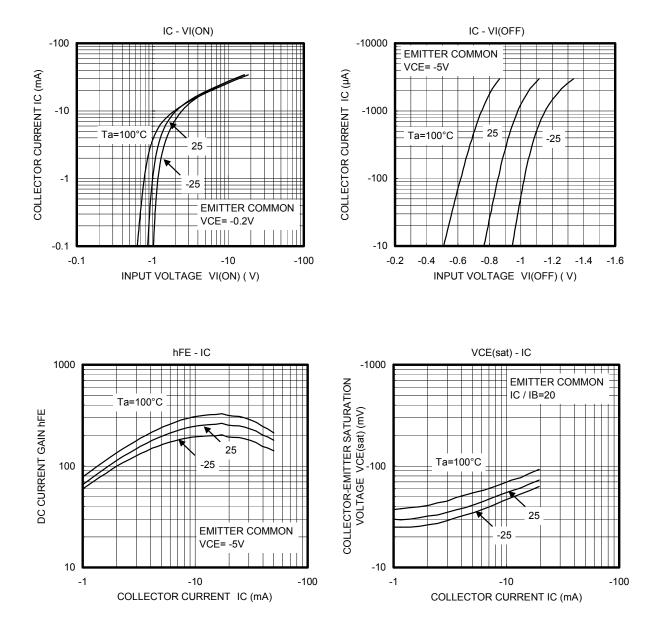
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input resistor	R1	—	17.6	22	26.4	kΩ
Resistor ratio	R1/R2		0.374	0.468	0.562	

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Q2



HANDLING PRECAUTION

Before handling individual devices that are not yet mounted on a circuit board, make sure that the environment is protected against static electricity. Operators should wear anti-static clothing, and containers and other objects that come direct contact with devices should be made of anti-static materials.

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