TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

2SK2508

Switching Regulator and DC-DC Converter and Motor Applications

Low drain–source ON resistance $RDS(ON) = 0.18 \Omega \text{ (typ.)}$

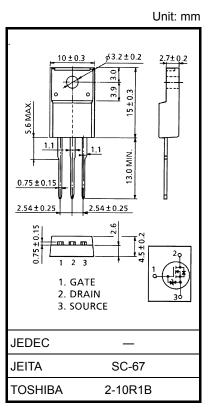
• High forward transfer admittance : $|Y_{fs}| = 13 S \text{ (typ.)}$

• Low leakage current : $IDSS = 100 \mu A \text{ (max) (V}_{DS} = 250 \text{ V)}$

Enhancement mode : $V_{th} = 1.5 \sim 3.5 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	250	V
Drain-gate voltage (R	_{GS} = 20 kΩ)	V_{DGR}	250	V
Gate-source voltage		V _{GSS}	±20	V
Drain current	DC (Note 1)	ID	13	А
	Pulse (Note 1)	I _{DP}	52	Α
Drain power dissipation	n (Tc = 25°C)	P _D	45	W
Single pulse avalanche energy (Note 2)		E _{AS}	148	mJ
Avalanche current		I _{AR}	13	Α
Repetitive avalanche	energy (Note 3)	E _{AR}	4.5	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature r	ange	T _{stg}	-55~150	°C



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 50 V, T_{ch} = 25°C (initial), L = 1.48 mH, R_{G} = 25 Ω , I_{AR} = 13 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.



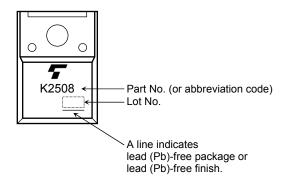
Electrical Characteristics (Ta = 25°C)

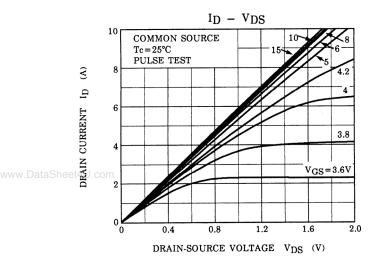
Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-off cur	rent	I _{DSS}	V _{DS} = 250 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source bro	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	250	_	_	V
Gate threshold v	roltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.5	_	3.5	V
Drain-source Of	N resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 6.5 A	_	0.18	0.25	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 6.5 A	6	13	_	S
Input capacitanc	e	C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		1800	_	pF
Reverse transfer	capacitance	C _{rss}			130	_	
Output capacitance		Coss			500	_	
T Switching time F	Rise time	t _r	V_{GS} V_{OV} V_{DD} V_{DD} V_{DD} V_{DD}	_	15	_	- ns
	Turn-on time	t _{on}		_	25	_	
	Fall time	t _f		_	10	_	
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\rm w} = 10 \mu \rm s$	_	70	_	
Total gate charge (Gate-source plus gate-drain)		Qg			40		
Gate-source charge		Q_{gs}	$V_{DD} \approx 200 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 13 \text{ A}$		25	_	nC -
Gate-drain ("miller") charge		Q_{gd}			15	_	

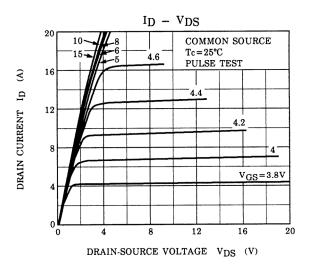
Source-Drain Ratings and Characteristics (Ta = 25°C)

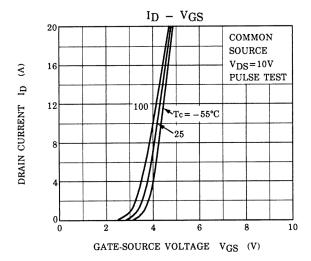
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	13	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	52	Α
Forward voltage (diode)	V_{DSF}	I _{DR} = 13 A, V _{GS} = 0 V	1	-	-2.0	V
Reverse recovery time	t _{rr}	I _{DR} = 13 A, V _{GS} = 0 V	1	260	_	ns
Reverse recovery charge	Q_{rr}	dl _{DR} / dt = 100 A / μs	_	0.3	_	μC

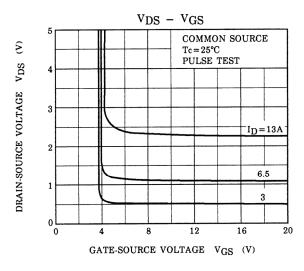
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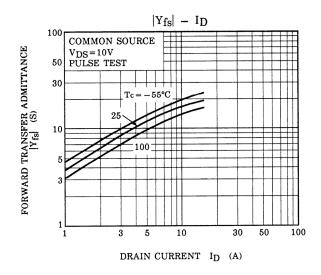


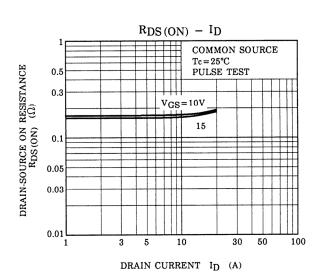


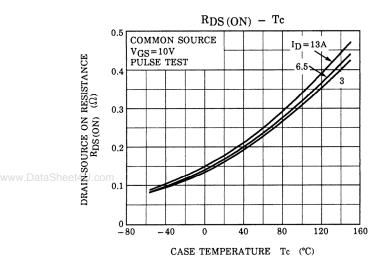


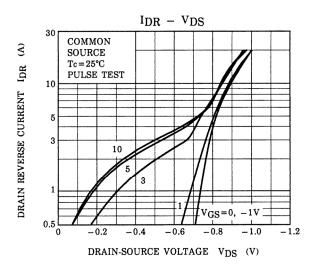


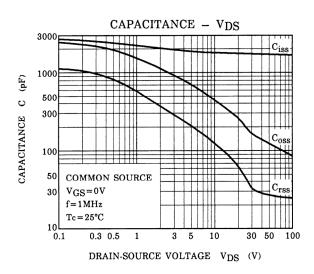


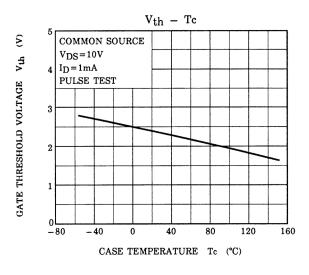


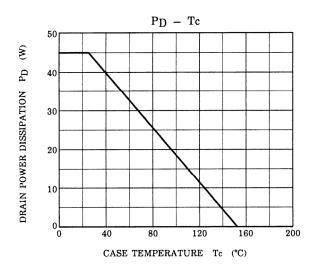


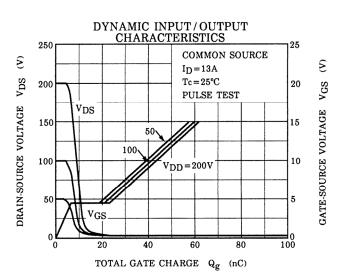


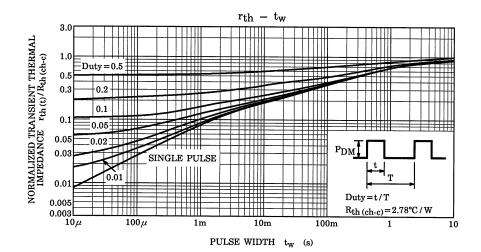




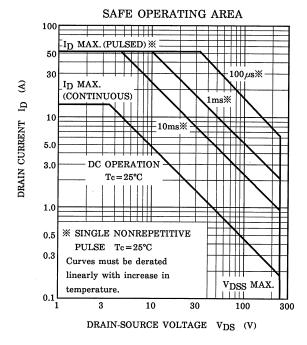


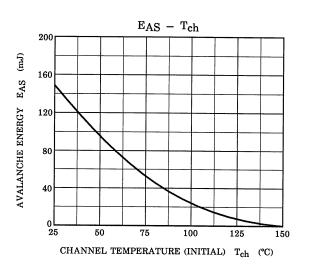


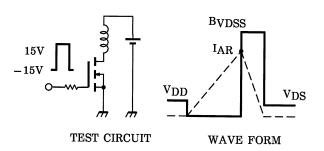




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$$R_G$$
 = 25 Ω
 V_{DD} = 50 V, L = 1.48 mH

$$EAS = \frac{1}{2} \cdot L \cdot I^{2} \cdot \left(\frac{BVDSS}{BVDSS - VDD} \right)$$

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