## **HD14501UB**

#### Triple Gate

Dual 4-input NAND Gate

2-input NOR/OR Gate

8-input AND/NAND Gate

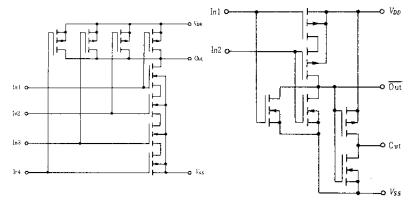
#### **■ FEATURES**

- Quiescent Current = 0.5nA typ/pkg @5V
- Noise Immunity = 45% of V<sub>DD</sub> typ
- Capable of Driving One Low-power Schottky TTL Load Over the Rated Temperature Range
- Pin-for Pin Replacements for MC14501UB Series

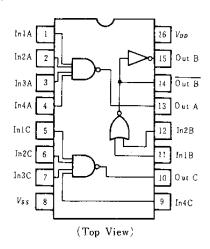
#### **CIRCUIT SCHEMATIC**

#### ●4-input NAND Gate

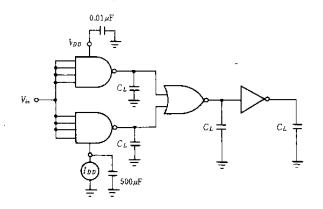
#### ●2-input NOR/OR Gate

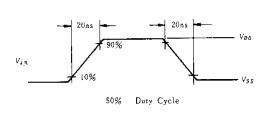


#### **■ PIN ARRANGEMENT**



#### POWER DISSIPATION TEST CIRCUIT AND WAVEFORM





#### ■ ELECTRICAL CHARACTERISTICS

Characteristic		Symbol	$V_{DD}(V)$ Test Conditions		-40°C		<b>25</b> ℃			85°C		Unit	
		Symoot			min	max	min	typ	max	min :	max	Oint	
Output Voltage		Vol	5.0		_	0.05		0	0.05	_	0.05	v	
			10	$V_{\it in} = V_{\it DD}$ or 0		0.05	_	0	0.05	_	0.05		
			15			0.05	-	0	0.05		0.05		
			5.0		4.95		4.95	5.0		4.95			
		Von	10	$V_{in} = 0$ or $V_{DD}$	9.95	_	9.95	10	-	9.95	-	V	
			15		14.95	_	14.95	15	-	14.95	_		
Input Voltage		V <sub>IL</sub>	5.0	$V_{out} = 4.5 \text{ or } 0.5 \text{V}$	-	1.0	-	2.25	1.0		,1.0	v	
			10	$V_{out} = 9.0 \text{ or } 1.0 \text{V}$		2.0		4.50	2.0	_	2.0		
			15	$V_{\rm out} = 13.5 \text{ or } 1.5 \text{V}$	<u> </u>	2.5	_	6.75	2.5	-	2.5		
			5.0	$V_{out}=0.5$ or $4.5\mathrm{V}$	4.0	]	4.0	2.75		4.0	_		
		$V_{IH}$	10	$V_{evt} = 1.0 \text{ or } 9.0 \text{V}$	8.0		8.0	5.50	-	8.0		v	
		ļ 	15	$V_{out} = 1.5$ or $13.5$ V	12.5	-	12.5	8.25		12.5	-		
:	NAND		5.0	$V_{OH}=2.5V$	-1.0	_	-0.8	-1.7		-0.6	_	mA	
		j	5.0	$V_{OH}=4.6V$	-0.2	_	-0.16	-0.36	+	-0.12			
			10	$V_{\text{OH}} = 9.5 \text{V}$	-0.5	-	-0.4	-0.9	-	-0.3			
			15	$V_{OH}=13.5\mathrm{V}$	-1.4	- 1	-1.2	-3.5	-	-1.0	_		
			5.0	$V_{OH}=2.5V$	-1.68		-1.4	-3.0		-1.05			
	NOD	,	5.0	$V_{OH} = 4.6 \text{V}$	-0.34	_	-0.28	-0.63	_	-0.21			
	NOR	Іон	10	V <sub>oH</sub> =9.5V	-0.84		-0.7	-1.58	_	-0.52			
			15	$V_{OH} = 13.5 \text{V}$	-2.52	_	-2.1	-6.12		-1.57			
	:		5.0	$V_{OH}=2.5V$	-2.88	_	-2.4	-5.1		-1.8	_		
Output	NOR-		5.0	$V_{OH}=4.6\mathrm{V}$	-0.58		-0.48	-1.08	_	-0.36			
Drive	Inverter		10	$V_{OH}=9.5V$	-1.44	_	-1.2	-2.7	_	-0.9	_		
Current			15	V <sub>OH</sub> = 13.5V	-4.32		-3.6	-10.5	_	-2.7	_		
	NAND	IoL	5.0	$V_{OL}=0.4V$	0.52	1	0.44	0.88	_	0.36		mA	
			10	$V_{oL}=0.5V$	1.3	_	1.1	2.25	_	0.9			
			15	Vol = 1.5V	3.6		3.0	8.8		2.4	-		
	NOR		5.0	Vol = 0.4V	0.79	_	0.66	1.32	_	0.54	_		
			10	$V_{OL}=0.5V$	1.98	_	1.65	3.37	_	1.36	_		
			15	$V_{oL}=1.5V$	5:4	_	4.5	13.2	_	3.57	_		
	NOR- Inverter	1	5.0	$V_{OL}=0.4V$	1.32		1.1	2.2		0.90	_		
			10	$V_{OL}=0.5\mathrm{V}$	3.3		2.75	5.63	_	2.27			
			15	Vol=1.5V	9.0		7.5	22.0	_	5.95	_		
Input Current		I in	15		_	±0.3	_	±0.00001	±0.3	-	±1.0	μΑ	
Input Capacitance		Cin	1	$V_{in} = 0$	_	_		5.0	7.5	-	-	рF	
Quiescent Current		$I_{DD}$	5.0		ro Signal, - 1.0 - 0.00		0.0005	0.5		3.8			
			10	Zero Signal,			0.0010	1.0		7.5	μA		
			15	per Package	_	2.0	_	0.0015	2.0	_	15	-	
Total Supply Current*			5.0	Dynamic $+I_{DD_1}$	_			1.2	_		_	μΑ	
		$I_{T}$	10	per Gate	_	_		2.4		_			
			15	$C_L = 50 \mathrm{pF}, f = 1 \mathrm{kHz}$	<u> </u>		+-	3.6				1	



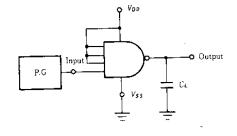
<sup>\*</sup> To calculate total supply current at frequency other than 1kHz.  $(V_{00}-5.0V I_7-(1.2\mu\text{A/kHz})f+I_{00}, (W_{00}-10V I_7-(2.4\mu\text{A/kHz})f+I_{00}, (W_{00}-15V I_7-(3.6\mu\text{A/kHz})f+I_{00})$ 

### **E**SWITCHING CHARACTERICS ( $C_L = 50 \,\mathrm{pF}$ , $Ta = 25 \,^{\circ}\mathrm{C}$ )

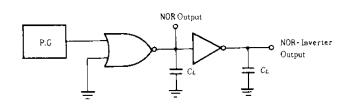
Characteristic		Symbol	Test Circuit	$V_{DD}(V)$	min	typ	max	Unit
	NAND, NOR	<i>t</i> ,	1, 2	5.0		180	400	ns
				10	-	90	200	
o n' m				15	-	65	160	
Output Rise Time	NOR-		2	5.0	_	100	200	
				10	_	50	100	
				15	_	37	80	
	NAND,	- t,	1, 2	5.0	_	100	200	ns
				10		50	100	
	NOR			15	_	37	80	
Output Fall Time	NOR- Inverter		2	5.0		60	140	
				10	_	40	100	
				15	-	30	75	
	NAND	tplн. tpнL	1	5.0	_	130	300	ns
				10	_	70	175	
				15	_	50	125	
Propagation	NOR		2	5.0	_	115	250	
				10	_	65	160	
Delay Time				15	_	45	100	
	NOR- Inverter		2	5.0	<del></del>	130	300	
				10	_	70	175	
				15		50	125	

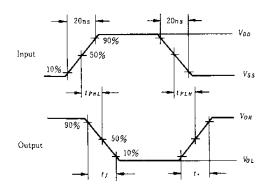
#### ■ SWITCHING TIME TEST CIRCUIT

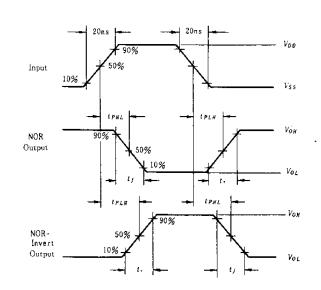
#### 1. NAND Gate



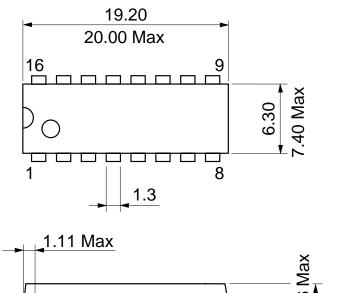
#### 2. NOR Gate, NOR-Inverter



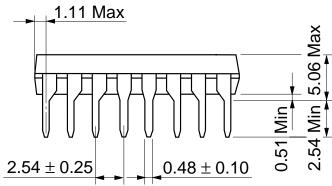


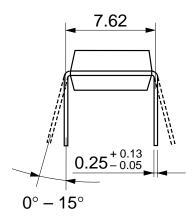


Unit: mm









Hitachi Code	DP-16
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	1.07 g

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