# TIP36C

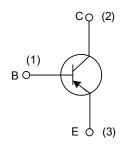
# PNP SILICON TRANSISTOR

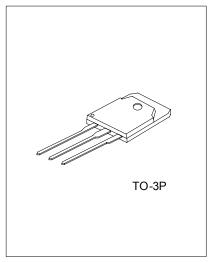
# **HIGH POWER TRANSISTORS**

#### **■** DESCRIPTION

The UTC TIP36C is a PNP Expitaxial-Base transistor, designed for using in general purpose amplifier and switching applications. Complement to TIP35C  $\,$ 

#### **■ INTERNAL SCHEMATIC DIAGRAM**

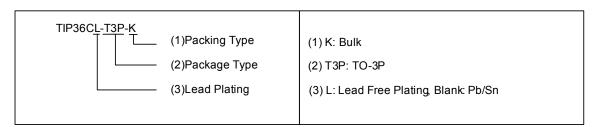




\*Pb-free plating product number: TIP36CL

## **■ ORDERING INFORMATION**

Order Number		Dookooo	Pin Assignment			Doolsing	
Normal	Lead Free Plating	Package	1	2	3	Packing	
TIP36C-T3P-K	TIP36C-T3P-K	TO-3P	В	С	Е	Bulk	



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#### ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-Base Voltage (I <sub>E</sub> = 0)	$V_{CBO}$	-100	٧
Collector-Emitter Voltage (I <sub>B</sub> = 0)	$V_{CEO}$	-100	٧
Emitter-Base Voltage (I <sub>C</sub> = 0)	$V_{EBO}$	-5	٧
Collector Current	I <sub>C</sub>	-25	Α
Collector Peak Current	I <sub>CM</sub>	-50	Α
Base Current	I <sub>B</sub>	-5	Α
Total Dissipation (Tc =25 )	$P_{D}$	125	W
Junction Temperature	$T_J$	+150	
Storage Temperature	T <sub>STG</sub>	-65 ~ <b>+</b> 150	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

## **■ THERMAL DATA**

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Thermal Resistance Junction-Case	$\theta_{ m JC}$			1	/ W

#### ■ ELECTRICAL CHARACTERISTICS (Tc =25 , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector Cut-off Current (I <sub>B</sub> = 0)	I <sub>CEO</sub>	V <sub>CE</sub> = -60 V			-1	mA
Emitter Cut-off Current (I <sub>C</sub> = 0)	I <sub>EBO</sub>	V <sub>EB</sub> = -5 V			-1	mA
Collector Cut-off Current (V <sub>BE</sub> = 0)	I <sub>CES</sub>	V <sub>CE</sub> = Rated V <sub>CEO</sub>			-0.7	mA
Collector-Emitter Sustaining Voltage (I <sub>B</sub> = 0)	V <sub>CEO(SUS)</sub> *	$I_C = -30 \text{ mA}$	-100			V
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub> *	$I_B = -1.5 \text{ A}, I_C = -15 \text{ A}$ $I_B = -5 \text{ A}, I_C = -25 \text{ A}$			-1.8	V
Collector-Entitler Saturation Voltage		$I_B = -5 \text{ A}, I_C = -25 \text{ A}$			-4	V
Door Emitter Voltage	\/ *	$V_{CE} = -4 \text{ V, } I_{C} = -15 \text{ A}$			-2	V
Base-Emitter Voltage	V BE(ON)	$V_{CE} = -4 \text{ V}, I_{C} = -25 \text{ A}$			-4	V
DC Current Gain	h <sub>FE</sub> *	$V_{CE} = -4 \text{ V}, I_{C} = -1.5 \text{ A}$	25		50	
		$V_{CE} = -4 \text{ V}, I_{C} = -15 \text{ A}$	10			
Transition Frequency	f <sub>T</sub>	$V_{CE} = -10 \text{ V}, I_{C} = -1 \text{ A}, f = 1 \text{ MHz}$	3			MHz
Small Signal Current Gain	h <sub>fe</sub>	$V_{CE} = -10 \text{ V}, I_{C} = -1 \text{ A}, f = 1 \text{ KHz}$	25			

<sup>\*</sup> Pulsed: Pulse Duration = 300  $\mu$ s, Duty Cycle  $\leq$  2 %

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