TOSHIBA FIELD EFFECT TRANSISTOR SILICON P CHANNEL MOS TYPE (L²-π-MOSⅣ)

2 S J 3 1 5

DC-DC CONVERTER

4-Volt Gate Drive

Low Drain-Source ON Resistance : $R_{DS(ON)} = 0.25\Omega$ (Typ.)

High Forward Transfer Admittance : $|Y_{fs}| = 3.0S$ (Typ.)

Low Leakage Current : $I_{DSS} = -100 \mu A \text{ (Max.) (V}_{DS} = -60 \text{V)}$

Enhancement-Mode : $V_{th} = -0.8 \sim -2.0 V$

 $(V_{DS} = -10V, I_D = -1mA)$

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT		
Drain-Source Voltage		$v_{ m DSS}$	-60	V	
Drain-Gate Voltage ($R_{GS} = 20k\Omega$)		$v_{ m DGR}$	-60	V	
Gate-Source Voltage		v_{GSS}	±20	V	
Drain Current	DC	$I_{\mathbf{D}}$	-5	A	
Drain Current	Pulse	$I_{ m DP}$	-20		
Drain Power Dissipation (T	P_{D}	20	W		
Channel Temperature		$\mathrm{T_{ch}}$	150	$^{\circ}\mathrm{C}$	
Storage Temperature Rang	е	$\mathrm{T_{stg}}$	-55~150	°C	

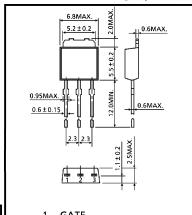
THERMAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Case	$R_{th(ch-c)}$	6.25	°C/W
Thermal Resistance, Channel to Ambient	R _{th(ch-a)}	125	°C/W

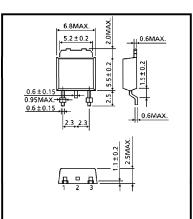
This transistor is an electrostatic sensitive device. Please handle with caution.

INDUSTRIAL APPLICATIONS

Unit in mm



- GATE DRAIN (HEAT SINK) SOURCE
- **JEDEC EIAJ** SC-64 TOSHIBA 2-7B1B



- 1. GATE
- 2. DRAIN (HEAT SINK)
- 3. SOURCE

JEDEC	_	
EIAJ	SC-64	
TOSHIBA	2-7B2B	

Weight: 0.36g

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ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACT	ERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage C	urrent	IGSS	$V_{GS} = \pm 16V, V_{DS} = 0V$	_	_	±10	μ A
Drain Cut-off Cu	ırrent	$I_{ m DSS}$	$V_{DS} = -60V, V_{GS} = 0V$	<u> </u>	_	-100	μ A
Drain-Source Breakdown Volt	age	V _{(BR)DSS}	$I_D = -10 \text{mA}, V_{GS} = 0 \text{V}$	-60	_	_	V
Gate Threshold	Voltage	$ m V_{th}$	$V_{DS} = -10V, I_D = -1mA$	-0.8	_	-2.0	V
Drain-Source ON	N Resistance	R _{DS(ON)}	$V_{GS} = -4V, I_D = -2.5A$ $V_{GS} = -10V, I_D = -2.5A$	_	0.31	0.40	Ω
Forward Transfe	r Admittance	Y _{fs}	$V_{DS} = -10V, I_{D} = -2.5A$	1.8	3.0	-	S
Input Capacitance Cia			_	500	_		
Reverse Transfer Capacitance		C_{rss}	$V_{DS} = -10V, V_{GS} = 0V,$ f=1MHz	_	90	_	pF
Output Capacitance		C _{oss}		_	290	_	
Switching Time	Rise Time		$V_{GS} \overset{OV}{\overset{I}{\overset{D}{\overset{D}{\overset{D}{\overset{C}{\overset{C}{\overset{C}{\overset{C}{C$	_	20	_	
	Turn-on Time	$t_{\mathbf{on}}$			30	_	ns
	Fall Time	t_f		_	30	_	
	Turn-off Time	${ m t}_{ m off}$		_	140		
Total Gate Charge (Gate-Source Plus Gate-Drain)		$\mathbf{Q}_{\mathbf{g}}$	$V_{DD} = -48V, V_{GS} = -10V,$	_	20	_	
Gate-Source Charge		$\mathbf{Q}_{\mathbf{g}\mathbf{s}}$	$I_D = -5A$	_	13	_	nC
Gate-Drain ("Miller") Charge		$\mathbf{Q}_{ ext{gd}}$		_	7	_	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	$I_{ m DR}$	_	_	_	-5	A
Pulse Drain Reverse Current	$I_{ m DRP}$	_	_	_	-20	A
Diode Forward Voltage	$v_{ m DSF}$	$I_{DR} = -5A$, $V_{GS} = 0V$	_	_	1.5	V

MARKING

