

July 2007

## MPSA13

## **NPN Darlington Transistor**

- This device is designed for applications requiring extremely high Current gain at collector Currents to 1.0A.
- Sourced from process 05.



1. Emitter 2. Base 3. Collector

## Absolute Maximum Ratings T<sub>a</sub> = 25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V <sub>CES</sub>	Collector-Emitter Voltage	30	V	
V <sub>CBO</sub>	Collector-Base Voltage	30	V	
V <sub>EBO</sub>	Emitter-Base Voltage	10	V	
I <sub>C</sub>	Collector Current - Continuous	1.2	А	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C	

## **Electrical Characteristics** T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units			
Off Charac	Off Characteristics							
V <sub>(BR)CES</sub>	Collector-Emitter Breakdown Voltage	$I_C = 100 \mu A, I_B = 0$	30		V			
I <sub>CBO</sub>	Collector-Cutoff Current	$V_{CB} = 30V, I_{E} = 0$		100	nA			
I <sub>EBO</sub>	Emitter-Cutoff Current	V <sub>EB</sub> = 10V, I <sub>C</sub> = 0		100	nA			
On Characteristics *								
h <sub>FE</sub>	DC Current Gain	$V_{CE} = 5.0V, I_{C} = 10mA$ $V_{CE} = 5.0, I_{C} = 100mA$	5,000 10,000					
V <sub>CE (sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 100mA, I <sub>B</sub> = 0.1mA		1.5	V			
V <sub>BE (on)</sub>	Base-Emitter On Voltage	$I_C = 100 \text{mA}, V_{CE} = 5.0 \text{V}$		2.0	V			
Small Signal Characteristics								
f <sub>T</sub>	Current Gain Bandwidth Product	$I_C = 10 \text{mA}, V_{CE} = 10 \text{V}, f = 100 \text{MHz}$	125		pF			

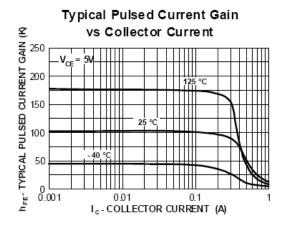
 $<sup>^{\</sup>star}$  Pulse Test: Pulse Width≤300  $\mu s,$  Duty Cycle≤2%

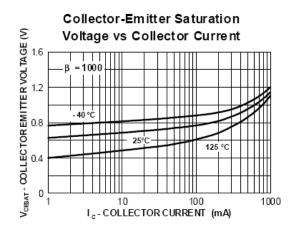
#### Thermal Characteristics T<sub>a</sub>=25°C unless otherwise noted

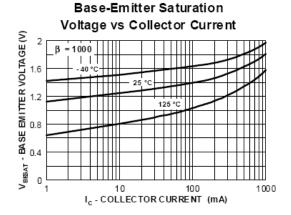
Symbol	Parameter	Max.	Units
P <sub>D</sub>	Total Device Dissipation Derate above 25°C	625 5.0	mW mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	83.3	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	°C/W

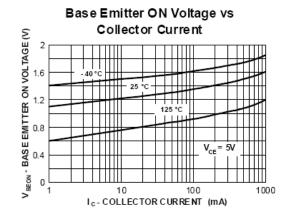
<sup>\*</sup> Device mounted on FR-4PCB 1.6"  $\times$  1.6"  $\times$  0.06".

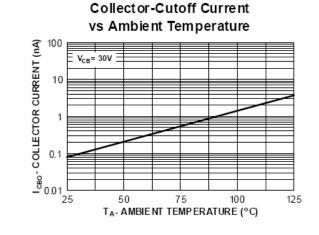
#### **Typical Characteristics**

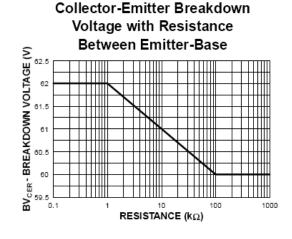






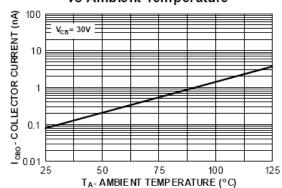




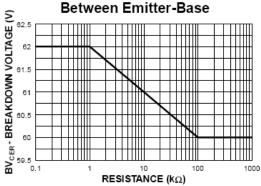


## Typical Characteristics (continued)

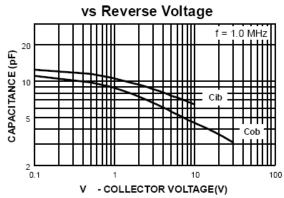
## Collector-Cutoff Current vs Ambient Temperature



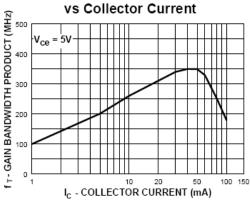
# Collector-Emitter Breakdown Voltage with Resistance



## Input and Output Capacitance

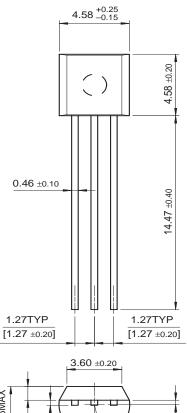


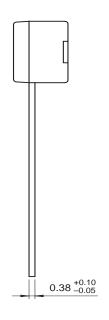
## Gain Bandwidth Product

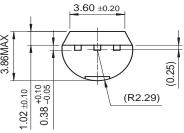


## **Mechanical Dimensions**

TO-92







Dimensions in Millimeters





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