2N5465

CASE 29-04, STYLE 7 TO-92 (TO-226AA)



**JFET AMPLIFIERS** 

P-CHANNEL - DEPLETION

## MAXIMUM RATINGS

Rating	Symbol	2N5460 2N5461 2N5462	2N5463 2N5464 2N5465	Unit
Drain-Gate Voltage	V <sub>DG</sub>	40	60	Vdc
Reverse Gate-Source Voltage	VGSR	40	60	Vdc
Forward Gate Current	IG(f)	10		mAdc
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	PD	3 - 2,	mW mW/°C	
Junction Temperature Range	TJ	-65 to +135		°C
Storage Channel Temperature Range	T <sub>stg</sub>	−65 to	+150	°C

ELECTRICAL CHARACTERISTICS (TA = 25°	C unless otherwise noted.)	,				
Characteristic		Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Gate-Source Breakdown Voltage (IG = 10 μAdc, V <sub>DS</sub> = 0)	2N5460, 2N5461, 2N5462 2N5463, 2N5464, 2N5465	V(BR)GSS	40 60	1-1	<del>-</del>	Vdc
Gate Reverse Current (VGS = 20 Vdc, VDS = 0) (VGS = 30 Vdc, VDS = 0) (VGS = 20 Vdc, VDS = 0, TA = 100°C) (VGS = 30 Vdc, VDS = 0, TA = 100°C)	2N5460, 2N5461, 2N5462 2N5463, 2N5464, 2N5465 2N5460, 2N5461, 2N5462 2N5463, 2N5464, 2N5465	<sup>1</sup> GSS		1111	5.0 5.0 1.0 1.0	nAdc µAdc
Gate Source Cutoff Voltage (VDS = 15 Vdc, I <sub>D</sub> = 1.0 μAdc)	2N5460, 2N5463 2N5461, 2N5464 2N5462, 2N5465	VGS(off)	0.75 1.0 1.8	) <u>-</u>	6.0 7.5 9.0	Vdc
Gate Source Voltage $\{V_{DS}=15\ Vdc, I_{D}=0.1\ mAdc\}$ $\{V_{DS}=15\ Vdc, I_{D}=0.2\ mAdc\}$ $\{V_{DS}=15\ Vdc, I_{D}=0.4\ mAdc\}$	2N5460, 2N5463 2N5461, 2N5464 2N5462, 2N5465	VGS	0.5 0.8 1.5	<u>-</u>	4.0 4.5 6.0	Vdc
ON CHARACTERISTICS					<b>,</b>	
Zero-Gate-Voltage Drain Current (VDS = 15 Vdc, VGS = 0, f = 1.0 kHz)	2N5460, 2N5463 2N5461, 2N5464 2N5462, 2N5465	IDSS	-1.0 -2.0 -4.0	_ _ _	-5.0 -9.0 -16	mAdc
SMALL-SIGNAL CHARACTERISTICS						
Forward Transfer Admittance (VDS = 15 Vdc, VGS = 0, f = 1.0 kHz)	2N5460, 2N5463 2N5461, 2N5464 2N5462, 2N5465	lYfs	1000 1500 2000		4000 5000 6000	μmhos
Output Admittance (VDS = 15 Vdc, VGS = 0, f = 1.0 kHz)		lyosl	_	_	75	μmhos
Input Capacitance (VDS = 15 Vdc, VGS = 0, f = 1.0 MHz)		Ciss	_	5.0	7.0	pF
Reverse Transfer Capacitance (VDS = 15 Vdc, VGS = 0, f = 1.0 MHz)	· · · · · · · · · · · · · · · · · · ·	C <sub>rss</sub>		1.0	2.0	pF
FUNCTIONAL CHARACTERISTICS	<del> </del>			r		1.
Noise Figure (V <sub>DS</sub> = 15 Vdc, V <sub>GS</sub> = 0, R <sub>G</sub> = 1.0 Megohm	n, f = 100 Hz, BW = 1.0 Hz)	NF		1.0	2.5	dB
Equivalent Short-Circuit Input Noise Voltage (VDS = 15 Vdc, VGS = 0, f = 100 Hz, BW =	1.0 Hz)	en		60	115	nV/√Hz
MOTOROLA SM	MALL-SIGNAL TRANSISTO	ORS, FETs A	AND DIOE	ES		
	4-44					dB nV/√Hz
						D'
					neri	
Elcodis.com electronic components distril	butor				*	

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2N5460 thru 2N5465

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# DRAIN CURRENT versus GATE SOURCE VOLTAGE

FIGURE 1 - VGS(off) = 2.0 VOLTS

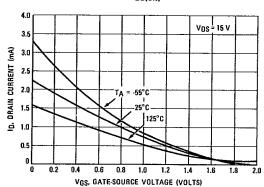


FIGURE 2 - VGS(off) = 4.0 VOLTS

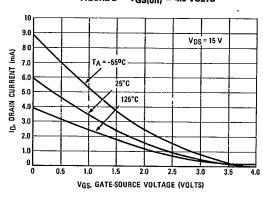
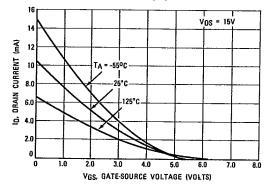


FIGURE 3 - VGS(off) = 5.0 VOLTS



FORWARD TRANSFER ADMITTANCE versus DRAIN CURRENT

FIGURE 4 --- VGS(off) = 2.0 VOLTS

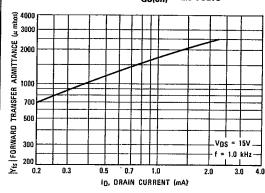


FIGURE 5 -- VGS(off) = 4.0 VOLTS

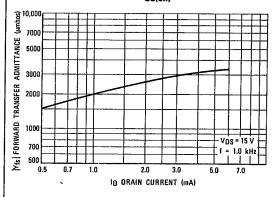
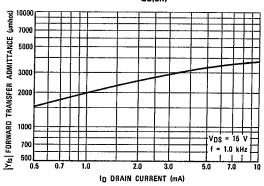
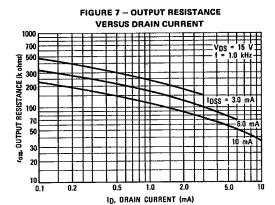


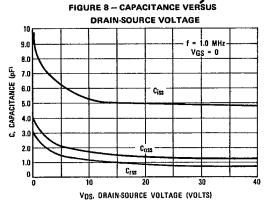
FIGURE 6 - VGS(off) = 5.0 VOLTS

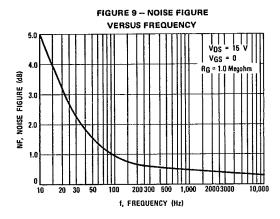


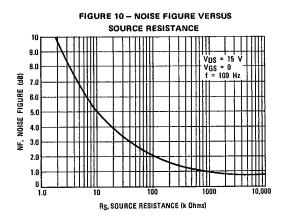
MOTOROLA SMALL-SIGNAL TRANSISTORS, FETs AND DIODES

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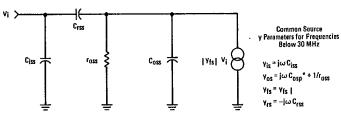








### FIGURE 11 - EQUIVALENT LOW FREQUENCY CIRCUIT



\*Cosp is Coss in parallel with Series Combination of Ciss and Crss.

### NOTE:

Graphical data is presented for dc conditions. Tabular data is given for pulsed conditions (Pulse Width = 630 ms, Duty Cycle = 10%).