## MC74LCX245

## Low-Voltage CMOS Octal Transceiver

## With 5 V-Tolerant Inputs and Outputs (3-State, Inverting)

The MC74LCX245 is a high performance, non-inverting octal transceiver operating from a 2.3 to 3.6 V supply. High impedance TTL compatible inputs significantly reduce current loading to input drivers while TTL compatible outputs offer improved switching noise performance. A $\mathrm{V}_{\mathrm{I}}$ specification of 5.5 V allows MC74LCX245 inputs to be safely driven from 5 V devices. The MC74LCX245 is suitable for memory address driving and all TTL level bus oriented transceiver applications.

Current drive capability is 24 mA at both A and B ports. The Transmit/Receive (T/R) input determines the direction of data flow through the bi-directional transceiver. Transmit (active-HIGH) enables data from A ports to B ports; Receive (active-LOW) enables data from B to A ports. The Output Enable input, when HIGH, disables both A and B ports by placing them in a HIGH Z condition.

- Designed for 2.3 to 3.6 V VCC Operation
- 5 V Tolerant - Interface Capability With 5 V TTL Logic
- Supports Live Insertion and Withdrawal
- IOFF Specification Guarantees High Impedance When $\mathrm{V}_{\mathrm{CC}}=0 \mathrm{~V}$
- LVTTL Compatible
- LVCMOS Compatible
- 24 mA Balanced Output Sink and Source Capability
- Near Zero Static Supply Current in All Three Logic States (10 $\mu \mathrm{A}$ ) Substantially Reduces System Power Requirements
- Latchup Performance Exceeds 500 mA
- ESD Performance: Human Body Model >2000 V; Machine Model >200 V

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$\begin{array}{ll}\text { A } & =\text { Assembly Location } \\ \text { L, WL } & =\text { Wafer Lot } \\ \text { Y, YY } & =\text { Year } \\ \text { W, WW } & \text { Work Week }\end{array}$

ORDERING INFORMATION

| Device | Package | Shipping |
| :--- | :---: | :---: |
| MC74LCX245DW | SO-20 | 38 Units/Rail |
| MC74LCX245DWR2 | SO-20 | 1000 Units/Reel |
| MC74LCX245DT | TSSOP-20 | 75 Units/Rail |
| MC74LCX245DTEL | TSSOP-20 | 2000 Units/Reel |
| MC74LCX245DTR2 | TSSOP-20 | 2500 Units/Reel |
| MC74LCX245M | SO <br> EIAJ-20 | 40 Units/Rail |
| MC74LCX245MEL | SO <br> EIAJ-20 | 2000 Units/Reel |



Figure 1. Pinout (Top View)

PIN NAMES

| PINS | FUNCTION |
| :--- | :--- |
| $\overline{\mathrm{OE}}$ | Output Enable Input |
| $\mathrm{T} / \overline{\mathrm{R}}$ | Transmit/Receive Input |
| A0-A7 | Side A 3-State Inputs or 3-State Outputs |
| B0-B7 | Side B 3-State Inputs or 3-StateOutputs |

TRUTH TABLE

| INPUTS |  | OPERATING MODE <br> Non-Inverting |
| :---: | :---: | :---: |
| $\overline{\mathrm{OE}}$ | $\mathrm{T} / \overline{\mathbf{R}}$ |  |
| L | L | A Data to B Bus |
| L | H | Z |
| H | X |  |

H = High Voltage Level
L = Low Voltage Level
Z = High Impedance State
$\mathrm{X}=$ High or Low Voltage Level and Transitions are Acceptable For ICC reasons, Do Not Float Inputs


Figure 2. Logic Diagram

ABSOLUTE MAXIMUM RATINGS*

| Symbol | Parameter | Value | Condition | Unit |
| :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\mathrm{CC}}$ | DC Supply Voltage | -0.5 to +7.0 |  | V |
| $V_{1}$ | DC Input Voltage | $-0.5 \leq \mathrm{V}_{1} \leq+7.0$ |  | V |
| $\mathrm{V}_{\mathrm{O}}$ | DC Output Voltage | $-0.5 \leq \mathrm{V}_{\mathrm{O}} \leq+7.0$ | Output in 3-State | V |
|  |  | $-0.5 \leq \mathrm{V}_{\mathrm{O}} \leq \mathrm{V}_{\mathrm{CC}}+0.5$ | Output in HIGH or LOW State (Note 1.) | V |
| IIK | DC Input Diode Current | -50 | $\mathrm{V}_{1}<\mathrm{GND}$ | mA |
| IOK | DC Output Diode Current | -50 | $\mathrm{V}_{\mathrm{O}}<$ GND | mA |
|  |  | +50 | $\mathrm{V}_{\mathrm{O}}>\mathrm{V}_{\mathrm{CC}}$ | mA |
| Io | DC Output Source/Sink Current | $\pm 50$ |  | mA |
| ICC | DC Supply Current Per Supply Pin | $\pm 100$ |  | mA |
| IGND | DC Ground Current Per Ground Pin | $\pm 100$ |  | mA |
| TSTG | Storage Temperature Range | -65 to +150 |  | ${ }^{\circ} \mathrm{C}$ |

* Absolute maximum continuous ratings are those values beyond which damage to the device may occur. Exposure to these conditions or conditions beyond those indicated may adversely affect device reliability. Functional operation under absolute-maximum-rated conditions is not implied.

1. Io absolute maximum rating must be observed.

RECOMMENDED OPERATING CONDITIONS

| Symbol | Parameter | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\mathrm{CC}}$ | Supply Voltage <br> Operating Data Retention Only | $\begin{aligned} & 2.0 \\ & 1.5 \end{aligned}$ | $\begin{aligned} & 2.5,3.3 \\ & 2.5,3.3 \end{aligned}$ | $\begin{aligned} & 3.6 \\ & 3.6 \end{aligned}$ | V |
| $\mathrm{V}_{1}$ | Input Voltage | 0 |  | 5.5 | V |
| $\mathrm{V}_{\mathrm{O}}$ | Output Voltage <br> (HIGH or LOW State) <br> (3-State) | $\begin{aligned} & 0 \\ & 0 \end{aligned}$ |  | $\begin{gathered} \mathrm{V}_{\mathrm{CC}} \\ 5.5 \end{gathered}$ | V |
| IOH | HIGH Level Output Current $\begin{aligned} & \mathrm{V}_{C C}=3.0 \mathrm{~V}-3.6 \mathrm{~V} \\ & \mathrm{~V}_{\mathrm{CC}}=2.7 \mathrm{~V}-3.0 \mathrm{~V} \\ & \mathrm{~V}_{\mathrm{CC}}=2.3 \mathrm{~V}-2.7 \mathrm{~V} \end{aligned}$ |  |  | $\begin{gathered} \hline-24 \\ -12 \\ -8 \end{gathered}$ | mA |
| IOL | LOW Level Output Current $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=3.0 \mathrm{~V}-3.6 \mathrm{~V} \\ & \mathrm{~V}_{\mathrm{CC}}=2.7 \mathrm{~V}-3.0 \mathrm{~V} \\ & \mathrm{~V}_{\mathrm{CC}}=2.3 \mathrm{~V}-2.7 \mathrm{~V} \end{aligned}$ |  |  | $\begin{gathered} +24 \\ +12 \\ +8 \end{gathered}$ | mA |
| $\mathrm{T}_{\mathrm{A}}$ | Operating Free-Air Temperature | -40 |  | +85 | ${ }^{\circ} \mathrm{C}$ |
| $\Delta t / \Delta \mathrm{V}$ | Input Transition Rise or Fall Rate, $\mathrm{V}_{\text {IN }}$ from 0.8 V to 2.0 V , $\mathrm{V}_{\mathrm{CC}}=3.0 \mathrm{~V}$ | 0 |  | 10 | ns/V |

DC ELECTRICAL CHARACTERISTICS

| Symbol | Characteristic | Condition | $\mathrm{T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min | Max |  |
| $\mathrm{V}_{\mathrm{IH}}$ | HIGH Level Input Voltage (Note 2.) | $2.3 \mathrm{~V} \leq \mathrm{V}_{\mathrm{CC}} \leq 2.7 \mathrm{~V}$ | 1.7 |  | V |
|  |  | $2.7 \mathrm{~V} \leq \mathrm{V}_{\mathrm{CC}} \leq 3.6 \mathrm{~V}$ | 2.0 |  |  |
| $\mathrm{V}_{\text {IL }}$ | LOW Level Input Voltage (Note 2.) | $2.3 \mathrm{~V} \leq \mathrm{V}_{\mathrm{CC}} \leq 2.7 \mathrm{~V}$ |  | 0.7 | V |
|  |  | $2.7 \mathrm{~V} \leq \mathrm{V}_{\mathrm{CC}} \leq 3.6 \mathrm{~V}$ |  | 0.8 |  |
| $\mathrm{V}_{\mathrm{OH}}$ | HIGH Level Output Voltage | $2.3 \mathrm{~V} \leq \mathrm{V}_{\mathrm{CC}} \leq 3.6 \mathrm{~V}$; $\mathrm{OLL}=100 \mu \mathrm{~A}$ | $\mathrm{V}_{\mathrm{CC}}-0.2$ |  | V |
|  |  | $\mathrm{V}_{\mathrm{CC}}=2.3 \mathrm{~V} ; \mathrm{IOH}=-8 \mathrm{~mA}$ | 1.8 |  |  |
|  |  | $\mathrm{V}_{\mathrm{CC}}=2.7 \mathrm{~V} ; \mathrm{IOH}=-12 \mathrm{~mA}$ | 2.2 |  |  |
|  |  | $\mathrm{V}_{\mathrm{CC}}=3.0 \mathrm{~V} ; \mathrm{IOH}=-18 \mathrm{~mA}$ | 2.4 |  |  |
|  |  | $\mathrm{V}_{\mathrm{CC}}=3.0 \mathrm{~V} ; \mathrm{IOH}=-24 \mathrm{~mA}$ | 2.2 |  |  |
| V OL | LOW Level Output Voltage | $2.3 \mathrm{~V} \leq \mathrm{V}_{\mathrm{CC}} \leq 3.6 \mathrm{~V}$; $\mathrm{IOL}=100 \mu \mathrm{~A}$ |  | 0.2 | V |
|  |  | $\mathrm{V}_{\mathrm{CC}}=2.3 \mathrm{~V} ; \mathrm{l}^{\text {OL }}=8 \mathrm{~mA}$ |  | 0.6 |  |
|  |  | $\mathrm{V}_{\mathrm{CC}}=2.7 \mathrm{~V} ; \mathrm{IOL}=12 \mathrm{~mA}$ |  | 0.4 |  |
|  |  | $\mathrm{V}_{\mathrm{CC}}=3.0 \mathrm{~V}$; $\mathrm{IOL}=16 \mathrm{~mA}$ |  | 0.4 |  |
|  |  | $\mathrm{V}_{\mathrm{CC}}=3.0 \mathrm{~V}$; $\mathrm{IOL}=24 \mathrm{~mA}$ |  | 0.55 |  |
| 1 | Input Leakage Current | $2.3 \mathrm{~V} \leq \mathrm{V}_{\mathrm{CC}} \leq 3.6 \mathrm{~V} ; 0 \mathrm{~V} \leq \mathrm{V}_{\mathrm{I}} \leq 5.5 \mathrm{~V}$ |  | $\pm 5$ | $\mu \mathrm{A}$ |
| IOZ | 3-State Output Current | $\begin{gathered} 2.3 \leq \mathrm{V}_{\mathrm{CC}} \leq 3.6 \mathrm{~V} ; 0 \mathrm{~V} \leq \mathrm{V}_{\mathrm{O}} \leq 5.5 \mathrm{~V} ; \\ \mathrm{V}_{\mathrm{I}}=\mathrm{V}_{\mathrm{IH}} \text { or } \mathrm{V}_{\text {IL }} \end{gathered}$ |  | $\pm 5$ | $\mu \mathrm{A}$ |
| IOFF | Power-Off Leakage Current | $\mathrm{V}_{\mathrm{CC}}=0 \mathrm{~V}$; $\mathrm{V}_{\text {I }}$ or $\mathrm{V}_{\mathrm{O}}=5.5 \mathrm{~V}$ |  | 10 | $\mu \mathrm{A}$ |
| ICC | Quiescent Supply Current | $2.3 \leq \mathrm{V}_{\mathrm{CC}} \leq 3.6 \mathrm{~V} ; \mathrm{V}_{\mathrm{I}}=\mathrm{GND}$ or $\mathrm{V}_{\mathrm{CC}}$ |  | 10 | $\mu \mathrm{A}$ |
|  |  | $2.3 \leq \mathrm{VCC} \leq 3.6 \mathrm{~V} ; 3.6 \leq \mathrm{V}_{1}$ or $\mathrm{V}_{\mathrm{O}} \leq 5.5 \mathrm{~V}$ |  | $\pm 10$ |  |
| $\Delta \mathrm{CCC}$ | Increase in ICC per Input | $2.3 \leq \mathrm{V}_{\mathrm{CC}} \leq 3.6 \mathrm{~V} ; \mathrm{V}_{\text {IH }}=\mathrm{V}_{\mathrm{CC}}-0.6 \mathrm{~V}$ |  | 500 | $\mu \mathrm{A}$ |

2. These values of $V_{l}$ are used to test $D C$ electrical characteristics only.

AC CHARACTERISTICS $t_{R}=t_{F}=2.5 \mathrm{~ns} ; \mathrm{R}_{\mathrm{L}}=500 \Omega$

| Symbol | Parameter | Waveform | Limits |  |  |  |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | $\mathrm{T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |  |  |  |  |  |  |
|  |  |  | $\mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V} \pm 0.3 \mathrm{~V}$ |  | $\mathrm{V}_{\mathrm{CC}}=2.7 \mathrm{~V}$ |  | $\mathrm{V}_{\mathrm{CC}}=2.5 \mathrm{~V} \pm 0.2 \mathrm{~V}$ |  |  |
|  |  |  | $\mathrm{C}_{\mathrm{L}}=50 \mathrm{pF}$ |  | $\mathrm{C}_{\mathrm{L}}=50 \mathrm{pF}$ |  | $\mathrm{C}_{\mathrm{L}}=30 \mathrm{pF}$ |  |  |
|  |  |  | Min | Max | Min | Max | Min | Max |  |
| $\begin{aligned} & \text { tPLH } \\ & \text { tPHL } \end{aligned}$ | Propagation Delay Input to Output | 1 | $\begin{aligned} & 1.5 \\ & 1.5 \end{aligned}$ | $\begin{aligned} & 7.0 \\ & 7.0 \end{aligned}$ | $\begin{aligned} & 1.5 \\ & 1.5 \end{aligned}$ | $\begin{aligned} & 8.0 \\ & 8.0 \end{aligned}$ | $\begin{aligned} & 1.5 \\ & 1.5 \end{aligned}$ | $\begin{aligned} & 8.4 \\ & 8.4 \end{aligned}$ | ns |
| $\begin{aligned} & \text { tPZH } \\ & \text { tPZL } \end{aligned}$ | Output Enable Time to High and Low Level | 2 | $\begin{aligned} & 1.5 \\ & 1.5 \end{aligned}$ | $\begin{aligned} & 8.5 \\ & 8.5 \end{aligned}$ | $\begin{aligned} & 1.5 \\ & 1.5 \end{aligned}$ | $\begin{aligned} & 9.5 \\ & 9.5 \end{aligned}$ | $\begin{aligned} & 1.5 \\ & 1.5 \end{aligned}$ | $\begin{aligned} & 10.5 \\ & 10.5 \end{aligned}$ | ns |
| tpHZ tPLZ | Output Disable Time From High and Low Level | 2 | $\begin{aligned} & 1.5 \\ & 1.5 \end{aligned}$ | $\begin{aligned} & 7.5 \\ & 7.5 \end{aligned}$ | $\begin{aligned} & 1.5 \\ & 1.5 \end{aligned}$ | $\begin{aligned} & 8.5 \\ & 8.5 \end{aligned}$ | $\begin{aligned} & 1.5 \\ & 1.5 \end{aligned}$ | $\begin{aligned} & 9.0 \\ & 9.0 \end{aligned}$ | ns |
| $\begin{aligned} & \text { toSHL } \\ & \text { toSLH } \\ & \hline \end{aligned}$ | Output-to-Output Skew (Note 3.) |  |  | $\begin{aligned} & 1.0 \\ & 1.0 \end{aligned}$ |  |  |  |  | ns |

3. Skew is defined as the absolute value of the difference between the actual propagation delay for any two separate outputs of the same device. The specification applies to any outputs switching in the same direction, either HIGH-to-LOW (tOSHL) or LOW-to-HIGH (tOSLH); parameter guaranteed by design.

## MC74LCX245

DYNAMIC SWITCHING CHARACTERISTICS

| Symbol | Characteristic | Condition | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min | Typ | Max |  |
| VOLP | Dynamic LOW Peak Voltage <br> (Note 4.) | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V}, \mathrm{C}_{\mathrm{L}}=50 \mathrm{pF}, \mathrm{~V}_{\mathrm{IH}}=3.3 \mathrm{~V}, \mathrm{~V}_{\mathrm{IL}}=0 \mathrm{~V} \\ & \mathrm{~V}_{\mathrm{CC}}=2.5 \mathrm{~V}, \mathrm{C}_{\mathrm{L}}=30 \mathrm{pF}, \mathrm{~V}_{\mathrm{IH}}=2.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{IL}}=0 \mathrm{~V} \end{aligned}$ |  | $\begin{aligned} & 0.8 \\ & 0.6 \end{aligned}$ |  | $\begin{aligned} & \text { V } \\ & \text { V } \end{aligned}$ |
| V OLV | Dynamic LOW Valley Voltage <br> (Note 4.) | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V}, \mathrm{C}_{\mathrm{L}}=50 \mathrm{pF}, \mathrm{~V}_{\mathrm{IH}}=3.3 \mathrm{~V}, \mathrm{~V}_{\mathrm{IL}}=0 \mathrm{~V} \\ & \mathrm{~V}_{\mathrm{CC}}=2.5 \mathrm{~V}, \mathrm{C}_{\mathrm{L}}=30 \mathrm{pF}, \mathrm{~V}_{\mathrm{IH}}=2.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{IL}}=0 \mathrm{~V} \end{aligned}$ |  | $\begin{aligned} & -0.8 \\ & -0.6 \end{aligned}$ |  | V |

4. Number of outputs defined as " $n$ ". Measured with " $n-1$ " outputs switching from HIGH-to-LOW or LOW-to-HIGH. The remaining output is measured in the LOW state.

CAPACITIVE CHARACTERISTICS

| Symbol | Parameter | Condition | Typical | Unit |
| :--- | :--- | :---: | :---: | :---: |
| $\mathrm{C}_{\mathrm{IN}}$ | Input Capacitance | $\mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V}, \mathrm{~V}_{\mathrm{I}}=0 \mathrm{~V}$ or $\mathrm{V}_{\mathrm{CC}}$ | 7 | pF |
| $\mathrm{C}_{\mathrm{I} / \mathrm{O}}$ | Input/Output Capacitance | $\mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V}, \mathrm{~V}_{\mathrm{I}}=0 \mathrm{~V}$ or $\mathrm{V}_{\mathrm{CC}}$ | 8 | pF |
| $\mathrm{C}_{\mathrm{PD}}$ | Power Dissipation Capacitance | $10 \mathrm{MHz}, \mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V}, \mathrm{~V}_{\mathrm{I}}=0 \mathrm{~V}$ or $\mathrm{V}_{\mathrm{CC}}$ | pF |  |



|  | $\mathrm{V}_{\mathbf{C C}}$ |  |  |
| :---: | :---: | :---: | :---: |
| Symbol | $\mathbf{3 . 3} \mathrm{V} \pm \mathbf{0 . 3} \mathrm{V}$ | $\mathbf{2 . 7} \mathrm{V}$ | $\mathbf{2 . 5} \mathrm{V} \pm \mathbf{0 . 2} \mathrm{V}$ |
| $\mathrm{Vmi}^{2}$ | 1.5 V | 1.5 V | $\mathrm{~V}_{\mathrm{CC}} / 2$ |
| $\mathrm{Vmo}^{2}$ | 1.5 V | 1.5 V | $\mathrm{~V}_{\mathrm{CC}} / 2$ |
| $\mathrm{~V}_{\mathrm{HZ}}$ | $\mathrm{V}_{\mathrm{OL}}+0.3 \mathrm{~V}$ | $\mathrm{~V}_{\mathrm{OL}}+0.3 \mathrm{~V}$ | $\mathrm{~V}_{\mathrm{OL}}+0.15 \mathrm{~V}$ |
| $\mathrm{~V}_{\mathrm{LZ}}$ | $\mathrm{V}_{\mathrm{OH}}-0.3 \mathrm{~V}$ | $\mathrm{~V}_{\mathrm{OH}}-0.3 \mathrm{~V}$ | $\mathrm{~V}_{\mathrm{OH}}-015 \mathrm{~V}$ |

Figure 3. AC Waveforms

## MC74LCX245



| TEST | SWITCH |
| :--- | :---: |
| tPLH, tPHL | Open |
| tPZL, tPLZ | 6 V at $\mathrm{V}_{\mathrm{CC}}=3.3 \pm 0.3 \mathrm{~V}$ |
|  | 6 V at $\mathrm{CCC}_{\mathrm{C}}=2.5 \pm 0.2 \mathrm{~V}$ |
| Open Collector/Drain tPLH and tPHL | 6 V |
| tPZH, tPHZ | GND |

$\mathrm{C}_{\mathrm{L}}=50 \mathrm{pF}$ at $\mathrm{V}_{\mathrm{CC}}=3.3 \pm 0.3 \mathrm{~V}$ or equivalent (includes jig and probe capacitance)
$C_{L}=30 \mathrm{pF}$ at $\mathrm{V}_{\mathrm{CC}}=2.5 \pm 0.2 \mathrm{~V}$ or equivalent (includes jig and probe capacitance)
$R_{L}=R_{1}=500 \Omega$ or equivalent
$\mathrm{R}_{\mathrm{T}}=\mathrm{Z}_{\mathrm{OUT}}$ of pulse generator (typically $50 \Omega$ )

Figure 4. Test Circuit

## PACKAGE DIMENSIONS

SO-20
DW SUFFIX
CASE 751D-05
ISSUE F


TSSOP-20
DT SUFFIX
CASE 948E-02
ISSUE A


## PACKAGE DIMENSIONS

SO EIAJ-20 M SUFFIX<br>CASE 967-01 ISSUE O

NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS AND ARE MEASURED AT THE PARTING LINE. MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
4. TERMINAL NUMBERS ARE SHOWN FOR REFERENCE ONLY.
5. THE LEAD WIDTH DIMENSION (b) DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 (0.003) TOTAL IN EXCESS OF THE LEAD WIDTH DIMENSION AT MAXIMUM MATERIAL CONDITION. DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OR THE FOOT. MINIMUM SPACE BETWEEN PROTRUSIONS AND ADJACENT LEAD TO BE 0.46 ( 0.018 ).

| DIM | MILLIMETERS |  | INCHES |  |
| :---: | :---: | :---: | :---: | :---: |
|  | MIN | MAX | MIN | MAX |
| A | --- | 2.05 | -- | 0.081 |
| $\mathrm{A}_{1}$ | 0.05 | 0.20 | 0.002 | 0.008 |
| b | 0.35 | 0.50 | 0.014 | 0.020 |
| c | 0.18 | 0.27 | 0.007 | 0.011 |
| D | 12.35 | 12.80 | 0.486 | 0.504 |
| E | 5.10 | 5.45 | 0.201 | 0.215 |
| e | 1.27 BSC |  | 0.050 BSC |  |
| $\mathrm{H}_{\mathrm{E}}$ | 7.40 | 8.20 | 0.291 | 0.323 |
| L | 0.50 | 0.85 | 0.020 | 0.033 |
| $\mathrm{L}_{\mathrm{E}}$ | 1.10 | 1.50 | 0.043 | 0.059 |
| M | $0^{\circ}$ | $10^{\circ}$ | $0^{\circ}$ | $10^{\circ}$ |
| $\mathrm{Q}_{1}$ | 0.70 | 0.90 | 0.028 | 0.035 |
| Z | --- | 0.81 | --- | 0.032 |


#### Abstract

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