



New Product

Si4908DY
Vishay Siliconix

Dual N-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ)
40	0.060 at V _{GS} = 10 V	5.0	5.6
	0.070 at V _{GS} = 4.5 V	4.7	

FEATURES

- TrenchFET® Power MOSFET
- 100 % R_g Tested

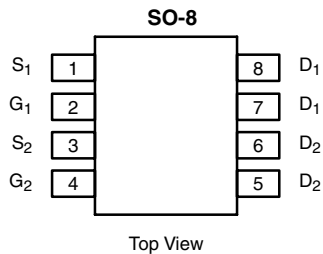
APPLICATIONS

- CCFL Inverter

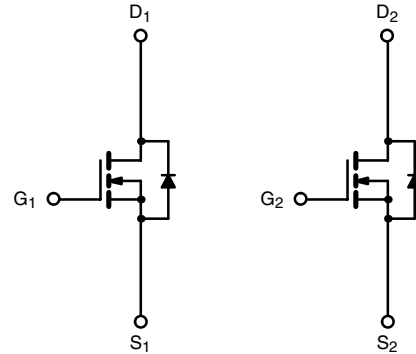


RoHS
COMPLIANT

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Ordering Information: Si4908DY-T1-E3 (Lead (Pb)-free)



N-Channel MOSFET

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED)			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	40	V
Gate-Source Voltage	V _{GS}	± 16	
Continuous Drain Current (T _J = 150 °C)	I _D	T _C = 25 °C	A
		T _C = 70 °C	
		T _A = 25 °C	
		T _A = 70 °C	
Pulsed Drain Current (10 μs Pulse Width)	I _{DM}	20	A
Source-Drain Current Diode Current	I _S	T _C = 25 °C	
		T _A = 25 °C	
Pulsed Source-Drain Current	I _{SM}	20	
Single Pulse Avalanche Current	I _{AS}	7	mJ
Single Pulse Avalanche Energy	E _{AS}	2.5	
Maximum Power Dissipation	P _D	T _C = 25 °C	W
		T _C = 70 °C	
		T _A = 25 °C	
		T _A = 70 °C	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Typ	Max	Unit
Maximum Junction-to-Ambient ^{b, d}	R _{thJA}	57	67.5	°C/W
Maximum Junction-to-Foot (Drain)	R _{thJF}	35	45	

Notes

- Based on T_C = 25 °C.
- Surface Mounted on 1" x 1" FR4 Board.
- t = 10 sec.
- Maximum under steady state conditions is 120 °C/W.

SPECIFICATIONS (T _J = 25 °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Condition	Min	Typ ^a	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	40			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA		40		
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J	I _D = 250 μA		-4.6		
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	0.8		2.2	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 16 V			100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 40 V, V _{GS} = 0 V			1	μA
		V _{DS} = 40 V, V _{GS} = 0 V, T _J = 55 °C			10	
On-State Drain Current ^b	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V	20			A
Drain-Source On-State Resistance ^b	r _{DS(on)}	V _{GS} = 10 V, I _D = 4.1 A		0.048	0.060	Ω
		V _{GS} = 4.5 V, I _D = 3.8 A		0.056	0.070	
Forward Transconductance ^b	g _{fs}	V _{DS} = 15 V, I _D = 4.1 A		15		S
Dynamic^a						
Input Capacitance	C _{iss}	N-Channel V _{DS} = 20 V, V _{GS} = 0 V, f = 1 MHz		355		pF
Output Capacitance	C _{oss}			50		
Reverse Transfer Capacitance	C _{rss}			29		
Total Gate Charge	Q _g	V _{DS} = 20 V, V _{GS} = 10 V, I _D = 5 A		8	12	nC
		N-Channel V _{DS} = 20 V, V _{GS} = 4.5 V, I _D = 5 A		3.7	6	
Gate-Source Charge	Q _{gs}			1.1		
Gate-Drain Charge	Q _{gd}			1.4		
Gate Resistance	R _g	f = 1 MHz		3.4	5.2	Ω
Turn-On Delay Time	t _{d(on)}	N-Channel V _{DD} = 20 V, R _L = 4 Ω I _D = 1 A, V _{GEN} = 10 V, R _g = 1 Ω		8	13	ns
Rise Time	t _r			20	30	
Turn-Off Delay Time	t _{d(off)}			23	35	
Fall Time	t _f			27	42	
Turn-On Delay Time	t _{d(on)}	N-Channel V _{DD} = 20 V, R _L = 4 Ω I _D = 1 A, V _{GEN} = 4.5 V, R _g = 1 Ω		74	110	
Rise Time	t _r			95	145	
Turn-Off Delay Time	t _{d(off)}			31	48	
Fall Time	t _f			33	50	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			2.3	A
Pulse Diode Forward Current ^a	I _{SM}				20	
Body Diode Voltage	V _{SD}	I _S = 1.5 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}	N-Channel I _F = 2 A, di/dt = 100 A/μs, T _J = 25 °C		26	40	ns
Body Diode Reverse Recovery Charge	Q _{rr}			26	40	nC
Reverse Recovery Fall Time	t _a			13		ns
Reverse Recovery Rise Time	t _b			13		

Notes

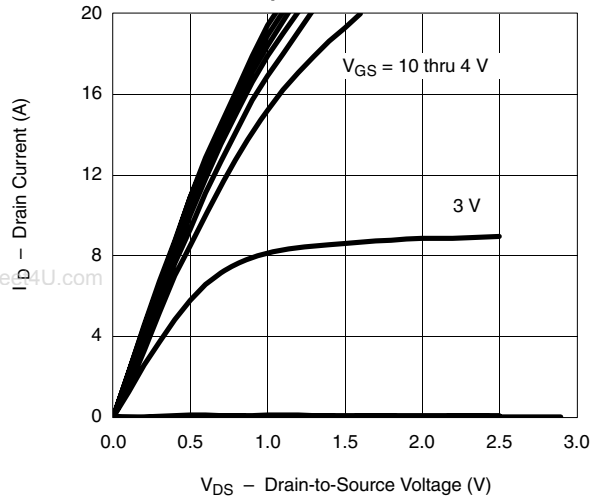
- a. Guaranteed by design, not subject to production testing.
b. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

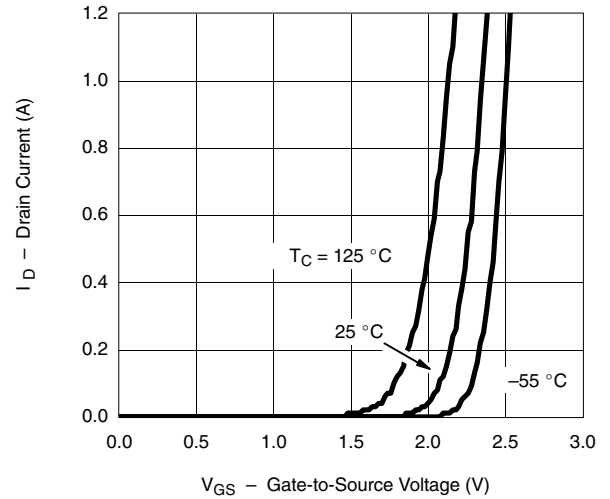


TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

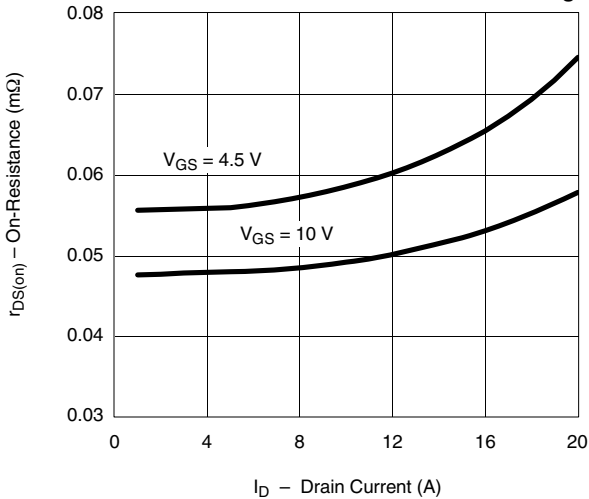
Output Characteristics



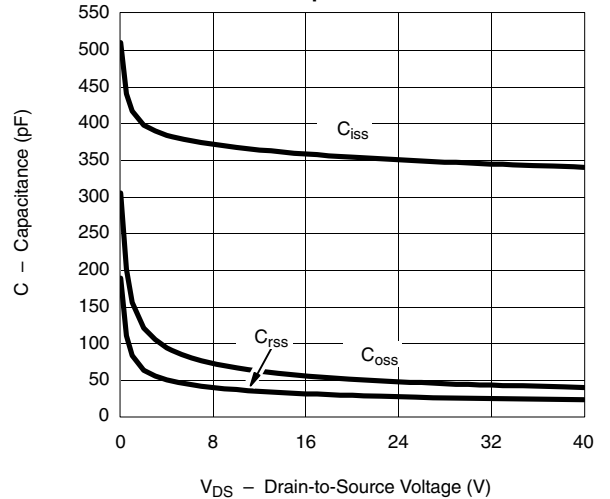
Transfer Characteristics



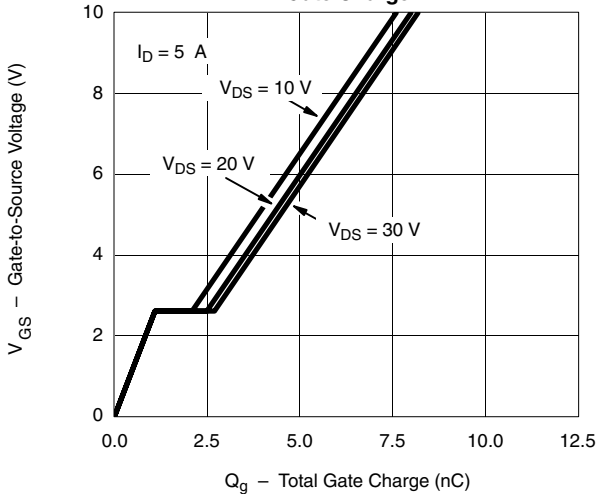
On-Resistance vs. Drain Current and Gate Voltage



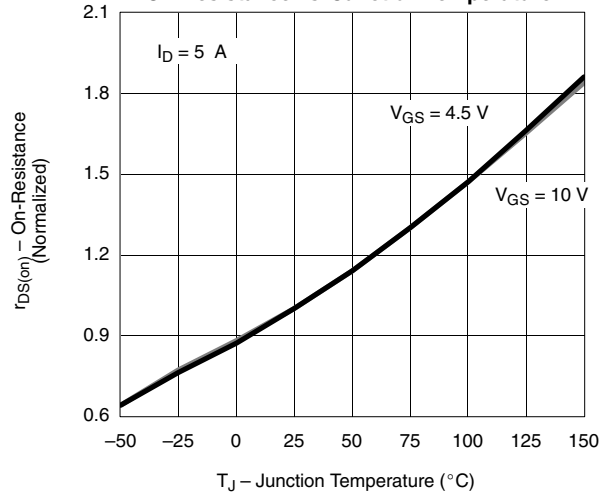
Capacitance



Gate Charge



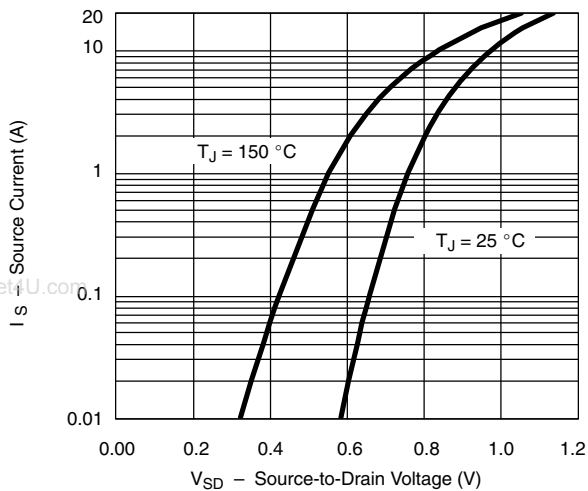
On-Resistance vs. Junction Temperature



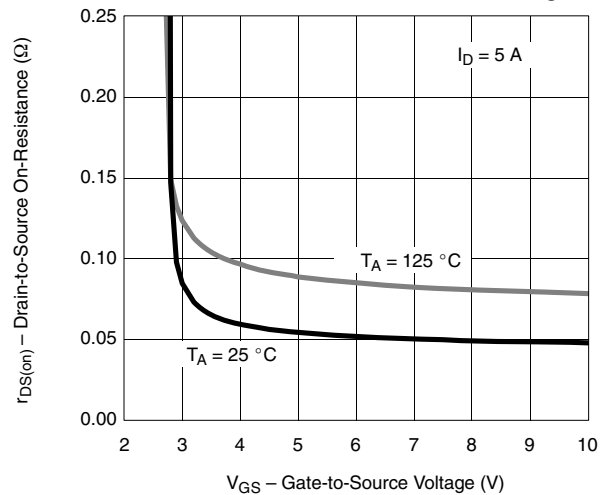


TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

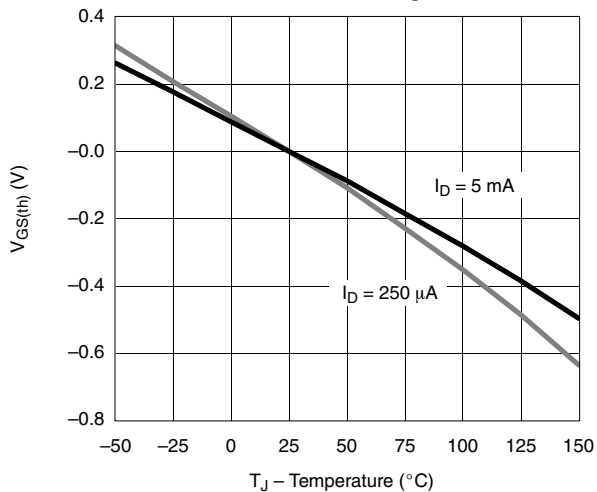
Source-Drain Diode Forward Voltage



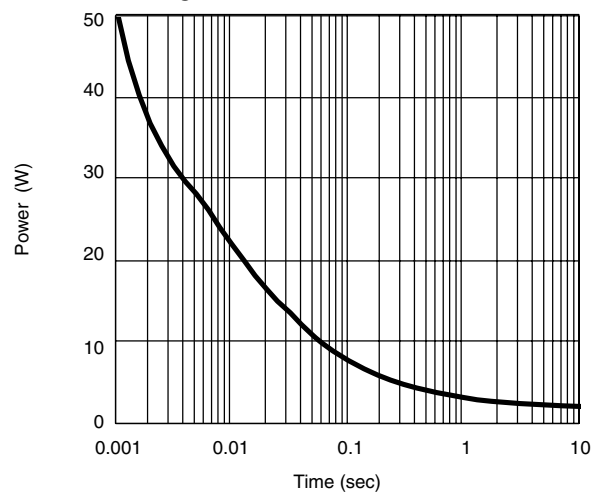
On-Resistance vs. Gate-to-Source Voltage



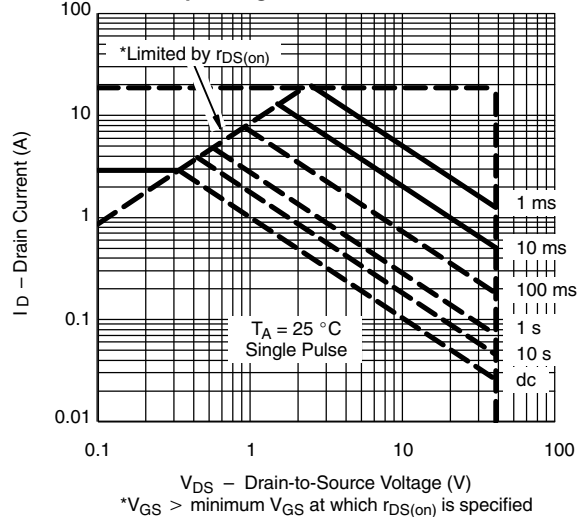
Threshold Voltage



Single Pulse Power, Junction-to-Ambient

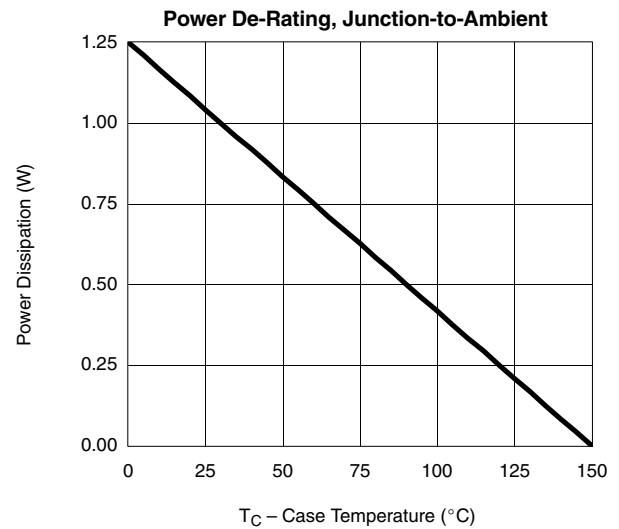
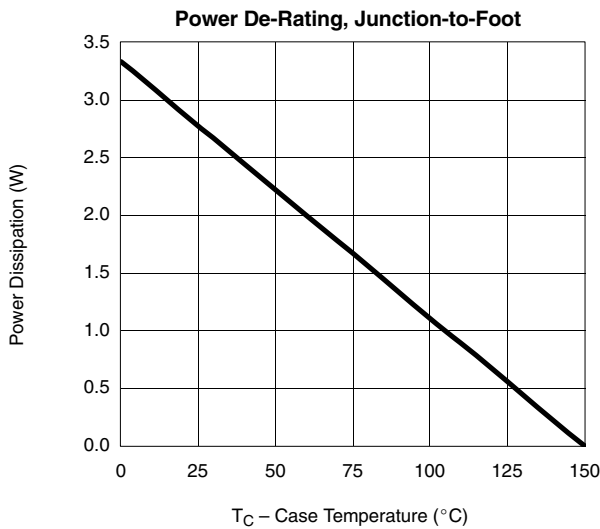
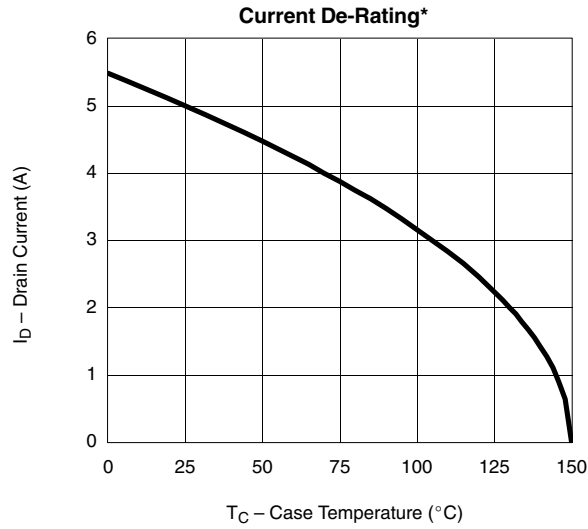


Safe Operating Area, Junction-to-Ambient



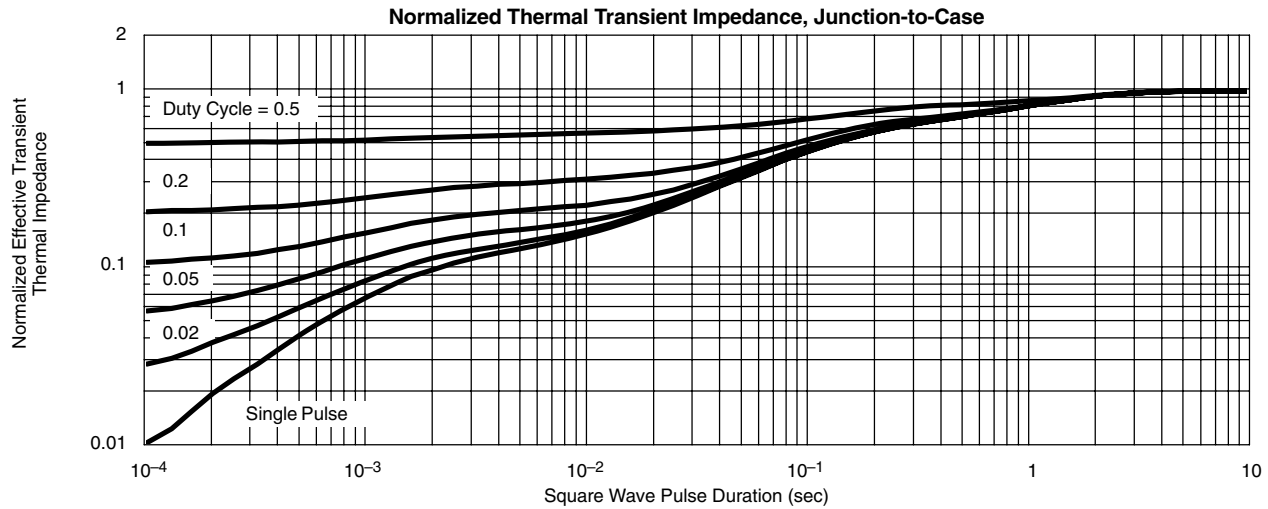
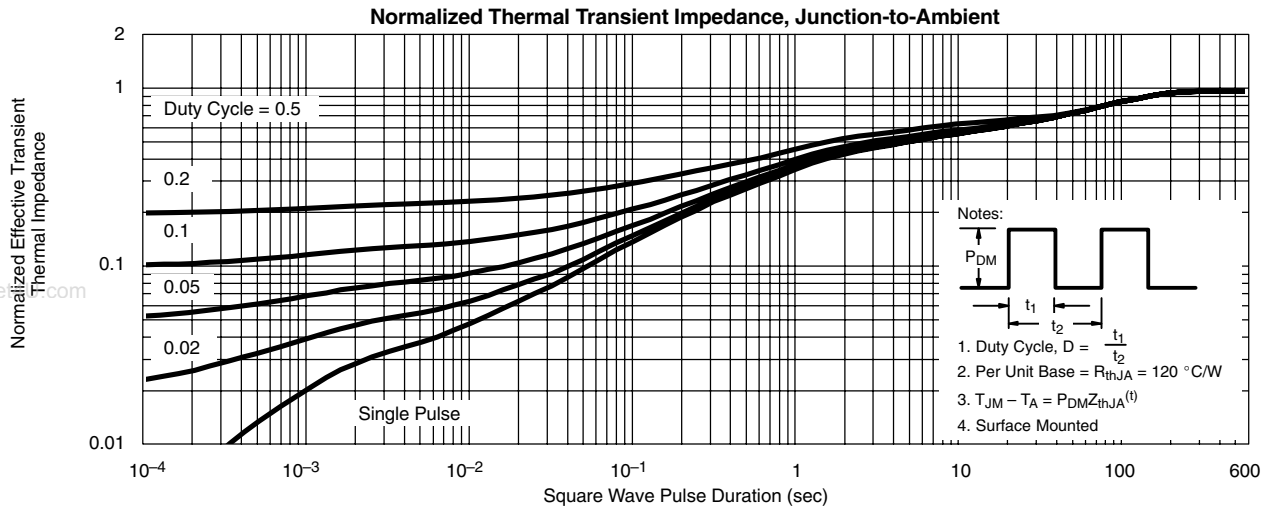


TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)



*The power dissipation P_b is based on $T_{J(max)} = 150\text{ °C}$, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <http://www.vishay.com/ppg?73698>.



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