

## N-Channel 60-V (D-S) MOSFET

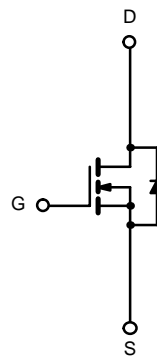
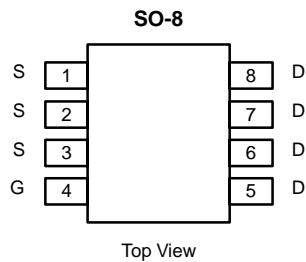
PRODUCT SUMMARY		
$V_{DS}$ (V)	$r_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
60	0.011 @ $V_{GS} = 10$ V	12.7
	0.013 @ $V_{GS} = 6.0$ V	11.7

### FEATURES

- TrenchFET® Power MOSFETS
- Extended Temperature Range

### APPLICATION

- Primary Side Switch



Ordering Information: Si4470EY  
Si4470EY-T1 (with Tape and Reel)

ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)					
Parameter		Symbol	10 secs	Steady State	Unit
Drain-Source Voltage		$V_{DS}$	60		V
Gate-Source Voltage		$V_{GS}$	$\pm 20$		
Continuous Drain Current ( $T_J = 150^\circ\text{C}$ ) <sup>a</sup>	$T_A = 25^\circ\text{C}$	$I_D$	12.7	9.0	A
	$T_A = 70^\circ\text{C}$		10.6	7.5	
Pulsed Drain Current		$I_{DM}$	50		
Avalanch Current		$I_{AS}$	50		
Continuous Source Current (Diode Conduction) <sup>a</sup>		$I_S$	3.1	1.5	W
Maximum Power Dissipation <sup>a</sup>	$T_A = 25^\circ\text{C}$	$P_D$	3.75	1.85	
	$T_A = 70^\circ\text{C}$		2.6	1.3	
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	-55 to 175		$^\circ\text{C}$

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	$t \leq 10$ sec	$R_{thJA}$	33	40	$^\circ\text{C/W}$
	Steady State		65	80	
Maximum Junction-to-Foot (Drain)		$R_{thJF}$	17	21	

Notes

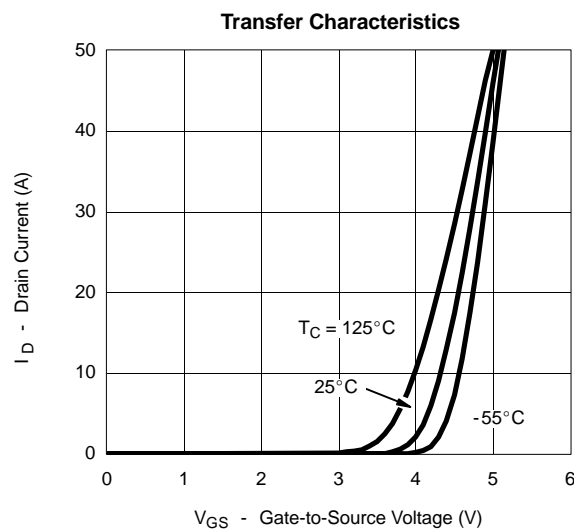
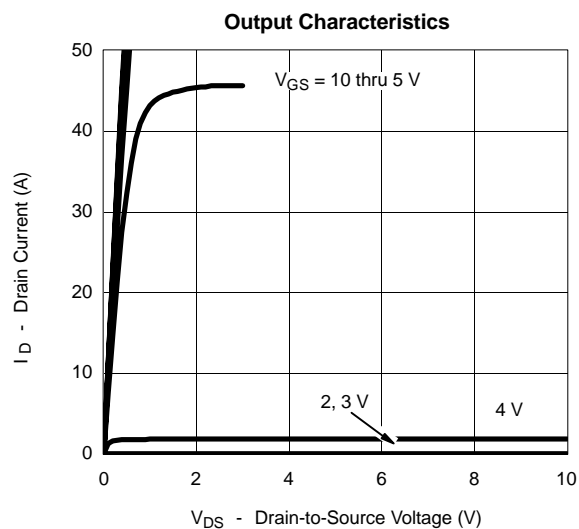
a. Surface Mounted on 1" x 1" FR4 Board.

**SPECIFICATIONS (T<sub>J</sub> = 25 °C UNLESS OTHERWISE NOTED)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	2.0			V
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 48 V, V <sub>GS</sub> = 0 V			1	μA
		V <sub>DS</sub> = 48 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			5	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 5 V, V <sub>GS</sub> = 10 V	50			A
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 12 A		0.009	0.011	Ω
		V <sub>GS</sub> = 6.0 V, I <sub>D</sub> = 10 A		0.0105	0.013	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 10 A		50		S
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = 3.0 A, V <sub>GS</sub> = 0 V		0.75	1.2	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 12 A		46	57	nC
Gate-Source Charge	Q <sub>gs</sub>			11.5		
Gate-Drain Charge	Q <sub>gd</sub>			11.5		
Gate Resistance	R <sub>g</sub>		0.25	0.85	1.4	Ω
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 30 V, R <sub>L</sub> = 30 Ω I <sub>D</sub> ≅ 1.0 A, V <sub>GEN</sub> = 10 V, R <sub>G</sub> = 6 Ω		16	25	ns
Rise Time	t <sub>r</sub>			12	18	
Turn-Off Delay Time	t <sub>d(off)</sub>			50	75	
Fall Time	t <sub>f</sub>			30	45	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 3.0 A, di/dt = 100 A/μs		40	60	

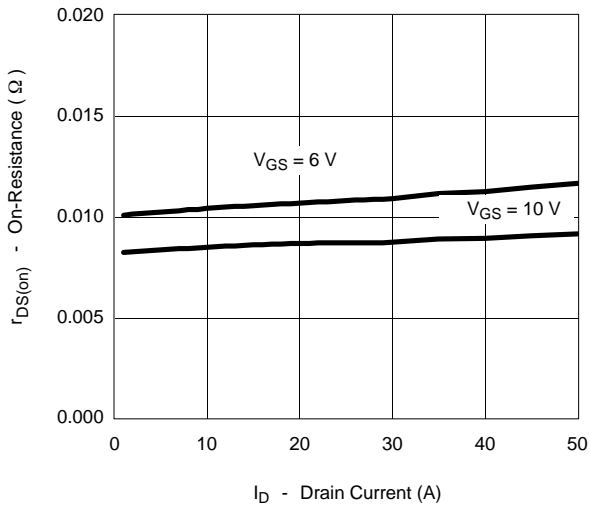
## Notes

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.  
b. Guaranteed by design, not subject to production testing.

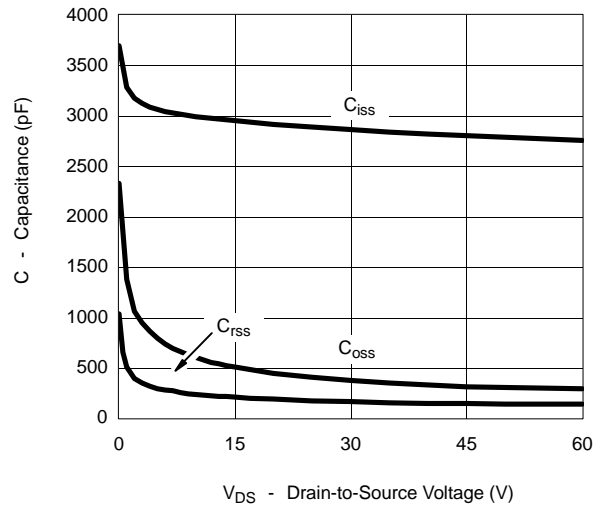
**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)**

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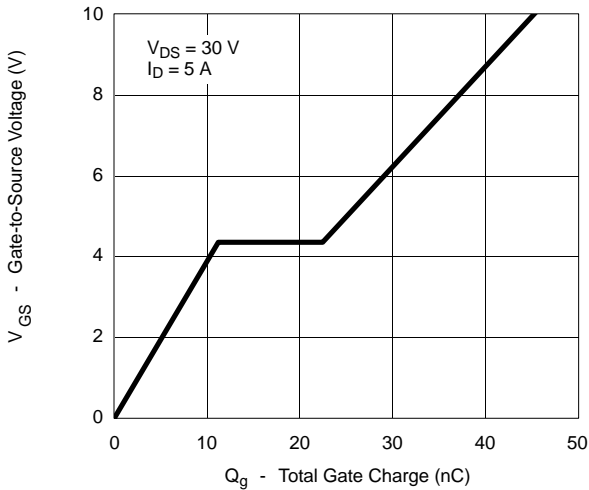
**On-Resistance vs. Drain Current**



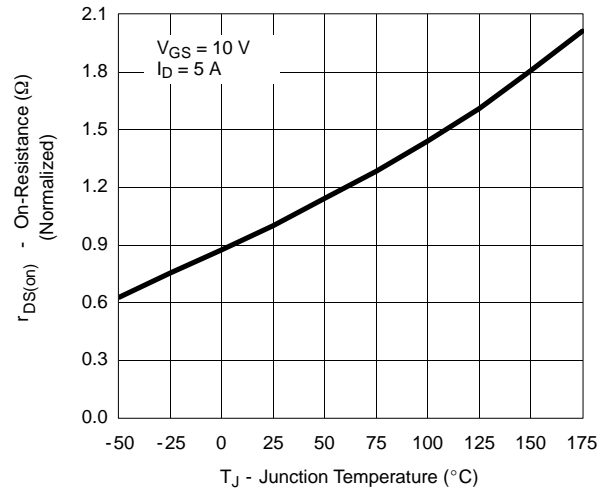
**Capacitance**



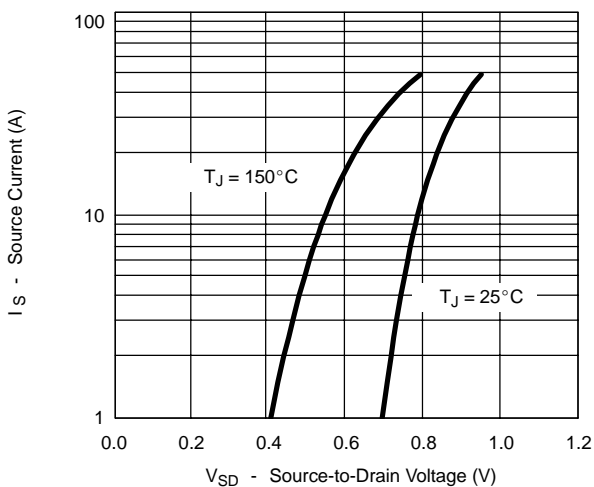
**Gate Charge**



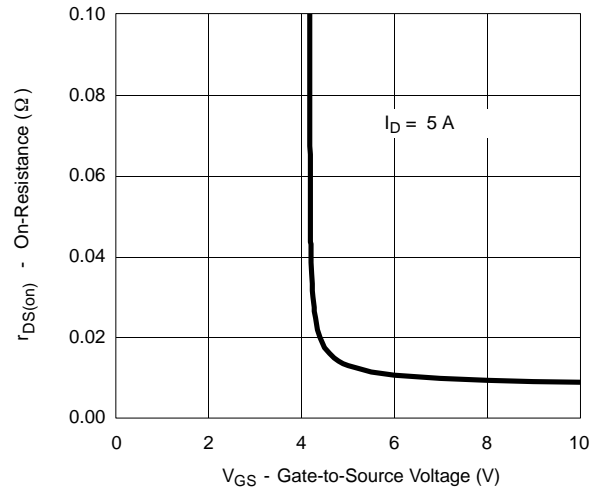
**On-Resistance vs. Junction Temperature**



**Source-Drain Diode Forward Voltage**



**On-Resistance vs. Gate-to-Source Voltage**



**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)**

