NEW PRODUCT

SBR05U20SN

0.5A SBR®
SURFACE MOUNT SUPER BARRIER RECTIFIER

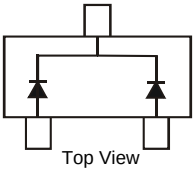
Features

- Ultra Low Forward Voltage Drop
- Superior Reverse Avalanche Capability
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- 150ºC Operating Junction Temperature
- **Lead Free Finish, RoHS Compliant (Note 1)**
- **"Green" Device, (Note 2)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

- Case: SC-59
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Terminals: Matte Tin Finish annealed over Copper leadframe Solderable per MIL-STD-202, Method 208
- Polarity: See Diagram
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.008 grams (approximate)

Top View



Top View
Device Schematic

Maximum Ratings @T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_{RM}	20	V
RMS Reverse Voltage	$V_{R(RMS)}$	14	V
Average Rectified Output Current (See Figure 1)	I_O	500	mA
Non-Repetitive Peak Forward Surge Current	I_{FSM}	3	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance Thermal Resisitance Junction to Ambient (Note 4)	$R_{\theta JA}$	134	ºC/W
Operating and Storage Temperature Range	T_J, T_{STG}	-65 to +150	ºC

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 3)	$V_{(BR)R}$	20	-	-	V	I_R = 100µA
Forward Voltage Drop	V_F	-	0.51 0.48	0.56 0.53	V	I_F = 0.5A, T_J = 25ºC I_F = 0.5A, T_J = 125ºC
Leakage Current (Note 3)	I_R	-	6 0.5	100 20	µA mA	V_R = 20V, T_J = 25ºC V_R = 20V, T_J = 125ºC

Notes:
1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see *EU Directive 2002/95/EC Annex Notes.*
2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
3. Short duration pulse test used to minimize self-heating effect.
4. Polymide PCB, 2oz, copper minimum recommended pad layout per http://www.diodes.com

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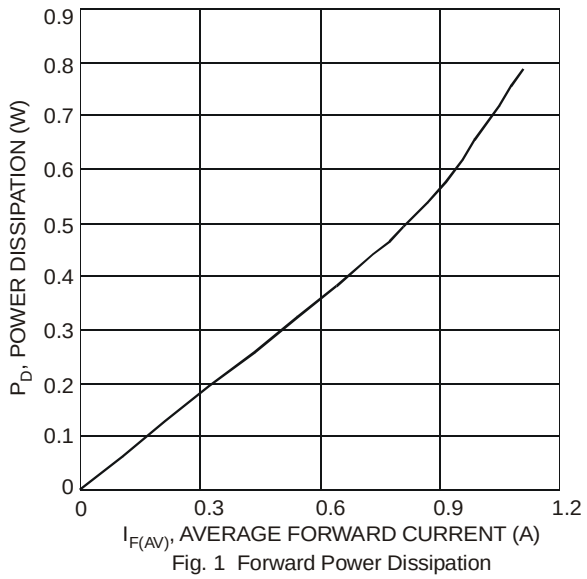

Fig. 1 Forward Power Dissipation

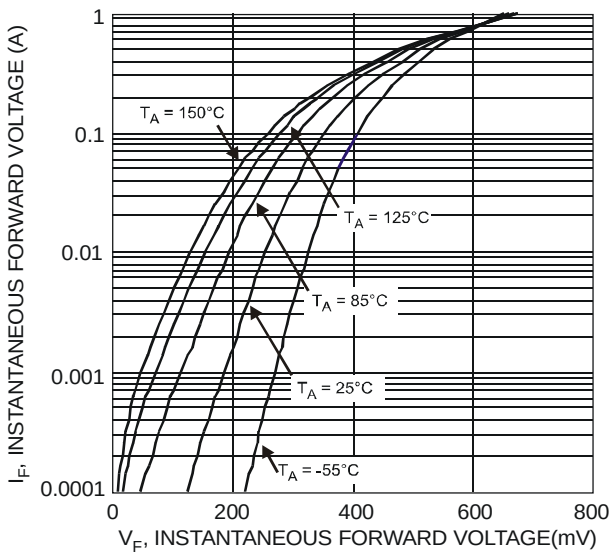

Fig. 2 Typical Forward Characteristics

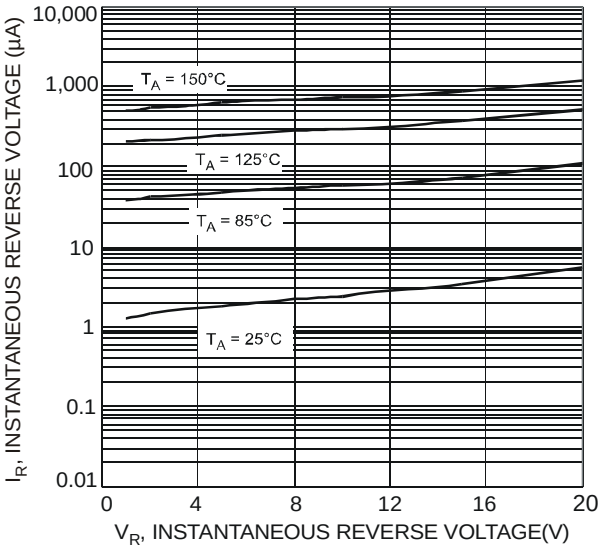

Fig. 3 Typical Reverse Characteristics

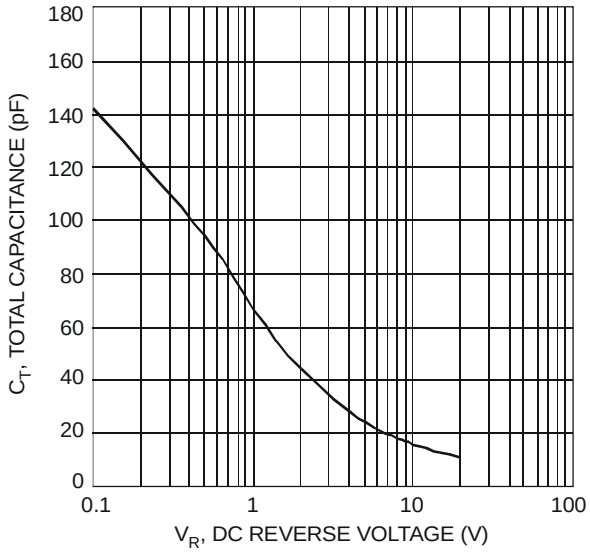

Fig. 4 Total Capacitance vs. Reverse Voltage

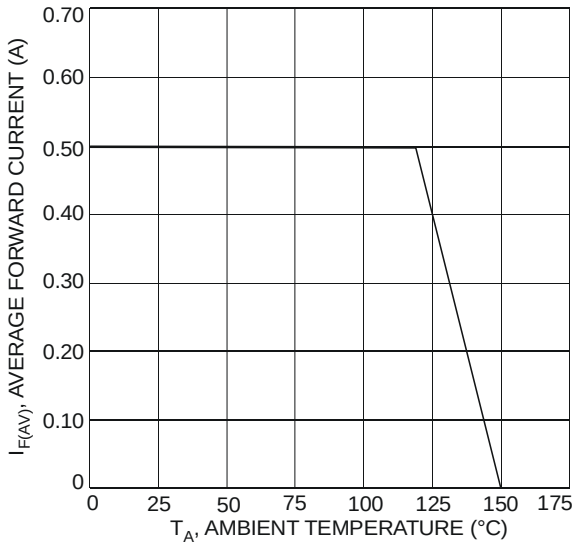

Fig. 5 Forward Current Derating Curve

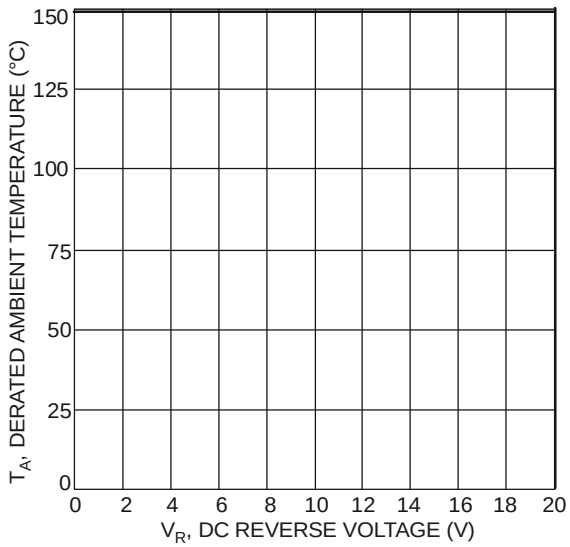

Fig. 6 Operating Temperature Derating

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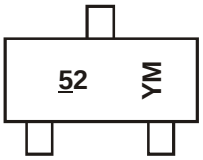
Ordering Information (Note 4)

Part Number	Case	Packaging
SBR05U20SN-7-F	SC-59	3000/Tape & Reel

Notes: 4. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



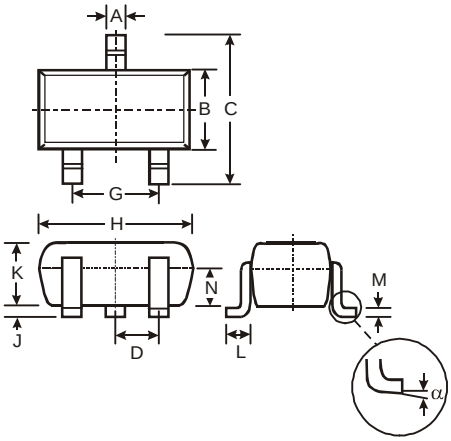
52 = Product Type Marking Code
YM = Date Code Marking
Y =Year (ex: V = 2008)
M = Month (ex: 9 = September)

Date Code Key

Year	2008	2009	2010	2011	2012	2013	2014	2015
Code	V	W	X	Y	Z	A	B	C

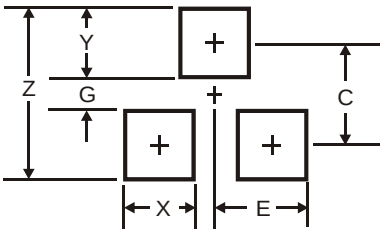
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Package Outline Dimensions



SC-59			
Dim	Min	Max	Typ
A	0.35	0.50	0.38
B	1.50	1.70	1.60
C	2.70	3.00	2.80
D	-	-	0.95
G	-	-	1.90
H	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
N	0.70	0.80	0.75
α	0°	8°	-
All Dimensions in mm			

Suggested Pad Layout



Dimensions	Value (in mm)
Z	4.0
G	1.2
X	0.9
Y	1.4
C	2.6
E	0.95