

# 10V Drive Nch MOSFET

### **R5009FNX**

#### Structure

Silicon N-channel MOSFET

#### Features

- 1)Fast reverse recovery time (t<sub>rr</sub>)
- 2) Low on-resistance.
- 3) Fast switching speed.
- 4) Gate-source voltage  $V_{\text{GSS}} \mbox{ garanteed to be } \pm 30 \mbox{V} \ . \label{eq:VGSS}$
- 5) Drive circuits can be simple.
- 6) Parallel use is easy.

#### Application

Switching

Packaging specifications

	• •		
Type	Package	Bulk	
туре	Basic ordering unit (pieces)	500	
R5009FNX		0	

● Absolute maximum ratings (Ta = 25°C)

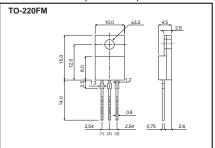
Paramet	Symbol	Limits	Unit	
Drain-source voltage		$V_{DSS}$	500	V
Gate-source voltage		$V_{GSS}$	±30	V
Drain current	Continuous	I <sub>D</sub> *3	±9	Α
	Pulsed	I <sub>DP</sub> *1	±36	Α
Source current (Body Diode)	Continuous	<sub>S</sub> *3	9	Α
	Pulsed	I <sub>SP</sub> *1	36	Α
Avalanche Current		I <sub>AS</sub> *2	4.5	Α
Avalanche Energy		E <sub>AS</sub> *2	5.4	mJ
Power dissipation (Tc=25°C)		$P_D$	50	W
Channel temperature	hannel temperature		150	°C
Range of storage temperature		Tstg	-55 to +150	°C

<sup>\*1</sup> Pw $\leq$ 10 $\mu$ s, Duty cycle $\leq$ 1%

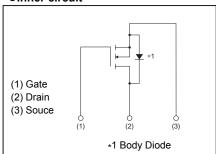
#### • Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to Case	Rth (ch-c)	2.5	°C/W

#### • Dimensions (Unit: mm)



#### •Inner circuit



<sup>\*2</sup> L=500 $\mu$ H, V<sub>DD</sub>=50V, Rg=25 $\Omega$ ,starting Tch=25 °C

<sup>\*3</sup> Limited only by maximum temperature allowed.

# ● Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	$I_{GSS}$	-	-	±100	nA	$V_{GS}$ =±30V, $V_{DS}$ =0V
Drain-source breakdown voltage	$V_{(BR)DSS}$	500	-	-	٧	I <sub>D</sub> =1mA, V <sub>GS</sub> =0V
Zero gate voltage drain current	I <sub>DSS</sub>	=	-	100	uA	V <sub>DS</sub> =500V, V <sub>GS</sub> =0V
Gate threshold voltage	$V_{GS (th)}$	2.0	-	4.0	V	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA
Static drain-source on-state resistance	R <sub>DS (on)</sub> *	-	0.65	0.84	Ω	I <sub>D</sub> =4.5A, V <sub>GS</sub> =10V
Forward transfer admittance	I Y <sub>fs</sub> I*	4.0	5.7	-	S	I <sub>D</sub> =4.5A, V <sub>DS</sub> =10V
Input capacitance	C <sub>iss</sub>	=	630	-	pF	V <sub>DS</sub> =25V
Output capacitance	C <sub>oss</sub>	=	400	-	pF	V <sub>GS</sub> =0V
Reverse transfer capacitance	C <sub>rss</sub>	=	25	-	pF	f=1MHz
Turn-on delay time	t <sub>d(on)</sub> *	-	24	-	ns	I <sub>D</sub> =4.5A, V <sub>DD</sub> ≒ 250V
Rise time	t <sub>r</sub> *	-	20	-	ns	V <sub>GS</sub> =10V
Turn-off delay time	t <sub>d(off)</sub> *	-	50	-	ns	$R_L$ =55.6 $\Omega$
Fall time	t <sub>f</sub> *	-	40	-	ns	$R_G$ =10 $\Omega$
Total gate charge	Q <sub>g</sub> *	-	18		nC	I <sub>D</sub> =9.0A, V <sub>DD</sub> ≒ 250V
Gate-source charge	Q <sub>gs</sub> *	-	3.5	-	nC	V <sub>GS</sub> =10V
Gate-drain charge	Q <sub>gd</sub> *	-	5.5	-	nC	

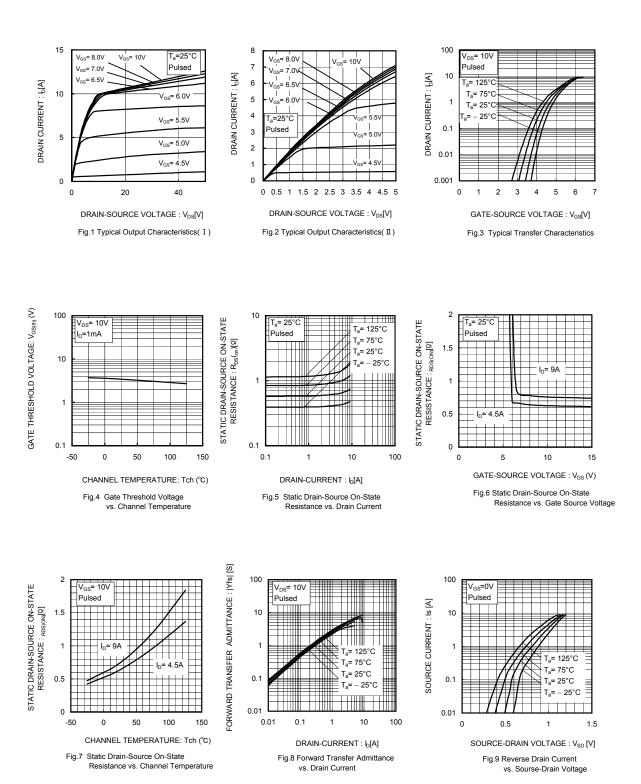
<sup>\*</sup>Pulsed

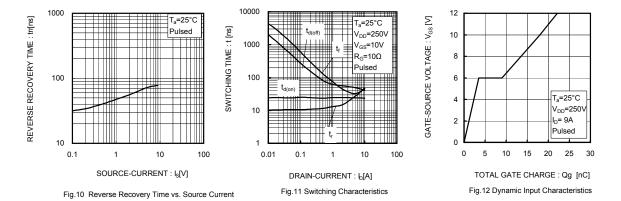
## ●Body diode characteristics (Source-Drain) (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward Voltage	V <sub>SD</sub> *	-	-	1.5	V	I <sub>s</sub> =9.0A, V <sub>GS</sub> =0V
Reverse Recovery Time	t <sub>rr</sub> *	48	78	108	ns	I <sub>s</sub> =9.0A, di/dt=100A/μs

<sup>\*</sup>Pulsed

#### Electrical characteristics curves





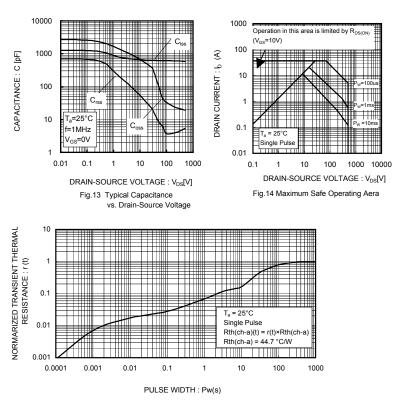


Fig.15 Normalized Transient Thermal Resistance vs. Pulse Width

#### Measurement circuits

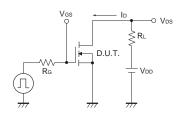


Fig.1-1 Switching Time Measurement Circuit

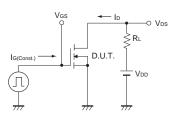


Fig.2-1 Gate Charge Measurement Circuit

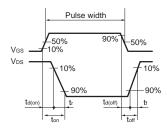


Fig.1-2 Switching Waveforms

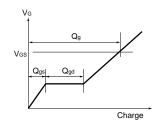


Fig.2-2 Gate Charge Waveform

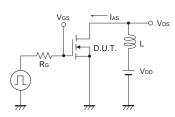


Fig.3-1 Avalanche Measurement Circuit

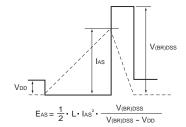


Fig.3-2 Avalanche Waveform

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