

S32xx Model
8 Pin Dip, **5V, HCMOS/TTL**

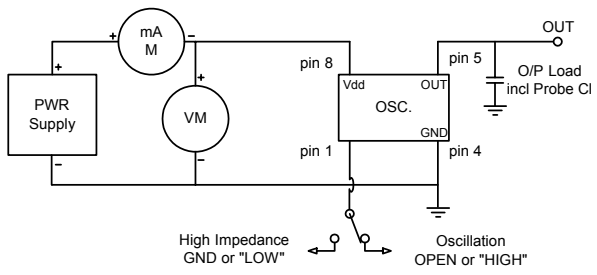
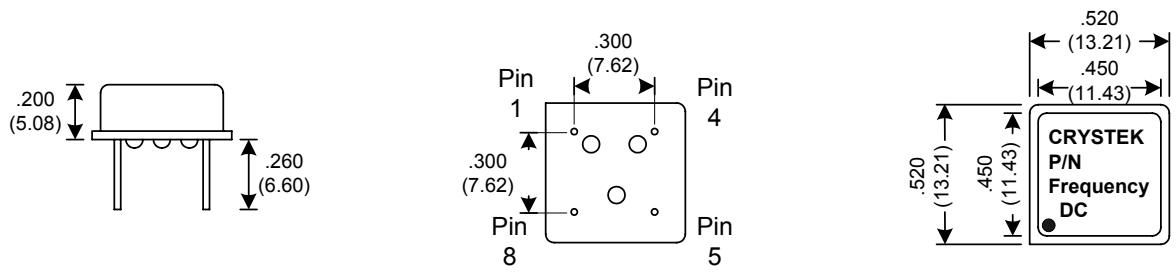


Frequency Range: 1.544MHz to 125MHz
Frequency Stability: ±10ppm to ±100ppm
Temperature Range:
 Operating: 0°C to 70°C
 (Option) -40°C to 85°C
Storage: -55°C to 120°C
Input Voltage: 5V ± 0.5V
Input Current: 40mA Max
Output: HCMOS/TTL
 Symmetry: 40/60% Max @ 50% Vdd
 (Option) 45/55% Max
 Rise/Fall Time: 4ns Typ, 6ns Max
 Logic: "0" = 10% Vdd Max
 "1" = 90% Vdd Min
 Load: 50pF/10TTL Max
Jitter RMS: 5ps Typ, 10ps Max
Aging: <3ppm 1st/yr, 1ppm every year thereafter

Clock Oscillator



Designed to meet today's requirements for low jitter applications. The oscillator utilizes fundamental and 3rd overtone crystal technology. No multiplier is used therefore reducing output jitter.



Tri-State Function	
Function pin 1	Output pin
Open	Active
"1" level 2.4V Min	Active
"0" level 0.4V Max	High Z

Crystek Part Number Guide	
Example: S3292-44.736	
Extended Temp: SE3292-44.736	
S= 0°C to 70°C	
*SE= -40°C to 85°C	
Symmetry 40/60%	
Part Number	Freq. Stability
S*3290	+/- 100ppm
S*3292	+/- 50ppm
S*3291	+/- 25ppm
S 3298	+/- 20ppm
S 3297	+/- 10ppm
Symmetry 45/55%	
Part Number	Freq. Stability
S*3990	+/- 100ppm
S*3992	+/- 50ppm
S*3991	+/- 25ppm
S 3998	+/- 20ppm
S 3997	+/- 10ppm

Specifications subject to change without notice. **TD-02064 Rev. B**