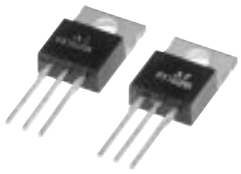
FS16UMA-5A

PRELIMINARY
Notice: This is not a final specification.
Some parametric limits are subject to change.

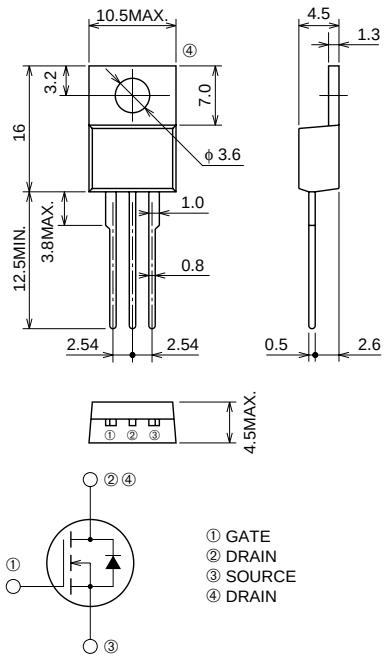
HIGH-SPEED SWITCHING USE

FS16UMA-5A



- 10V DRIVE
- VDSS 250V
- rDS (ON) (MAX) 0.25Ω
- ID 16A

OUTLINE DRAWING Dimensions in mm



TO-220

APPLICATION

Cs Switch for CRT Display monitor

MAXIMUM RATINGS (Tc = 25°C)

Symbol	Parameter	Conditions	Ratings	Unit
VDSS	Drain-source voltage	VGS = 0V	250	V
VGSS	Gate-source voltage	VDS = 0V	±20	V
ID	Drain current		16	A
IDM	Drain current (Pulsed)		48	A
IDA	Avalanche drain current (Pulsed)	L = 200µH	16	A
PD	Maximum power dissipation		80	W
Tch	Channel temperature		–55 ~ +150	°C
Tstg	Storage temperature		–55 ~ +150	°C
—	Weight	Typical value	2.0	g

MITSUBISHI ELECTRIC

ELECTRICAL CHARACTERISTICS (Tch = 25°C)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
$V_{(BR)DSS}$	Drain-source breakdown voltage	I_D = 1mA, V_{GS} = 0V	250	—	—	V
I_{GSS}	Gate-source leakage current	V_{GS} = ±20V, V_{DS} = 0V	—	—	±10	µA
I_{DSS}	Drain-source leakage current	V_{DS} = 250V, V_{GS} = 0V	—	—	1	mA
$V_{GS(th)}$	Gate-source threshold voltage	I_D = 1mA, V_{DS} = 10V	2.0	3.0	4.0	V
$r_{DS(ON)}$	Drain-source on-state resistance	I_D = 8A, V_{GS} = 10V	—	0.19	0.25	Ω
$V_{DS(ON)}$	Drain-source on-state voltage	I_D = 8A, V_{GS} = 10V	—	1.52	2.00	V
$\|y_{fs}\|$	Forward transfer admittance	I_D = 8A, V_{DS} = 10V	—	16.0	—	S
C_{iss}	Input capacitance	V_{DS} = 25V, V_{GS} = 0V, f = 1MHz	—	1850	—	pF
C_{oss}	Output capacitance		—	180	—	pF
C_{rss}	Reverse transfer capacitance		—	50	—	pF
$t_{d(on)}$	Turn-on delay time	V_{DD} = 150V, I_D = 8A, V_{GS} = 10V, R_{GEN} = R_{GS} = 50Ω	—	30	—	ns
t_r	Rise time		—	50	—	ns
$t_{d(off)}$	Turn-off delay time		—	320	—	ns
t_f	Fall time		—	70	—	ns
V_{SD}	Source-drain voltage	I_S = 8A, V_{GS} = 0V	—	0.95	—	V
$R_{th(ch-c)}$	Thermal resistance	Channel to case	—	—	1.56	°C/W