

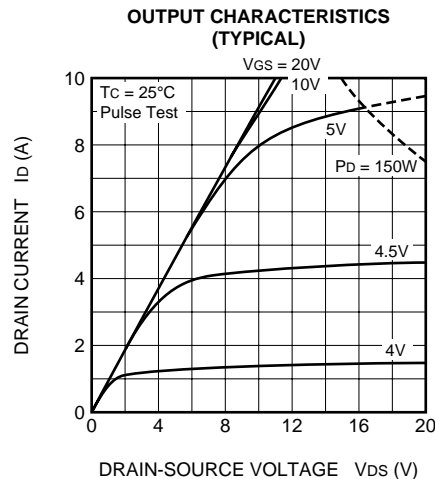
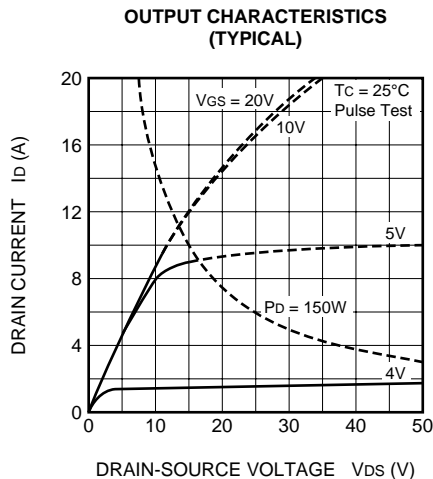
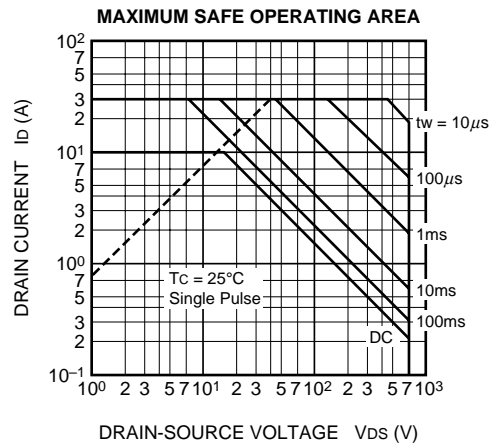
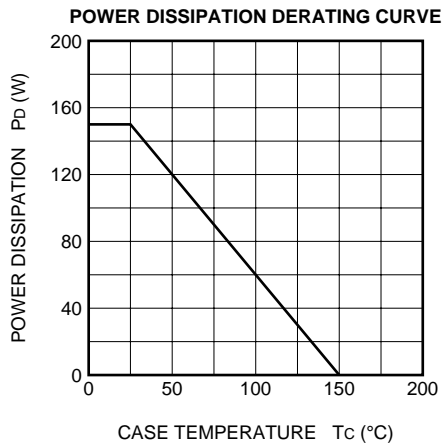
FS10VS-14A

HIGH-SPEED SWITCHING USE

ELECTRICAL CHARACTERISTICS (Tch = 25°C)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V(BR)DSS	Drain-source breakdown voltage	Id = 1mA, VGS = 0V	700	—	—	V
V(BR)GSS	Gate-source breakdown voltage	IGS = ±100μA, VDS = 0V	±30	—	—	V
IGSS	Gate-source leakage current	VGS = ±25V, VDS = 0V	—	—	±10	μA
IDSS	Drain-source leakage current	VDS = 700V, VGS = 0V	—	—	1	mA
VGS(th)	Gate-source threshold voltage	Id = 1mA, VDS = 10V	2	3	4	V
rDS(ON)	Drain-source on-state resistance	Id = 5A, VGS = 10V	—	1.0	1.3	Ω
VDS(ON)	Drain-source on-state voltage	Id = 5A, VGS = 10V	—	5.0	6.5	V
yfs	Forward transfer admittance	Id = 5A, VDS = 10V	4.8	8.0	—	S
Ciss	Input capacitance	VDS = 25V, VGS = 0V, f = 1MHz	—	1380	—	pF
Coss	Output capacitance		—	150	—	pF
Crss	Reverse transfer capacitance		—	32	—	pF
td(on)	Turn-on delay time		—	25	—	ns
tr	Rise time	VDD = 200V, Id = 5A, VGS = 10V, RGEN = RGS = 50Ω	—	33	—	ns
td(off)	Turn-off delay time		—	170	—	ns
tf	Fall time		—	55	—	ns
VSD	Source-drain voltage	IS = 5A, VGS = 0V	—	1.0	1.5	V
Rth(ch-c)	Thermal resistance	Channel to case	—	—	0.83	°C/W

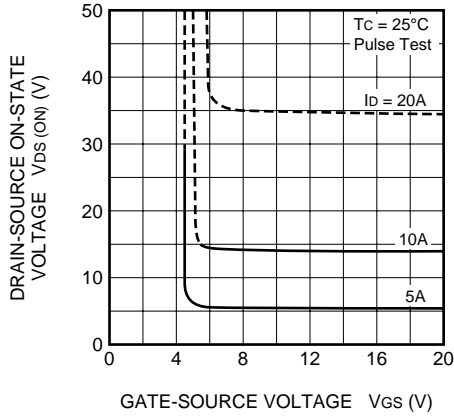
PERFORMANCE CURVES



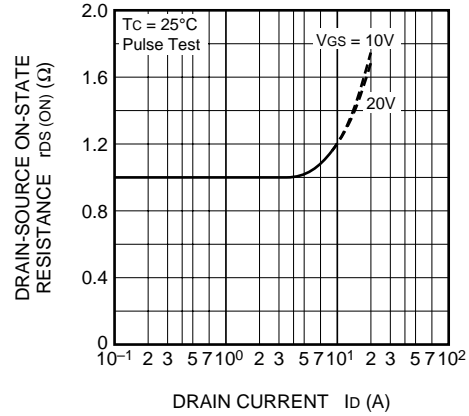
FS10VS-14A

HIGH-SPEED SWITCHING USE

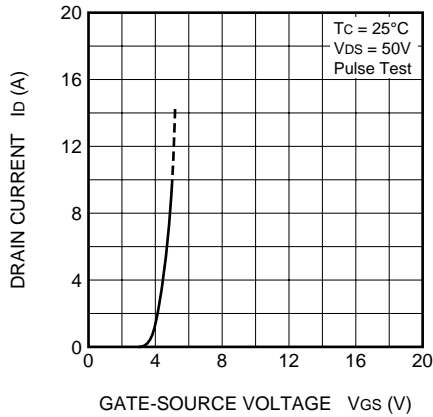
ON-STATE VOLTAGE VS. GATE-SOURCE VOLTAGE (TYPICAL)



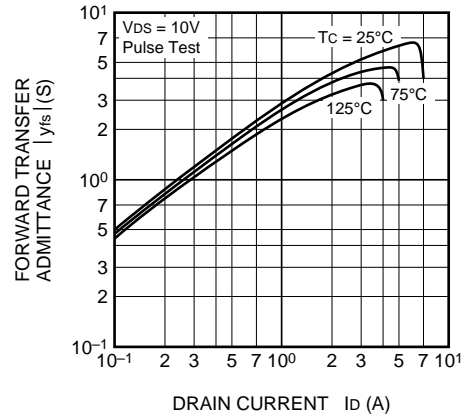
ON-STATE RESISTANCE VS. DRAIN CURRENT (TYPICAL)



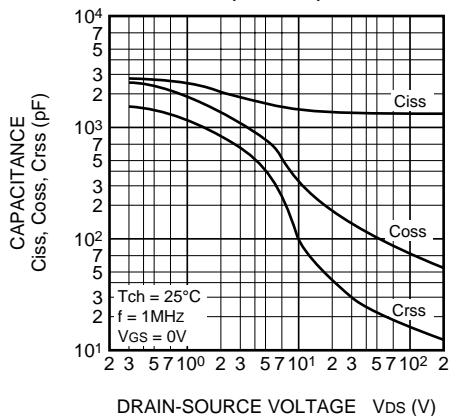
TRANSFER CHARACTERISTICS (TYPICAL)



FORWARD TRANSFER ADMITTANCE VS. DRAIN CURRENT (TYPICAL)



CAPACITANCE VS. DRAIN-SOURCE VOLTAGE (TYPICAL)



SWITCHING CHARACTERISTICS (TYPICAL)

