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10mD3421F



Specifications and Applications Information

06/28/07

Preliminary

10m Class Single Lamp DC to AC Inverter

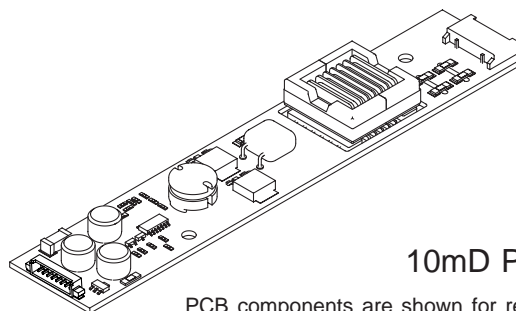
The ERG 10mD3421F (10m Class) low profile dc to ac inverter is specifically designed to power the the following display module(s) to a moderate brightness level from a +5 volt dc power supply.

- Kyocera TCG057QV1AD-G00
- Kyocera KCG057QV1DC-G50

This inverter can be dimmed using an external analog control voltage or an external PWM generator.

This low profile inverter features:

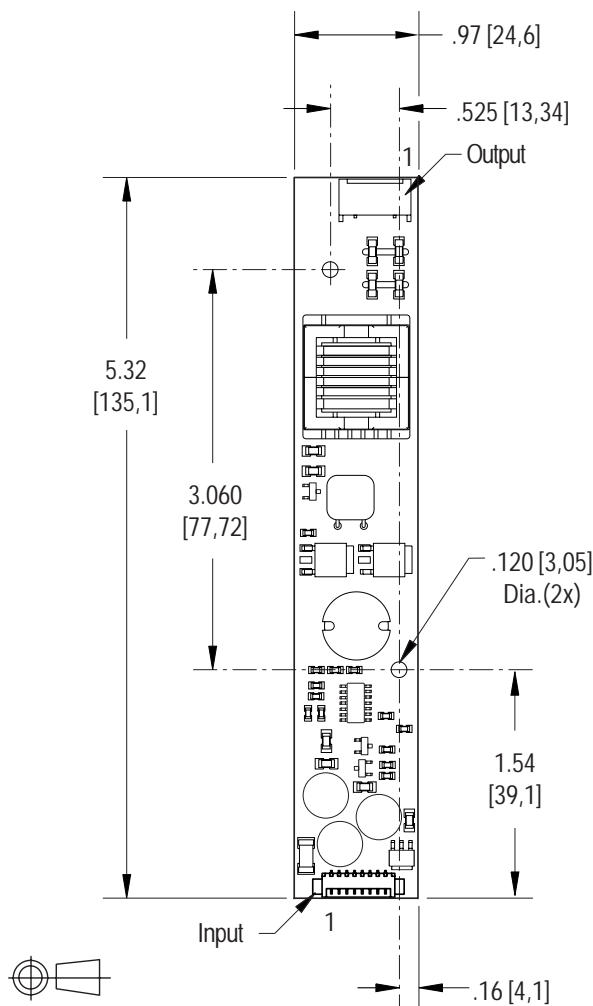
- ✓ Less Than 10 mm in Height
- ✓ LCD Module Specific
- ✓ Display Compatible Output Connector
- ✓ Firm Specifications
- ✓ High Dimming Ratio
- ✓ Application Information
- ✓ Designed, Manufactured and Supported in the USA
- ✓ Custom Input and Output Voltages
- ✓ Flexible System Interface



10mD Package

PCB components are shown for reference only. Actual product may differ from that shown.

Package Configuration



PCB components are shown for reference only. Actual product may differ from that shown.

Mass: 22 grams

Connectors

Input Connector	Output Connector
Molex 53261-0871	JST SM02(8.0)B-BHS-1-TB
J1-1 Vin(+)	J2-1 ACout
J1-2 Vin(+)	J2-2 ACreturn
J1-3 GND	
J1-4 GND	
J1-5 Enable	
J1-6 Control	
J1-7 GND	
J1-8 GND	



Absolute Maximum Ratings

Rating	Symbol	Value	Units
Input Voltage Range	V _{in}	-0.3 to +5.5	Vdc
Storage Temperature	T _{stg}	-40 to +85	°C

Operating Characteristics

With referenced display and lamp warm-up of 5 minutes.
Unless otherwise noted V_{in} = 5.00 Volts dc and T_a = 25°C

Characteristic	Symbol	Min	Typ	Max	Units
Input Voltage	V _{in}	+4.50	+5.00	+5.25	Vdc
Component Surface Temperature (note 2)	T _s	-20	-	+80	°C
Input Current (note 1)	I _{in}	-	0.75	0.86	Adc
Operating Frequency	F _o	36	41	46	kHz
Minimum Output Voltage (note 3)	V _{out} (min)	2200	-	-	Vrms
Efficiency	h	-	77	-	%
Output Current (per lamp)	I _{out}	-	4.2	-	mArms
Output Voltage	V _{out}	-	685	-	Vrms
Enable Pin (note 4)					
Turn-off Threshold	V _{thoff}	GND	-	0.5	Vdc
Turn-on Threshold	V _{thon}	2.0	-	V _{in}	Vdc
Impedance to V _{in}	R _{Enable}	9.5	10.0	10.5	kOhms

Specifications subject to change without notice.

(Note 1) Input current in excess of maximum may indicate a load/inverter mismatch condition, which can result in reduced reliability. Please contact ERG technical support.

(Note 2) Surface temperature must not exceed 80 degrees C; thermal management actions may be required.

(Note 3) Provided data is not tested but guaranteed by design.

(Note 4) The inverter is always enabled with an internal pullup resistor tied to the enable pin. A ground on the enable input will turn the inverter off.

Application Notes:

- 1) The minimum distance from high voltage areas of the inverter to any conductive material should be .12 inches per kilovolt of starting voltage.
- 2) Mounting hardware to be non-conductive.
- 3) Open framed inverters should not be used in applications at altitudes over 10,000 feet.
- 4) Contact ERG for possible exceptions.



Onboard PWM

Unless otherwise noted $V_{in} = 5.00$ Volts DC, $T_a = 25$ °C and unit has been running for 5 minutes.

Characteristic	Symbol	Min	Typ	Max	Units
Frequency	f_{pwm}	-	160	-	Hz
Control Input Bias Current	I_{cbias}	-	-	10	μA

Pin Descriptions

Vin Input voltage to the inverter.

GND Inverter ground.

Control Analog voltage input to the onboard pulse width modulator. Increasing this voltage increases the off time of the onboard PWM resulting in decreased brightness. The inverter is full on when this voltage is near inverter ground.

Enable Inverter Enable. The inverter is always enabled with an internal pullup resistor tied to the enable pin. Pull this pin low to disable inverter operation. The onboard PWM is always utilized.

Application information

The 10mD series of inverters is designed to power one cold cathode fluorescent lamp with six watts. An external analog control interfaces with an onboard pulse width modulator to provide dimming control. The 10mD inverter can reliably dim to less than 5% duty cycle.

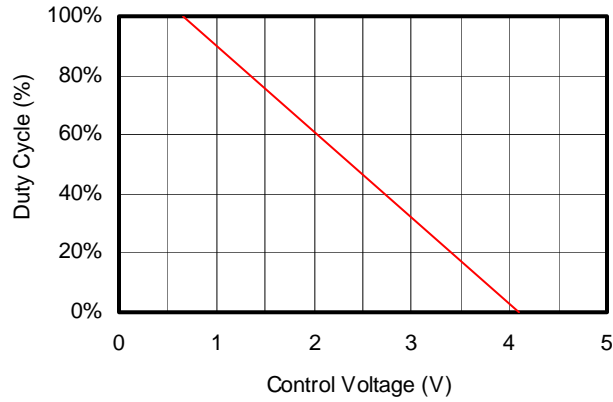
External shutdown of the inverter is accomplished using the Enable pin. Pulling this pin low (below V_{thoff}) disables the inverter.

If analog voltage dimming is required, the analog voltage is applied to the Control pin. Figure 1 shows how to connect the inverter for onboard PWM operation. Graph 1 shows the relationship of PWM duty cycle to input control voltage.

If an external PWM is used, simply connect the Enable pin to the PWM source and connect the Control pin to inverter ground. If the onboard PWM is used, connect the analog voltage to the Control pin.

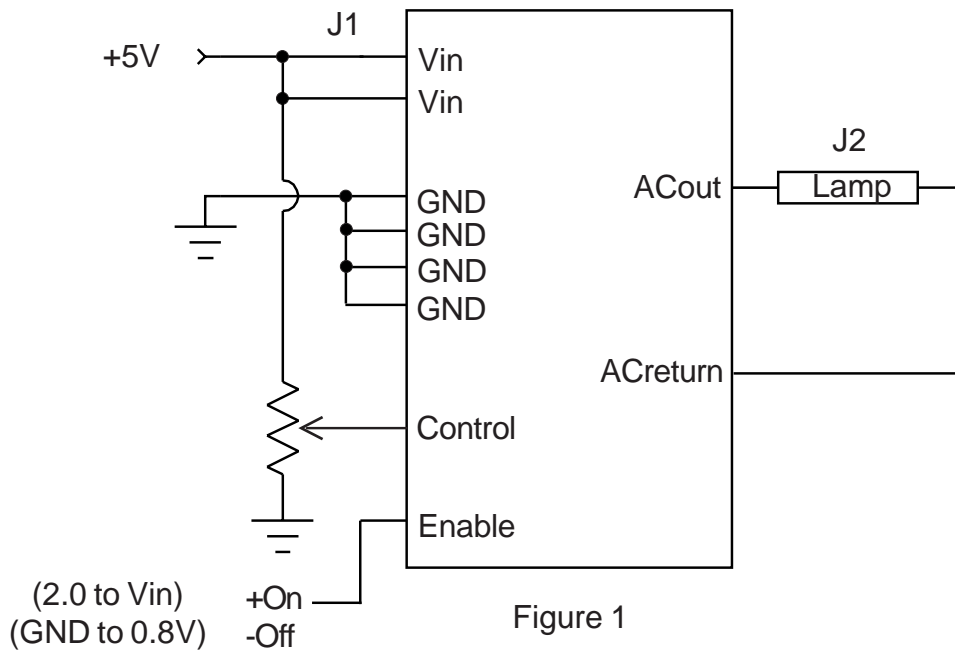


Control Voltage vs. Duty Cycle



Graph 1

Typical Application



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