TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (π-MOS VII)

2SK3669

Switching Regulator, Audio Amplifier and Motor Drive Applications

Low drain-source ON-resistance: R_{DS} (ON) = 95 mΩ (typ.)

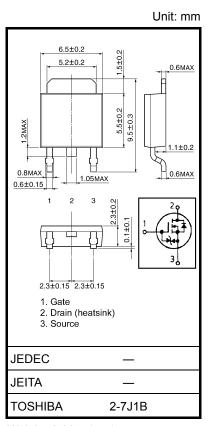
• High forward transfer admittance: $|Y_{fs}| = 6 \text{ S (typ.)}$

• Low leakage current: $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 100 \text{ V)}$

• Enhancement mode: $V_{th} = 3.0 \text{ to } 5.0 \text{ V } (V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	100	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	100	V	
Gate-source voltage		V_{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	10		
	Pulse (t _w ≤ 10 ms) (Note 1)	I _{DP}	15	Α	
	Pulse (t _w ≤ 1 ms) (Note 1)	IDP	28		
Drain power dissipation (Tc = 25°C)		PD	20	W	
Single-pulse avalanche energy (Note 2)		E _{AS}	280	mJ	
Avalanche current		I _{AR}	10	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	2	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	6.25	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	125	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 50$ V, $T_{ch} = 25$ °C (initial), L = 3.44 mH, $I_{AR} = 10$ A, $R_G = 25$ Ω

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



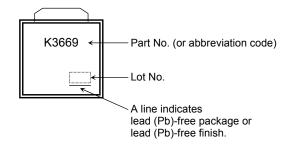
Electrical Characteristics (Ta = 25°C)

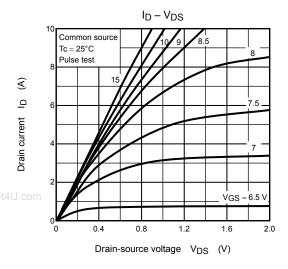
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA
Drain cutoff curre	nt	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source brea	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	100	_	_	V
Gate threshold voltage		V _{th}	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	3.0	_	5.0	V
Drain-source ON-resistance		R _{DS} (ON)	V _{GS} = 10 V, I _D = 5 A	_	95	125	mΩ
Forward transfer admittance		Y _{fs}	V _{DS} = 10 V, I _D = 5 A	3	6	_	S
Input capacitance		C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	480	_	pF
Reverse transfer capacitance		C _{rss}		_	9	_	
Output capacitance		C _{oss}		_	220	_	
Switching time	Rise time	t _r	10 V	_	2	_	- ns
	Turn-on time	t _{on}		_	12	_	
	Fall time	t _f		_	2	_	
	Turn-off time	t _{off}	V _{DD} ≈ 50 V Duty ≤ 1%, t _w = 10 μs	_	12	_	
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \approx 80 \text{ V}, V_{GS} = 10 \text{ V},$ $I_{D} = 10 \text{ A}$	_	8.0	_	nC
Gate-source charge		Q _{gs}		_	5.6	_	
Gate-drain ("Miller") charge		Q _{gd}		_	2.4	_	

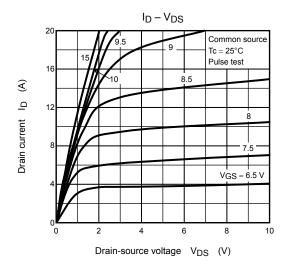
Source-Drain Diode Ratings and Characteristics (Ta = 25°C)

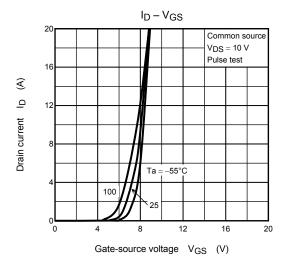
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	10	Α
Pulse drain reverse current (t _W ≤ 10 ms) (Note 1)	I _{DRP}	_	_	_	15	Α
Pulse drain reverse current $(t_W \le 1 \text{ ms}) \text{ (Note 1)}$	I _{DRP}	_	_	_	28	Α
Forward voltage (diode)	V _{DS2F}	I _{DR1} = 10 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 10 A, V _{GS} = 0 V,	_	65	_	ns
Reverse recovery charge	Qrr	dI _{DR} /dt = 50 A/μs	_	90	_	nC

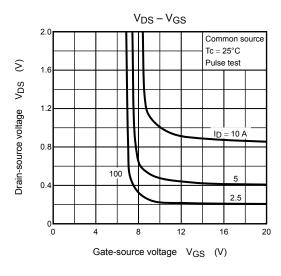
Marking

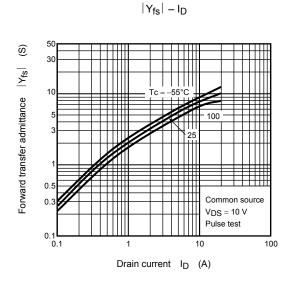


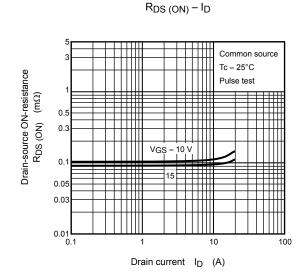


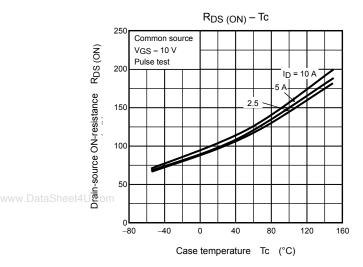


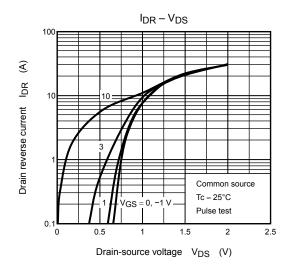


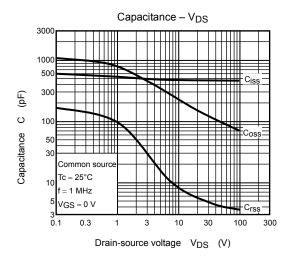


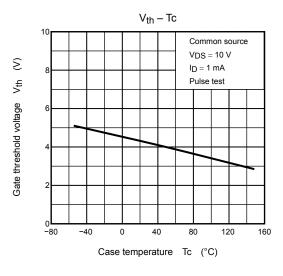


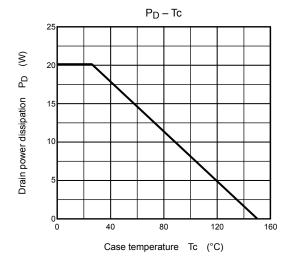


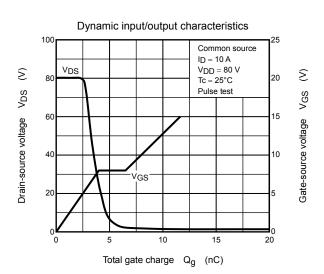


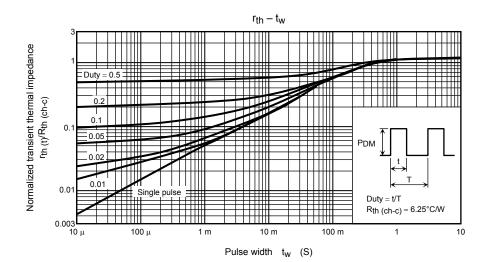




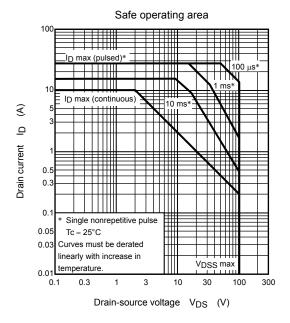


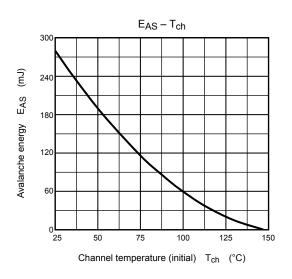


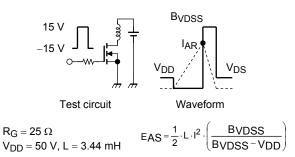




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