

# MOS FIELD EFFECT TRANSISTOR

# 2SK3054

# N-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

# DESCRIPTION

The 2SK3054 is a switching device which can be driven directly by a 2.5-V power source.

The 2SK3054 has excellent switching characteristics, and is suitable for use as a high-speed switching device in digital circuits.

## FEATURES

- Can be driven by a 2.5-V power source
- Low gate cut-off voltage

# ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs= 0 V)	VDSS	50	V
Gate to Source Voltage (VDS= 0 V)	Vgss	±7	V
Drain Current (DC)	D(DC)	±0.1	А
Drain Current (pulse) <sup>Note</sup>	D(pulse)	±0.2	А
Total Power Dissipation	Рт	150	mW
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	–55 to +150	°C

**Note** PW  $\leq$  10 ms, Duty cycle  $\leq$  50 %

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version. Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

#### Document No. D14209EJ2V0DS00 (2nd edition) Date Published March 2000 NS CP(K) Printed in Japan

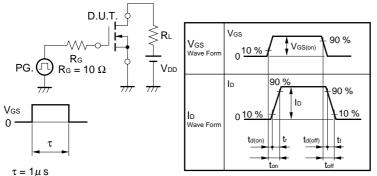
### ORDERING INFORMATION

PART NUMBER	PACKAGE	
2SK3054	SC-70	

# ELECTRICAL CHARACTERISTICS (TA = 25 °C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Drain Cut-off Current	IDSS	Vds = 50 V, Vgs = 0 V			1	μA
Gate Leakage Current	lgss	$V_{GS} = \pm 7 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$			±5	μA
Gate to Source Cut-off Voltage	VGS(off)	$V_{DS} = 3 V, I_D = 1 \mu A$	0.9	1.2	1.5	V
Forward Transfer Admittance	y <sub>fs</sub>	V <sub>DS</sub> = 3 V, I <sub>D</sub> = 10 mA	20	38		mS
Drain to Source On-state Resistance	RDS(on)1	Vgs = 2.5 V, Id = 10 mA		22	40	Ω
	RDS(on)2	V <sub>GS</sub> = 4.0 V, I <sub>D</sub> = 10 mA		14	20	Ω
Input Capacitance	Ciss	V <sub>DS</sub> = 3 V		8		pF
Output Capacitance	Coss	V <sub>GS</sub> = 0 V		7		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		3		pF
Turn-on Delay Time	td(on)	V <sub>DD</sub> = 3 V		15		ns
Rise Time	tr	l⊳ = 20 mA		100		ns
Turn-off Delay Time	td(off)	VGS(on) = 3 V		30		ns
Fall Time	tr	R <sub>G</sub> = 10 Ω, R <sub>L</sub> = 150 Ω		35		ns

# **TEST CIRCUIT SWITCHING TIME**



 $\tau = 1\mu s$ Duty Cycle  $\leq 1 \%$ 

100

80

60

40

20

0

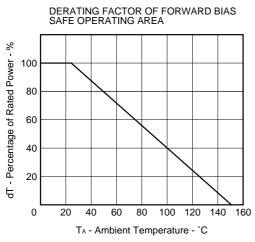
lo - Drain Current - mA

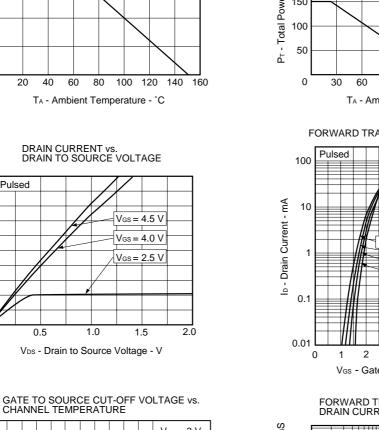
\*

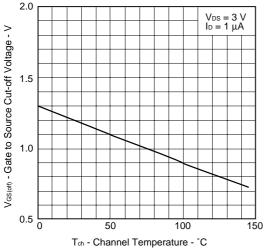
Pulsed

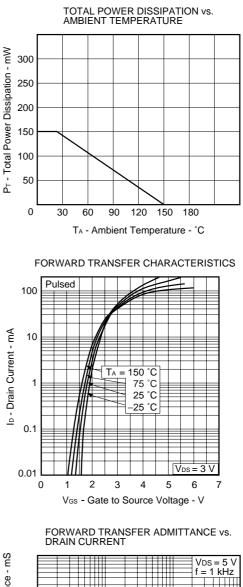
0.5

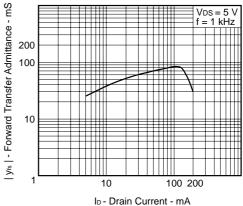
## TYPICAL CHARACTERISTICS (TA = 25 °C)



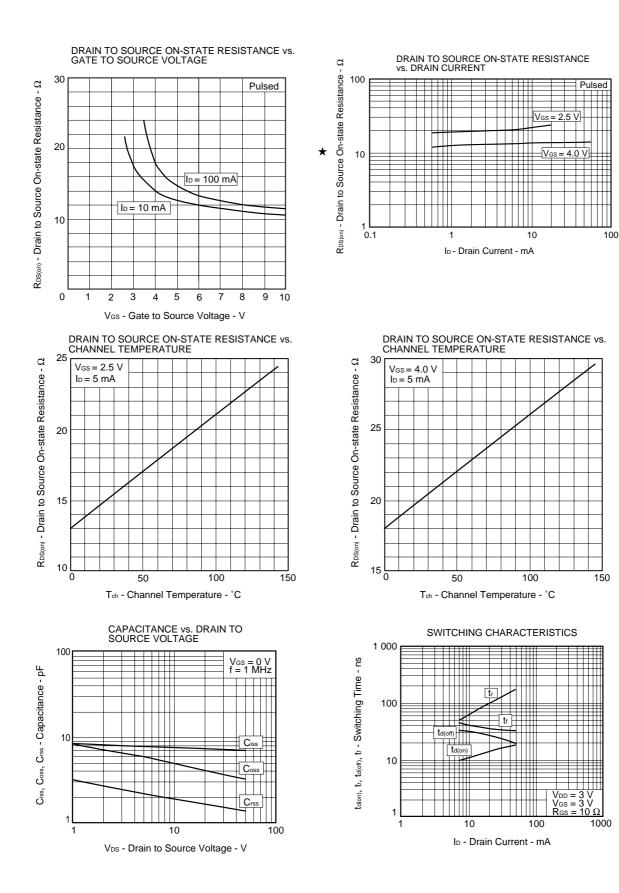




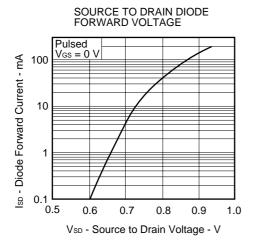




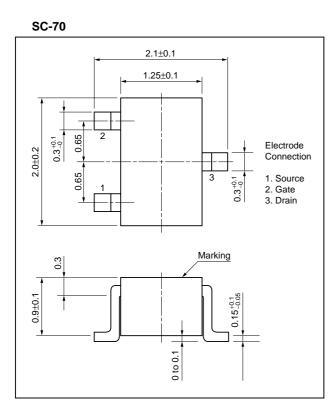
Data Sheet D14209EJ2V0DS00



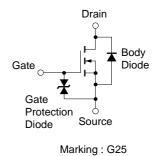
NEC



# PACKAGE DRAWING (Unit: mm)







**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

# NEC

[MEMO]

- The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version.
- No part of this document may be copied or reproduced in any form or by any means without the prior written consent of NEC Corporation. NEC Corporation assumes no responsibility for any errors which may appear in this document.
- NEC Corporation does not assume any liability for infringement of patents, copyrights or other intellectual property
  rights of third parties by or arising from use of a device described herein or any other liability arising from use
  of such device. No license, either express, implied or otherwise, is granted under any patents, copyrights or other
  intellectual property rights of NEC Corporation or others.
- Descriptions of circuits, software, and other related information in this document are provided for illustrative purposes in semiconductor product operation and application examples. The incorporation of these circuits, software, and information in the design of the customer's equipment shall be done under the full responsibility of the customer. NEC Corporation assumes no responsibility for any losses incurred by the customer or third parties arising from the use of these circuits, software, and information.
- While NEC Corporation has been making continuous effort to enhance the reliability of its semiconductor devices, the possibility of defects cannot be eliminated entirely. To minimize risks of damage or injury to persons or property arising from a defect in an NEC semiconductor device, customers must incorporate sufficient safety measures in its design, such as redundancy, fire-containment, and anti-failure features.
- NEC devices are classified into the following three quality grades:
   "Standard", "Special", and "Specific". The Specific quality grade applies only to devices developed based on a customer designated "quality assurance program" for a specific application. The recommended applications of a device depend on its quality grade, as indicated below. Customers must check the quality grade of each device before using it in a particular application.
  - Standard: Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots
  - Special: Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support)
  - Specific: Aircraft, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems or medical equipment for life support, etc.

The quality grade of NEC devices is "Standard" unless otherwise specified in NEC's Data Sheets or Data Books. If customers intend to use NEC devices for applications other than those specified for Standard quality grade, they should contact an NEC sales representative in advance.