TOSHIBA 2SK2835

TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE (π -MOS V)

2 S K 2 8 3 5

HIGH SPEED, HIGH CURRENT SWITCHING APPLICATIONS CHOPPER REGULATOR, DC-DC CONVERTER AND MOTOR DRIVE **APPLICATIONS**

Low Drain-Source ON Resistance : $R_{DS(ON)} = 0.56\Omega$ (Typ.)

High Forward Transfer Admittance : $|Y_{fS}| = 4.5S$ (Typ.)

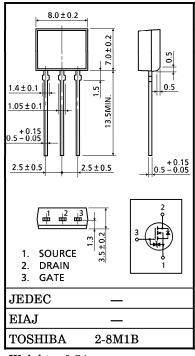
Low Leakage Current : $I_{DSS} = 100 \mu A$ (Max.) ($V_{DS} = 200 V$)

Enhancement-Mode : $V_{th} = 1.5 \sim 3.5 V \text{ (V}_{DS} = 10 V, I_D = 1 \text{mA)}$

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERIS	SYMBOL	RATING	UNIT		
Drain-Source Voltage	$v_{ m DSS}$	200	V		
Drain-Gate Voltage (RG	$v_{ m DGR}$	200	V		
Gate-Source Voltage	v_{GSS}	±20	V		
Drain Current	DC	$I_{\mathbf{D}}$	5	A	
	Pulse	I_{DP}	20		
Drain Power Dissipation	$P_{\mathbf{D}}$	1.3	W		
Single Pulse Avalanche	EAS	65	mJ		
Avalanche Current	I_{AR}	5	A		
Repetitive Avalanche En	$\mathbf{E}_{\mathbf{A}\mathbf{R}}$	0.13	mJ		
Channel Temperature	$\mathrm{T_{ch}}$	150	°C		
Storage Temperature Ra	$\mathrm{T_{stg}}$	-55~150	°C		

INDUSTRIAL APPLICATIONS Unit in mm



Weight: 0.54g

THERMAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Ambient	R _{th (ch-a)}	96.1	°C/W

Note:

Repetitive rating; Pulse Width Limited by Max. junction temperature.

** V_{DD} =50V, Starting T_{ch} =25°C, L=4.2mH, R_{G} =25 Ω , $I_{AR} = 5A$

This transistor is an electrostatic sensitive device. Please handle with caution.

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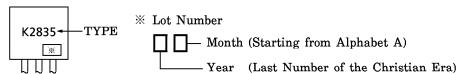
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARAC	CTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage	Current	I_{GSS}	$V_{GS} = \pm 16V, V_{DS} = 0V$	_	_	±10	μ A
Drain Cut-off	Current	$I_{ m DSS}$	$V_{DS} = 200V, V_{GS} = 0V$	_	_	100	μ A
Drain-Source Voltage	Breakdown		$I_D=10$ mA, $V_{GS}=0$ V	200	_	_	V
Gate Threshol	ld Voltage	v_{th}	$V_{DS}=10V, I_{D}=1mA$	1.5	_	3.5	V
Drain-Source	ON Resistance	R _{DS} (ON)	$V_{GS} = 10V, I_D = 2.5A$	_	0.56	0.8	Ω
Forward Tran Admittance	sfer	Y _{fs}	$V_{DS} = 10V, I_{D} = 2.5A$	2.0	4.5	_	S
Input Capacitance		C_{iss}		_	440	_	pF
Reverse Transfer Capacitance		C_{rss}	$V_{DS} = 10V, V_{GS} = 0V$ f=1MHz	_	35	_	
Output Capacitance		C_{oss}		_	120	_	
Switching Time	Rise Time	t _r	$V_{GS} \stackrel{10V}{\underset{0V}{\text{Jos}}} \stackrel{I_{D}=2.5\text{A}}{\underset{R_{L}=40\Omega}{\text{O}}} V_{out}$ $V_{IN}: t_r, t_f < 5\text{ns}, t_w = 10\mu\text{s}$	_	15	_	
	Turn-on Time	ton		_	20	_	ns
	Fall Time	t_f		_	15	_	ns
	Turn-off Time	t _{off}			60	_	
Total Gate Charge (Gate-Source Plus Gate-Drain)		$\mathbf{Q}_{\mathbf{g}}$	$V_{\mathrm{DD}} \stackrel{.}{=} 100 \mathrm{V}, \ V_{\mathrm{GS}} = 10 \mathrm{V}$ $I_{\mathrm{D}} = 5 \mathrm{A}$	_	10	_	nC
Gate-Source Charge		$\mathbf{Q}_{\mathbf{g}\mathbf{s}}$		_	6	_] "[
Gate-Drain ("Miller") Charge		$\mathbf{Q}_{\mathbf{gd}}$		_	4	_	

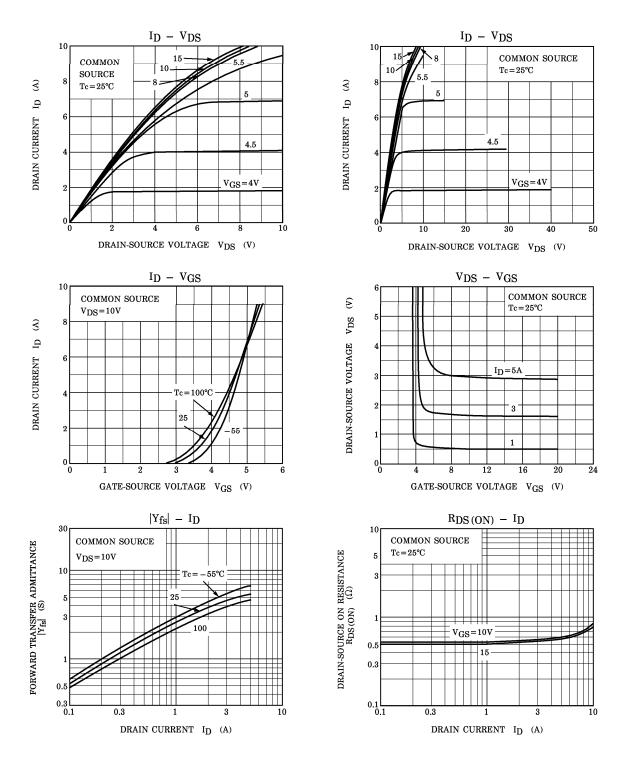
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

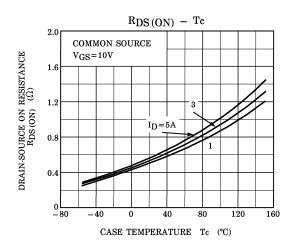
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	$I_{ m DR}$	_	_	_	5	A
Pulse Drain Reverse Current	$I_{ m DRP}$	_	_	_	20	A
Diode Forward Voltage	$v_{ m DSF}$	$I_{DR}=5A, V_{GS}=0V$	_	_	-2.0	V
Reverse Recovery Time	t_{rr}	$I_{DR}=5A, V_{GS}=0V$	_	150	_	ns
Reverse Recovery Charge	Q_{rr}	$dI_{ m DR}$ / dt = 100A / μs	_	0.45	_	μ C

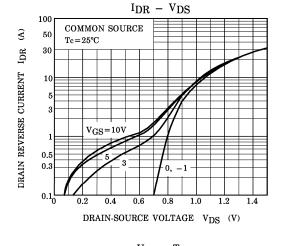
MARKING

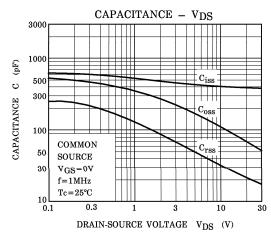


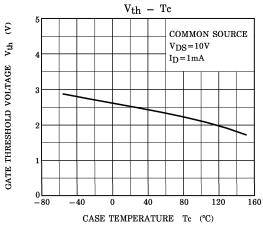
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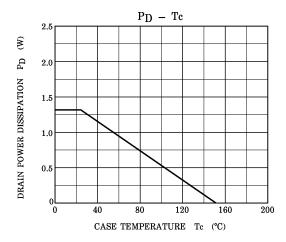


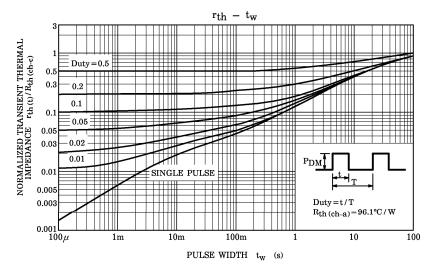


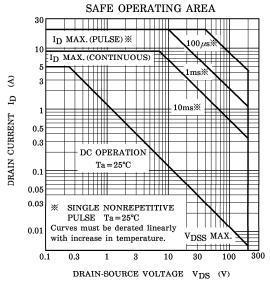


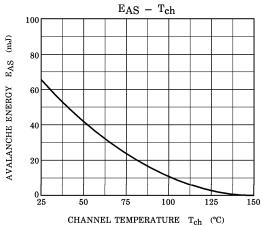


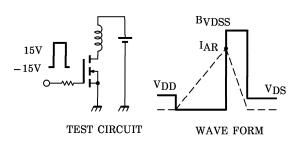












$$\begin{array}{ll} \text{Peak I}_{AR} = 5\text{A, R}_{G} = 25\Omega & \text{E}_{AS} = \frac{1}{2} \cdot \text{L} \cdot \text{I}^{2} \cdot (\frac{\text{BVDSS}}{\text{BVDSS} - \text{V}_{DD}}) \end{array}$$