

TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE (π -MOS ν)

2SK2750

HIGH SPEED, HIGH CURRENT SWITCHING APPLICATIONS

CHOPPER REGULATOR, DC-DC CONVERTER AND MOTOR DRIVE APPLICATIONS

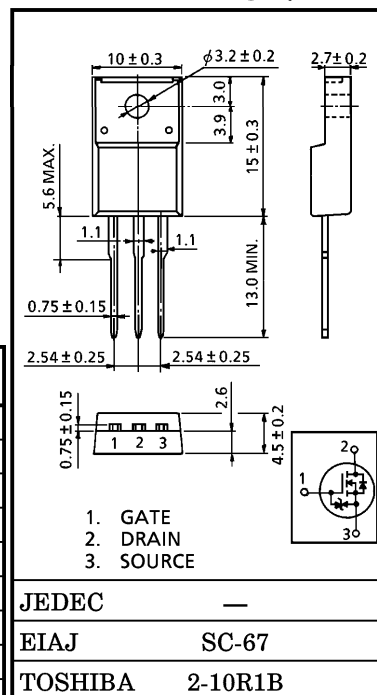
INDUSTRIAL APPLICATIONS

Unit in mm

- Low Drain-Source ON Resistance : $R_{DS(ON)} = 1.7\Omega$ (Typ.)
- High Forward Transfer Admittance : $|Y_{fs}| = 3.0S$ (Typ.)
- Low Leakage Current : $I_{DSS} = 100\mu A$ (Max.) ($V_{DS} = 600V$)
- Enhancement-Mode : $V_{th} = 2.0 \sim 4.0V$ ($V_{DS} = 10V, I_D = 1mA$)

MAXIMUM RATINGS ($T_a = 25^\circ C$)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Drain-Source Voltage		V_{DSS}	600	V
Drain-Gate Voltage ($R_{GS} = 20k\Omega$)		V_{DGR}	600	V
Gate-Source Voltage		V_{GSS}	± 30	V
Drain Current	DC	I_D	3.5	A
	Pulse	I_{DP}	14	A
Drain Power Dissipation ($T_c = 25^\circ C$)		P_D	35	W
Single Pulse Avalanche Energy**		E_{AS}	201	mJ
Avalanche Current		I_{AR}	3.5	A
Repetitive Avalanche Energy*		E_{AR}	3.5	mJ
Channel Temperature		T_{ch}	150	$^\circ C$
Storage Temperature Range		T_{stg}	$-55 \sim 150$	$^\circ C$



Weight : 1.9g

THERMAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Case	$R_{th(ch-c)}$	3.57	$^\circ C/W$
Thermal Resistance, Channel to Ambient	$R_{th(ch-a)}$	62.5	$^\circ C/W$

Note ;

* Repetitive rating ; Pulse Width Limited by Max. junction temperature.

** $V_{DD} = 90V$, Starting $T_{ch} = 25^\circ C$, $L = 28.8mH$, $R_G = 25\Omega$, $I_{AR} = 3.5A$

This transistor is an electrostatic sensitive device. Please handle with caution.

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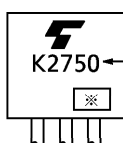
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Gate Leakage Current	I_{GSS}	$V_{GS} = \pm 25V, V_{DS} = 0V$	—	—	± 10	μA	
Gate-Source Breakdown Voltage	$V_{(BR)GSS}$	$I_G = \pm 10\mu A, V_{DS} = 0V$	± 30	—	—	V	
Drain Cut-off Current	I_{DSS}	$V_{DS} = 600V, V_{GS} = 0V$	—	—	100	μA	
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = 10mA, V_{GS} = 0V$	600	—	—	V	
Gate Threshold Voltage	V_{th}	$V_{DS} = 10V, I_D = 1mA$	2.0	—	4.0	V	
Drain-Source ON Resistance	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 1.8A$	—	1.7	2.2	Ω	
Forward Transfer Admittance	$ Y_{fs} $	$V_{DS} = 10V, I_D = 1.8A$	2.0	3.0	—	S	
Input Capacitance	C_{iss}	$V_{DS} = 25V, V_{GS} = 0V, f = 1MHz$	—	800	—	pF	
Reverse Transfer Capacitance	C_{rss}		—	6	—		
Output Capacitance	C_{oss}		—	65	—		
Switching Time	Rise Time	t_r		—	15	—	ns
	Turn-on Time	t_{on}		—	50	—	
	Fall Time	t_f		—	15	—	
	Turn-off Time	t_{off}		$V_{IN} : t_r, t_f < 5ns,$ $Duty \leq 1\%, t_w = 10\mu s$	—	85	
Total Gate Charge (Gate-Source Plus Gate-Drain)	Q_g	$V_{DD} \approx 400V, V_{GS} = 10V, I_D = 3.5A$	—	20	—	nC	
Gate-Source Charge	Q_{gs}		—	10	—		
Gate-Drain ("Miller") Charge	Q_{gd}		—	10	—		

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	I_{DR}	—	—	—	3.5	A
Pulse Drain Reverse Current	I_{DRP}	—	—	—	14	A
Diode Forward Voltage	V_{DSF}	$I_{DR} = 3.5A, V_{GS} = 0V$	—	—	-1.7	V
Reverse Recovery Time	t_{rr}	$I_{DR} = 3.5A, V_{GS} = 0V$	—	400	—	ns
Reverse Recovery Charge	Q_{rr}	$dI_{DR} / dt = 100A / \mu s$	—	2.6	—	μC

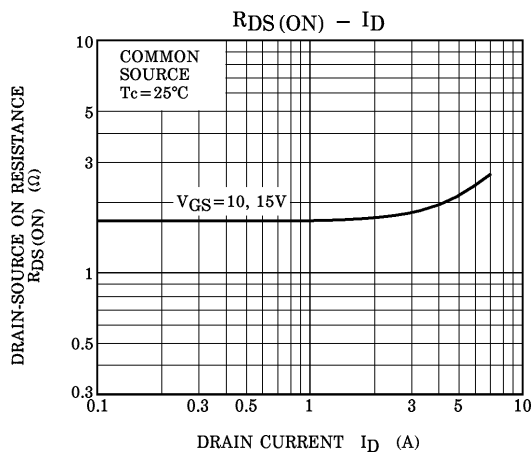
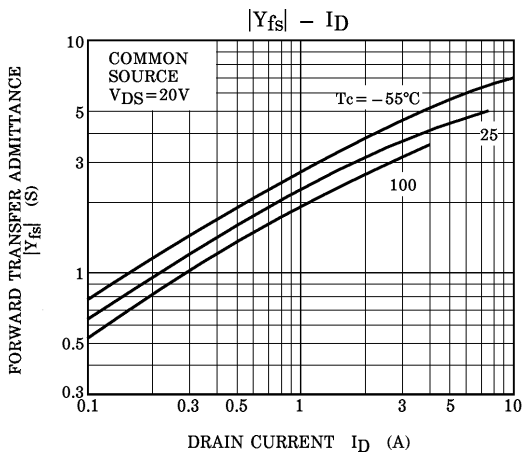
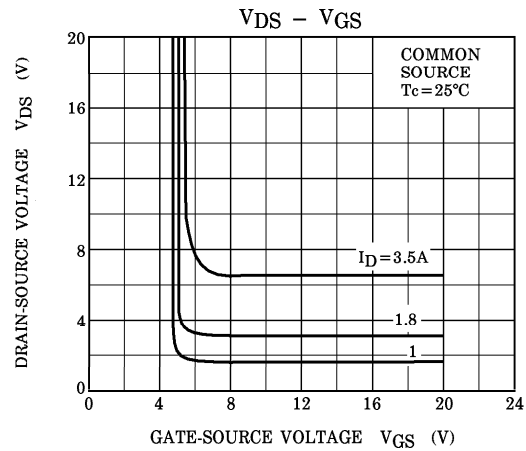
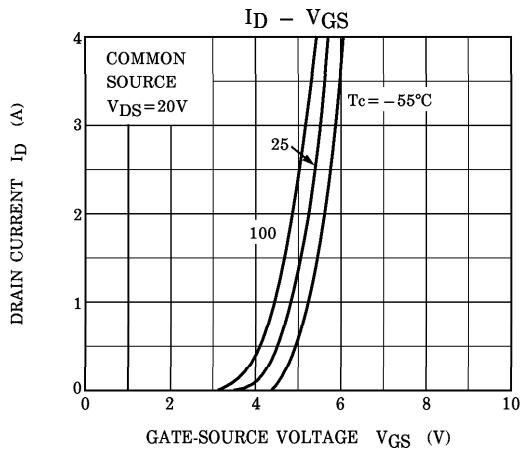
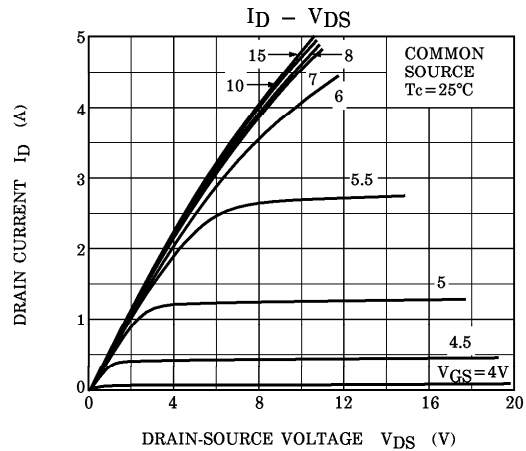
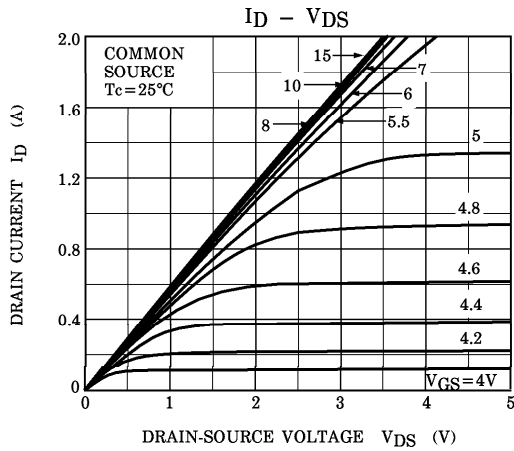
MARKING

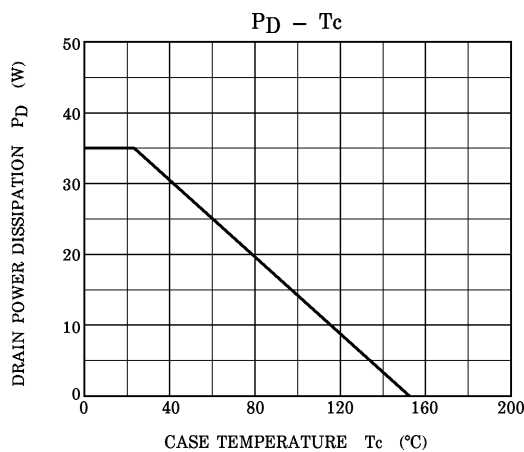
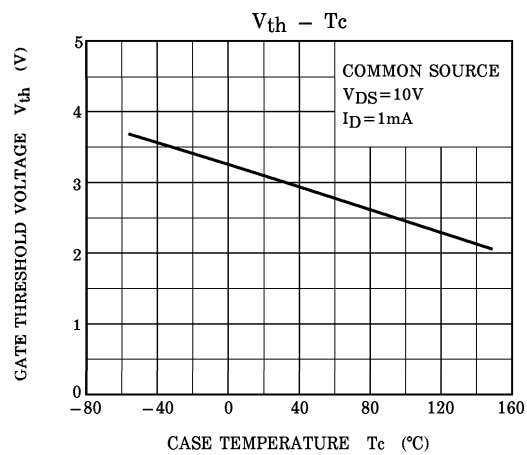
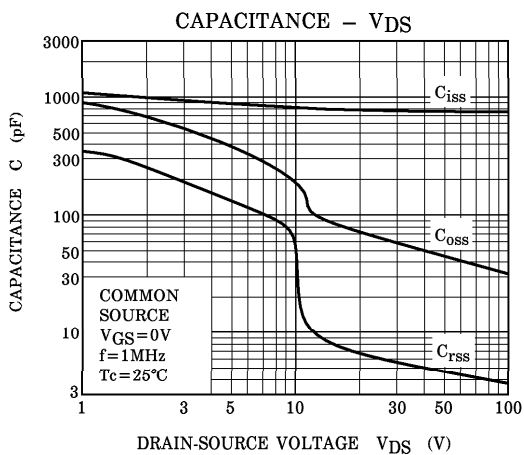
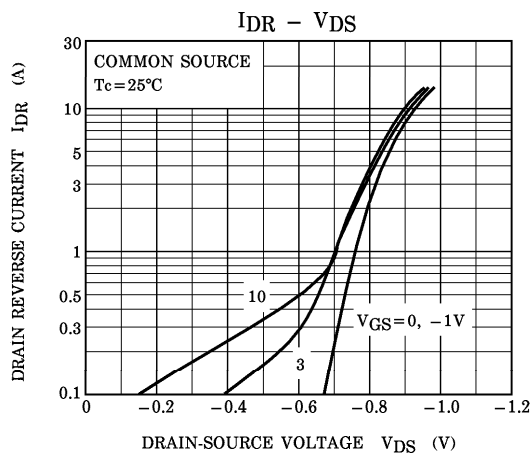
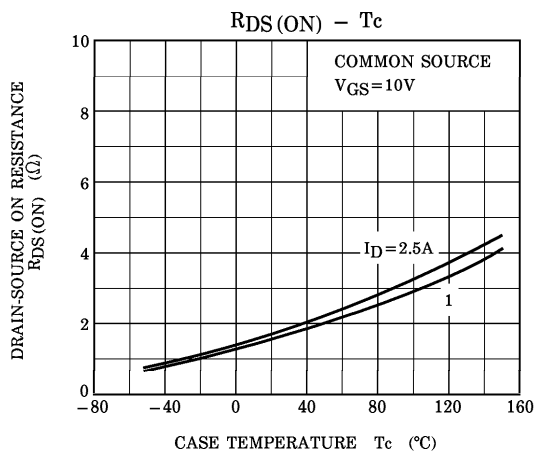


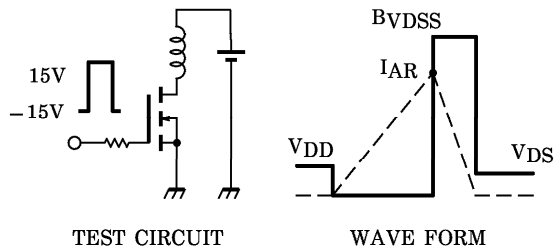
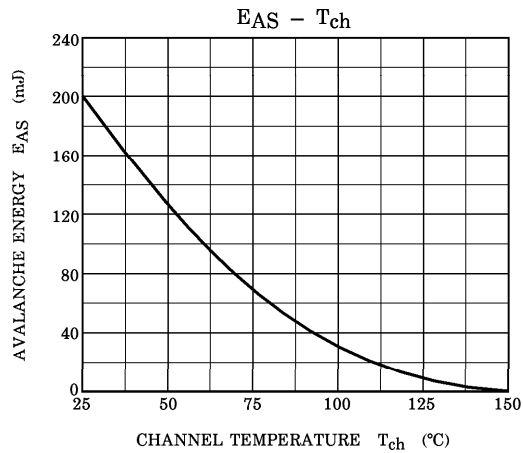
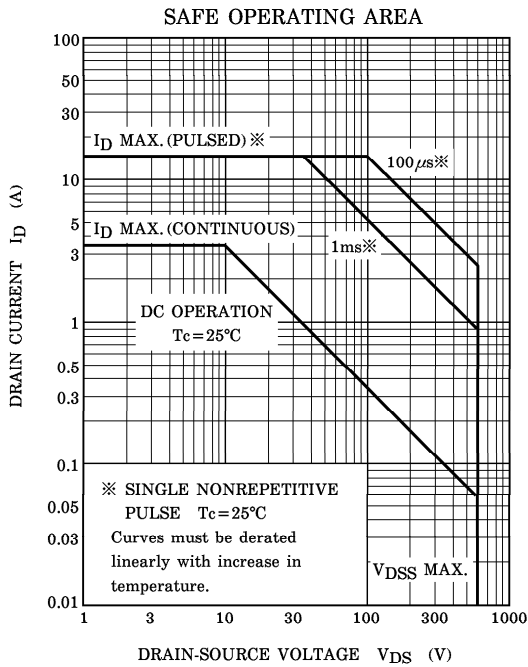
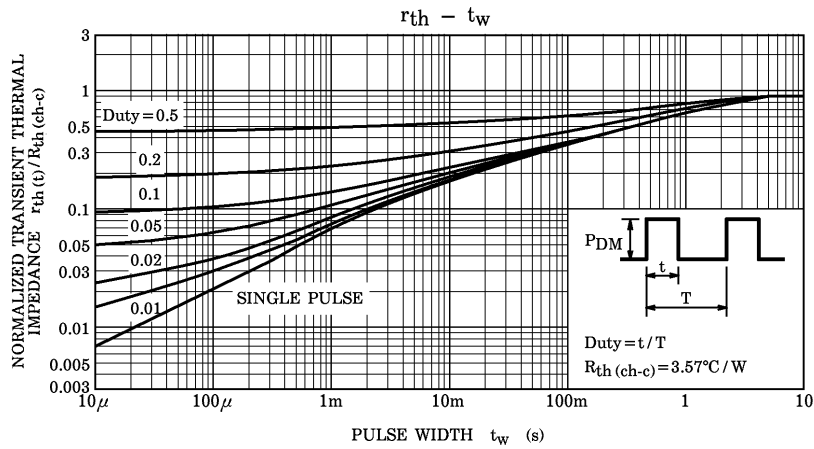
※ Lot Number

□ □ — Month (Starting from Alphabet A)

— Year (Last Number of the Christian Era)







Peak $I_{AR} = 3.5A$, $R_G = 25\Omega$
 $V_{DD} = 90V$, $L = 28.8mH$

$$EAS = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - V_{DD}} \right)$$