TOSHIBA 2SK2661

TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE (π -MOS V)

2 S K 2 6 6 1

HIGH SPEED, HIGH CURRENT SWITCHING APPLICATIONS CHOPPER REGULATOR, DC-DC CONVERTER AND MOTOR DRIVE **APPLICATIONS**

Low Drain-Source ON Resistance : $R_{DS(ON)} = 1.35\Omega$ (Typ.)

High Forward Transfer Admittance : $|Y_{fs}| = 4.0S$ (Typ.)

Low Leakage Current : $I_{DSS} = 100 \mu A \text{ (Max.) (V}_{DS} = 500 \text{V)}$

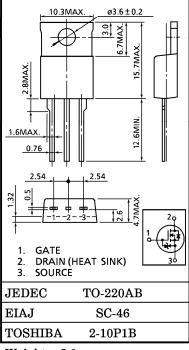
Enhancement-Mode : $V_{th} = 2.0 \sim 4.0 \text{V} (V_{DS} = 10 \text{V}, I_D = 1 \text{mA})$

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERIST	SYMBOL RATING		UNIT	
Drain-Source Voltage	$v_{ m DSS}$	500	V	
Drain-Gate Voltage (RGS	$v_{ m DGR}$	500	V	
Gate-Source Voltage	v_{GSS}	±30	V	
Drain Current	DC	$I_{\mathbf{D}}$	5	Α
	Pulse	I_{DP}	20	Α
Drain Power Dissipation	$P_{\mathbf{D}}$	75	W	
Single Pulse Avalanche	EAS	180	mJ	
Avalanche Current	I_{AR}	5	Α	
Repetitive Avalanche En	E_{AR}	7.5	mJ	
Channel Temperature	$\mathrm{T_{ch}}$	150	°C	
Storage Temperature Ran	$\mathrm{T_{stg}}$	-55~150	°C	

INDUSTRIAL APPLICATIONS

Unit in mm



Weight: 2.0g

THERMAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Case	R _{th (ch-c)}	1.67	°C/W
Thermal Resistance, Channel to Ambient	R _{th (ch-a)}	83.3	°C/W

Note;

- * Repetitive rating; Pulse Width Limited by Max. junction temperature.
- ** V_{DD} =90V, Starting T_{ch} =25°C, L=12.2mH, R_G =25 Ω ,

This transistor is an electrostatic sensitive device. Please handle with caution.

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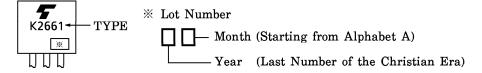
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARAC	CTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage	Current	IGSS	$V_{GS} = \pm 25V, V_{DS} = 0V$	_	_	±10	μ A
Gate-Source I Voltage	Breakdown		$I_{G} = \pm 10 \mu A, V_{DS} = 0 V$	±30	_	_	V
Drain Cut-off	Current	IDSS	$V_{DS}=500V, V_{GS}=0V$	_	_	100	μ A
Drain-Source Voltage	Breakdown	V (BR) DSS	I _D =10mA, V _{GS} =0V	500	_	_	V
Gate Thresho	ld Voltage	$V_{ m th}$	$V_{DS}=10V, I_{D}=1mA$	2.0	_	4.0	V
Drain-Source	ON Resistance	R _{DS} (ON)	$V_{GS} = 10V, I_D = 2.5A$	_	1.35	1.50	Ω
Forward Tran Admittance	sfer	Y _{fs}	$V_{DS} = 10V, I_D = 2.5A$	2.5	4.0	_	S
Input Capacit	ance	Ciss		_	780	_	
Reverse Transfer Capacitance		C_{rss}	V_{DS} =25V, V_{GS} =0V, f=1MHz	_	60	_	рF
Output Capacitance		Coss		_	200	_	
Switching Time Rise Time Turn-on Tin Fall Time	Rise Time	t _r	V _{GS} 10V $^{I_D=2.5A}$ OV OUT	_	12	_	
	Turn-on Time	t _{on}	$R_{L}=$ $R_{L}=$ 90Ω $V_{DD}=225V$	_	25	_	ns
	Fall Time	tf		_	15	_	115
	Turn-off Time	t _{off}	$egin{aligned} ext{V}_{ ext{IN}}: ext{t}_{ ext{r}}, ext{t}_{ ext{f}} {<} 5 ext{ns}, \ ext{Duty} \leq 1 \%, ext{t}_{ ext{W}} {=} 10 \mu ext{s} \end{aligned}$	_	60	_	
Total Gate Charge (Gate- Source Plus Gate-Drain)		$\mathbf{Q}_{\mathbf{g}}$	V	_	17	_	nC
Gate-Source Charge		$\mathbf{Q}_{\mathbf{g}\mathbf{s}}$	$V_{DD} = 400V, V_{GS} = 10V, I_D = 5A$	_	11	_	nC
Gate-Drain ("Miller") Charge		$\mathbf{Q}_{\mathbf{gd}}$		_	6	_	

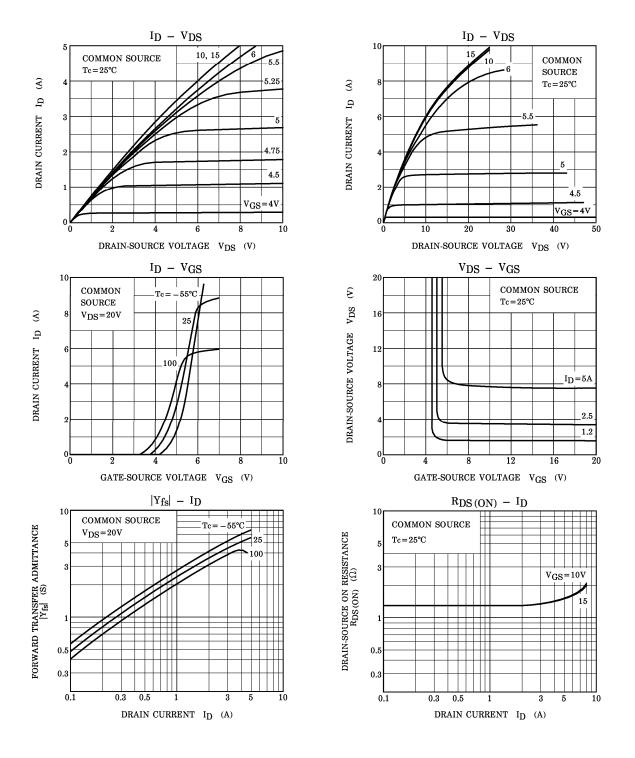
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

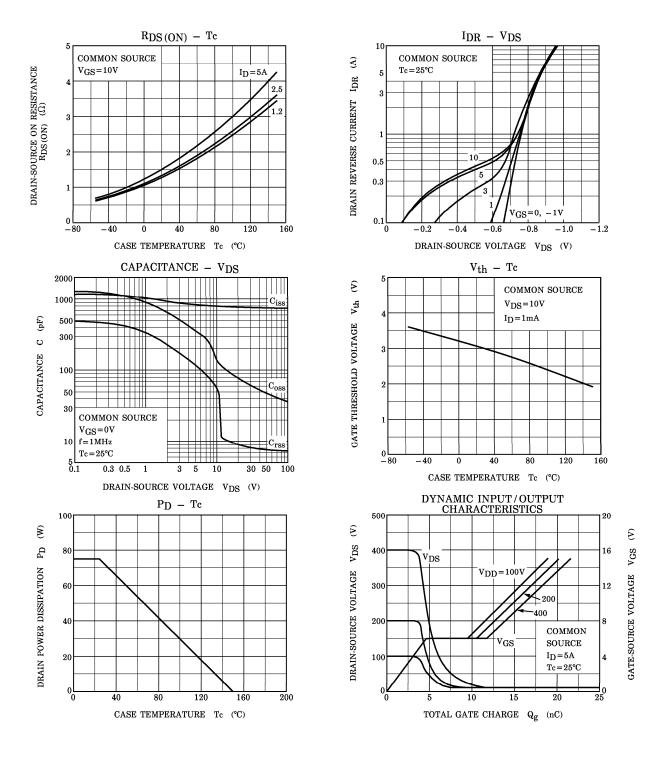
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	$I_{ m DR}$	_	_	_	5	A
Pulse Drain Reverse Current	$I_{ m DRP}$	_	_	_	20	A
Diode Forward Voltage	$V_{ m DSF}$	$I_{DR}=5A, V_{GS}=0V$	_	_	-1.7	V
Reverse Recovery Time	t _{rr}	$I_{DR}=5A, V_{GS}=0V$	_	1400	_	ns
Reverse Recovery Charge	Q_{rr}	$dI_{ m DR}$ / dt = 100A / μ s	_	9	_	μC

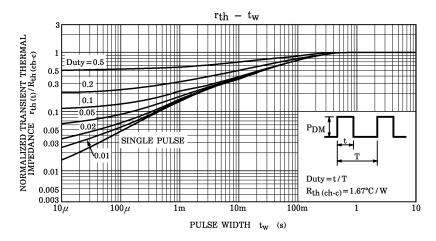
MARKING

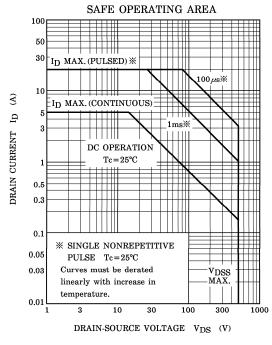


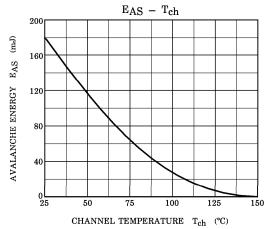
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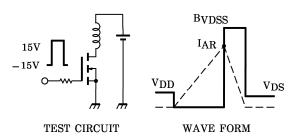












$$\begin{array}{ll} \text{Peak I}_{AR} = 5\text{A, R}_{G} = 25\Omega \\ \text{V}_{DD} = 90\text{V, L} = 12.2\text{mH} \end{array} \quad \text{E}_{AS} = \frac{1}{2} \cdot \text{L} \cdot \text{I}^{2} \cdot (\frac{\text{BVDSS}}{\text{BVDSS} - \text{V}_{DD}})$$