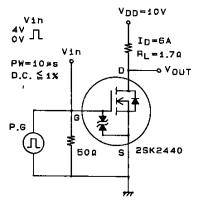


Features

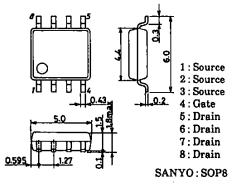
- · Low ON resistance.
- · Ultrahigh-speed switching.
- · 2.5V drive.

Absolute Maximum Ratings at Ta = 25°C unit						
Drain-to-Source Voltage	V_{DSS}			20	v	
Gate-to-Source Voltage	V _{GSS}		-	±10	v	
Drain Current(DC)	ID		_	6	À	
Drain Current(Pulse)	I _{DP}	PW≦10µs, duty cycle≦1%		48	A	
Allowable Power Dissipation	PD	Mounted on ceramic board		2.0	w	
•	5	$(1200 \text{mm}^2 \times 0.8 \text{mm})$			••	
Channel Temperature	Tch	• • • • • • • • • • • • • • • • • • • •		150	°C	
Storage Temperature	Tstg		-55 to $+150$		°Č	
	-				-	
Electrical Characteristics at Ta =	25°C		min	typ	max	unit
D-S Breakdown Voltage	V _{(BR)DSS}	$I_D = 1 m A, V_{GS} = 0$	20			v
Zero-Gate Voltage	I _{DSS}	$V_{DS} = 16V, V_{GS} = 0$			100	μÂ
Drain Current						F
Gate-to-Source Leakage Current	I _{GSS}	$V_{GS} = \pm 8V, V_{DS} = 0$			±10	μ Α
Cutoff Voltage	V _{GS(off)}	$V_{DS} = 10V, I_D = 1mA$	0.4		1.4	`v
Forward Transfer Admittance		$V_{DS} = 10V, I_{D} = 6A$	10	14		S
Static Drain-to-Source		$I_D = 6A, V_{GS} = 4V$		30	38	mΩ
ON-State Resistance		$I_{D} = 2A, V_{GS} = 2.5V$		40	58	$m\Omega$
Input Capacitance	Ciss	$V_{DS} = 10V, f = 1MHz$		1000		pF
Output Capacitance	Coss	$V_{DS} = 10V, f = 1MHz$		750		pF
Reverse Transfer Capacitance	Crss	$V_{DS} = 10V, f = 1MHz$		400		pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit.		25		ns
Rise Time	tr	"		135		ns
Turn-OFF Delay Time	$t_{d(off)}$	//		135		ns
Fall Time	tf	//		150		ns
Diode Forward Voltage	V _{SD}	$I_S = 6A, V_{GS} = 0$		1.0	1.2	v

Switching Time Test Circuit



Package Dimensions 2116 (unit:mm)



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