TOSHIBA 2SK2417

TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE (π -MOS V)

2 S K 2 4 1 7

HIGH SPEED, HIGH CURRENT SWITCHING APPLICATIONS CHOPPER REGULATOR, DC-DC CONVERTER AND MOTOR DRIVE **APPLICATIONS**

Low Drain-Source ON Resistance : $R_{DS(ON)} = 0.42\Omega$ (Typ.)

High Forward Transfer Admittance : $|Y_{fs}| = 7.5S$ (Typ.)

Low Leakage Current

: $I_{DSS} = 100 \mu A \text{ (Max.)} (V_{DS} = 250 \text{ V})$

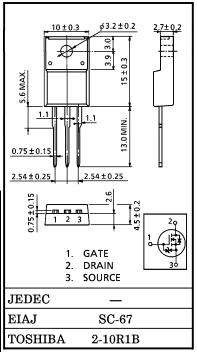
Enhancement-Mode

: $V_{th} = 1.5 \sim 3.5 V (V_{DS} = 10 V, I_D = 1 mA)$

MAXIMUM RATINGS (Ta = 25°C)

| CHARACTERIS | SYMBOL | RATING | UNIT | |
|-------------------------|----------------------------|-------------------|----------------------|---|
| Drain-Source Voltage | $V_{ m DSS}$ | 250 | V | |
| Drain-Gate Voltage (RG | $v_{ m DGR}$ | 250 | V | |
| Gate-Source Voltage | v_{GSS} | ±20 | V | |
| Drain Current | DC | $I_{\mathbf{D}}$ | 7.5 | A |
| | Pulse | I_{DP} | 30 | A |
| Drain Power Dissipation | P_{D} | 30 | W | |
| Single Pulse Avalanche | EAS | 110 | mJ | |
| Avalanche Current | I_{AR} | 7.5 | A | |
| Repetitive Avalanche En | E_{AR} | 3 | mJ | |
| Channel Temperature | $\mathrm{T_{ch}}$ | 150 | °C | |
| Storage Temperature Ra | $\mathrm{T_{stg}}$ | -55~150 | $^{\circ}\mathrm{C}$ | |

INDUSTRIAL APPLICATIONS Unit in mm



Weight: 1.9g

THERMAL CHARACTERISTICS

| CHARACTERISTIC | SYMBOL | MAX. | UNIT |
|--|------------------------|------|------|
| Thermal Resistance, Channel to Case | R _{th (ch-c)} | 4.16 | °C/W |
| Thermal Resistance, Channel to Ambient | R _{th (ch-a)} | 62.5 | °C/W |

Note;

- * Repetitive rating; Pulse Width Limited by Max. junction temperature.
- ** V_{DD} =50V, Starting T_{ch} =25°C, L=3.3mH, R_{G} =25 Ω ,

This transistor is an electrostatic sensitive device. Please handle with caution.

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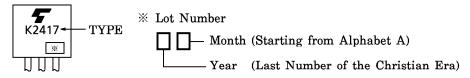
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

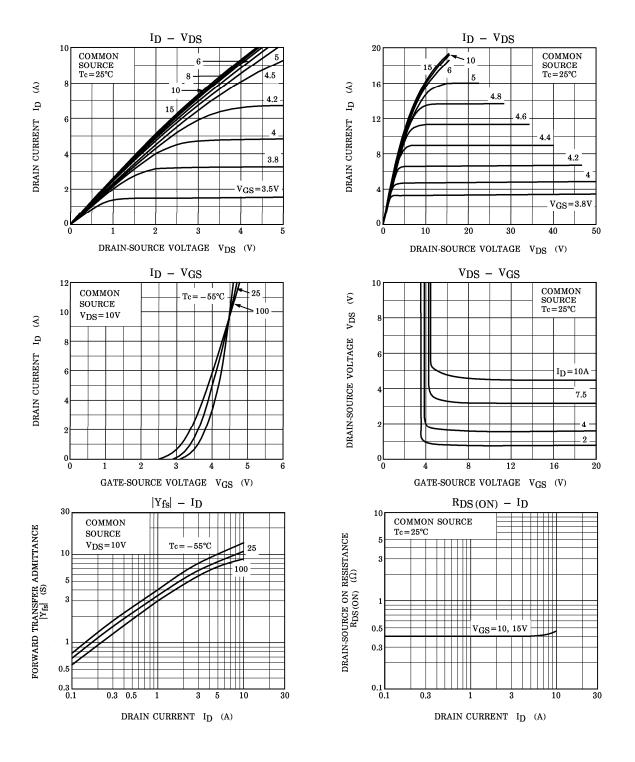
| | | • • | | | | | |
|---|---------------|-------------------------------------|--|------|------|------|---------|
| CHARACTERISTIC | | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNIT |
| Gate Leakage Current | | IGSS | $V_{GS} = \pm 16V, V_{DS} = 0V$ | _ | _ | ±10 | μ A |
| Drain Cut-of | f Current | IDSS | $V_{DS} = 250V, V_{GS} = 0V$ | _ | _ | 100 | μ A |
| Drain-Source Voltage | Breakdown | V (BR) DSS | $I_D=10$ mA, $V_{GS}=0$ V | 250 | _ | _ | V |
| Gate Thresho | old Voltage | $V_{ m th}$ | $V_{DS}=10V, I_{D}=1mA$ | 1.5 | _ | 3.5 | V |
| Drain-Source ON Resistance | | R _{DS} (ON) | $V_{GS} = 10V, I_D = 3.5A$ | _ | 0.42 | 0.5 | Ω |
| Forward Transfer Admittance | | Y _{fs} | $V_{DS} = 10V, I_D = 3.5A$ | 4 | 7.5 | _ | S |
| Input Capacitance Reverse Transfer Capacitance | | $\mathrm{c}_{\mathrm{iss}}$ | V _{DS} =10V, V _{GS} =0V, f=1MHz | _ | 700 | _ | pF |
| | | C _{rss} | | _ | 80 | _ | |
| Output Capacitance | | Coss | | _ | 270 | _ | |
| Switching Time | Rise Time | t _r | $V_{GS} \xrightarrow{10V} I_{D} = 3.5A V_{OUT}$ $R_{L} = 28.6\Omega$ $V_{DD} = 10V$ $R_{L} = 28.6\Omega$ | _ | 10 | _ | |
| | Turn-on Time | t _{on} | | _ | 20 | _ | ng |
| | Fall Time | tf | | | 10 | _ | ns |
| | Turn-off Time | t _{off} | $V_{\rm IN}: t_{\rm r}, t_{\rm f}{<}5{ m ns}, \ 100{ m V} \ { m Duty} \le 1\%, \ t_{ m W}{=}10\mu{ m s}$ | _ | 70 | _ | |
| Total Gate Charge (Gate-Source Plus Gate-Drain) | | $\mathbf{Q}_{\mathbf{g}}$ | V _{DD} ≒200V, V _{GS} =10V | _ | 20 | _ | C |
| Gate-Source Charge | | $\mathbf{Q}_{\mathbf{g}\mathbf{s}}$ | $I_{D}=7.5A$ | | 13 | | nC |
| Gate-Drain ("Miller") Charge | | $\mathbf{Q}_{\mathbf{gd}}$ | | | 7 | _ | |

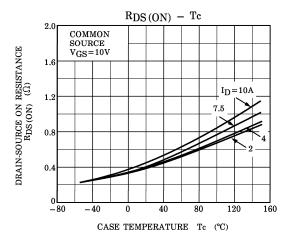
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

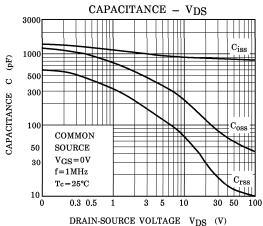
| CHARACTERISTIC | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNIT |
|-------------------------------------|--------------|--------------------------------|------|------|------|---------|
| Continuous Drain Reverse Current | $I_{ m DR}$ | _ | _ | _ | 7.5 | A |
| Pulse Drain Reverse Current | $I_{ m DRP}$ | _ | _ | _ | 30 | A |
| Diode Forward Voltage | $v_{ m DSF}$ | I_{DR} =7.5A, V_{GS} =0V | _ | _ | -2.0 | V |
| Reverse Recovery Time | t_{rr} | I_{DR} =7.5A, V_{GS} =0V | _ | 180 | _ | ns |
| Reverse Recovery Charge | Q_{rr} | $ m dI_{DR}/dt\!=\!100A/\mu s$ | _ | 1.1 | _ | μ C |

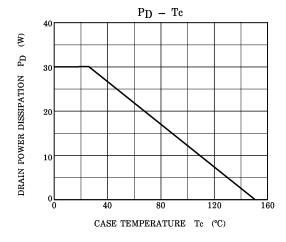
MARKING

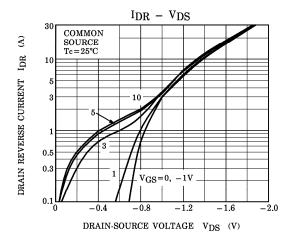


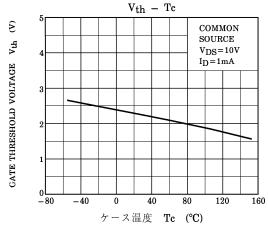


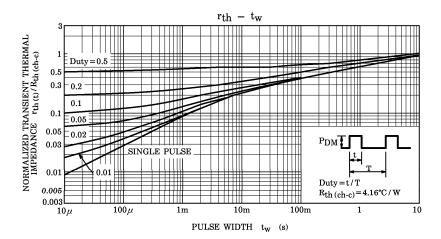


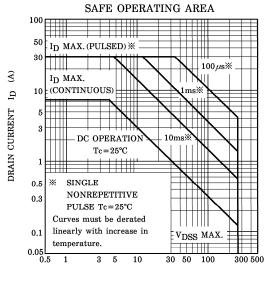


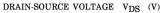


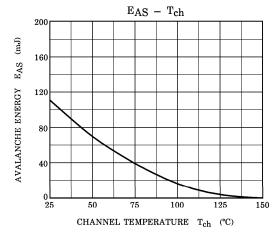


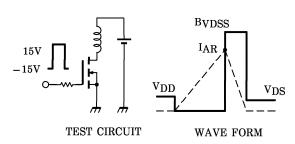












$$\begin{array}{ll} \text{Peak I}_{AR} = 7.5\text{A}, \; R_G = 25\Omega \\ V_{DD} = 50\text{V}, \; L = 3.3\text{mH} \end{array} \quad E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot (\frac{B\text{VDSS}}{B\text{VDSS} - \text{V}_{DD}})$$