

# MOS FIELD EFFECT TRANSISTORS 2SK2359/2SK2360

# SWITCHING N-CHANNEL POWER MOS FET INDUSTRIAL USE

#### **DESCRIPTION**

The 2SK2359, 2SK2359-Z/2SK2360, 2SK2360-Z is N-Channel MOS Field Effect Transistor designed for high voltage switching applications.

### **FEATURES**

· Low On-Resistance

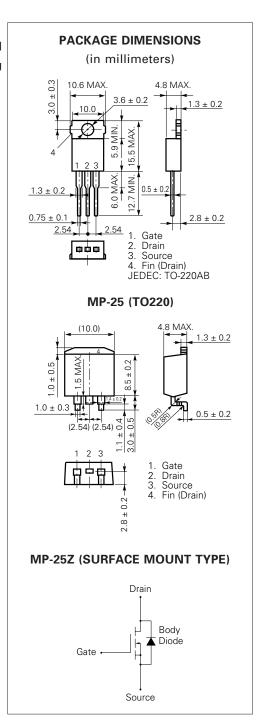
2SK2359:  $R_{DS(on)} = 0.9 \Omega$  (Vgs = 10 V, ID = 4.0 A) 2SK2360:  $R_{DS(on)} = 1.0 \Omega$  (Vgs = 10 V, ID = 4.0 A)

- Low Ciss Ciss = 1050 pF TYP.
- High Avalanche Capability Ratings

## ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

Drain to Source Voltage(2SK2359/2SK2360)	VDSS	450/500	V
Gate to Source Voltage	Vgss	±30	V
Drain Current (DC)	ID(DC)	±7.0	Α
Drain Current (pulse)*	ID(pulse)	±28	Α
Total Power Dissipation (Tc = 25 °C)	P <sub>T1</sub>	75	W
Total Power Dissipation (T <sub>A</sub> = 25 °C)	P <sub>T2</sub>	1.5	W
Channel Temperature	$T_ch$	150	°C
Storage Temperature	T <sub>stg</sub> -	-55 to +150	°C
Single Avalanche Current**	las	7.0	Α
Single Avalanche Energy**	Eas	17	mJ

- \* PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1 %
- \*\* Starting T<sub>ch</sub> = 25 °C, R<sub>G</sub> = 25  $\Omega$ , V<sub>GS</sub> = 20 V  $\rightarrow$  0



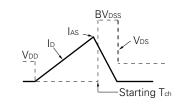


# **ELECTRICAL CHARACTERISTICS (TA = 25 °C)**

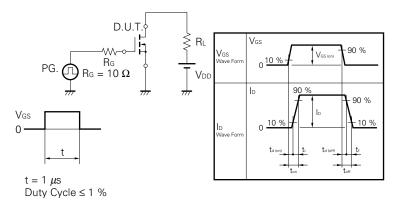
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS	
Drain to Source On-State Resistance	R <sub>DS(on)</sub>		0.7	0.9	mΩ	Vgs = 10 V	2SK2359
			0.8	1.0		V <sub>D</sub> = 4.0 V	2SK2360
Gate to Source Cutoff Voltage	V <sub>GS(off)</sub>	2.5		3.5	V	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA V <sub>DS</sub> = 10 V, I <sub>D</sub> = 4.0 A	
Forward Transfer Admittance	l yfs l	3.0			S		
Drain Leakage Current	IDSS			100	μΑ	V <sub>DS</sub> = V <sub>DSS</sub> , V <sub>G</sub>	s = 0
Gate to Source Leakage Current	Igss			±100	nA	V <sub>GS</sub> = ±30 V, V	'DS = 0
Input Capacitance	Ciss		1050		pF	V <sub>DS</sub> = 10 V	
Output Capacitance	Coss		200		pF	V <sub>G</sub> S = 0	
Reverse Transfer Capacitance	Crss		26		pF	f = 1 MHz	
Turn-On Delay Time	td(on)		14		ns	ID = 4.0 A	
Rise Time	tr		9		ns	V <sub>GS</sub> = 10 V V <sub>DD</sub> = 150 V	
Turn-Off Delay Time	td(off)		56		ns		
Fall Time	tf		14		ns	$R_G = 10 \Omega R_L$	= 37.5 Ω
Total Gate Charge	QG		27		nC	ID = 7.0 A	
Gate to Source Charge	Qgs		5.5		nC	V <sub>DD</sub> = 400 V	
Gate to Drain Charge	Q <sub>GD</sub>		12		nC	Vgs = 10 V	
Body Diode Forward Voltage	V <sub>F(S-D)</sub>		1.0		V	IF = 7.0 A, VGS	= 0
Reverse Recovery Time	trr		300		ns	IF = 7.0 A, VGS	= 0
Reverse Recovery Charge	Qrr		1.5		μC	di/dt = 50 A/μs	3

## Test Circuit 1 Avalanche Capability

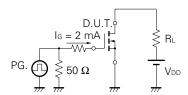
# $V_{GS} = 20 - 0$ $V_{GS} = 20 - 0$



## **Test Circuit 2 Switching Time**

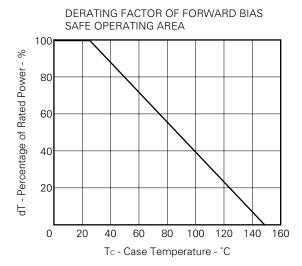


# **Test Circuit 3 Gate Charge**

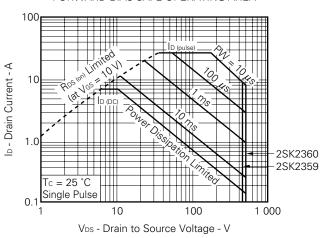


The application circuits and their parameters are for references only and are not intended for use in actual design-in's.

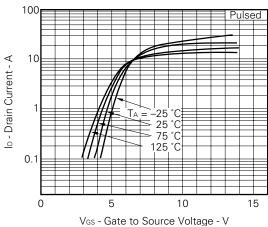
### TYPICAL CHARACTERISTICS (TA = 25 °C)

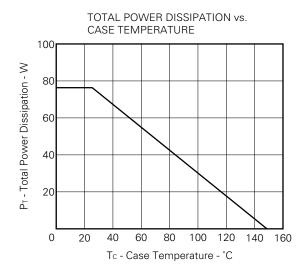


### FORWARD BIAS SAFE OPERATING AREA

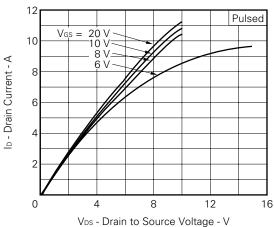


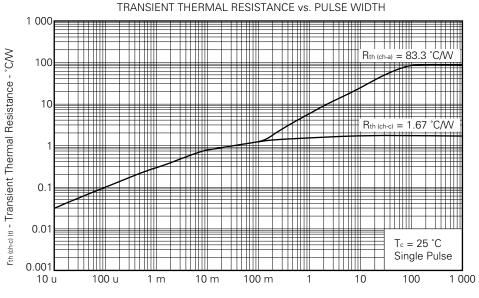
DRAIN CURRENT vs. GATE TO SOURCE VOLTAGE





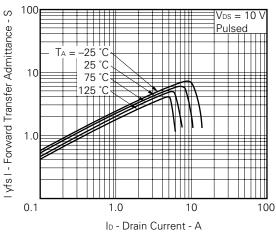
DRAIN CURRENT vs.
DRAIN TO SOURCE VOLTAGE



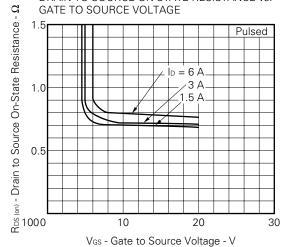


PW - Pulse Width - s

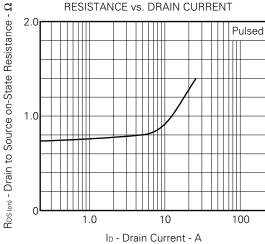




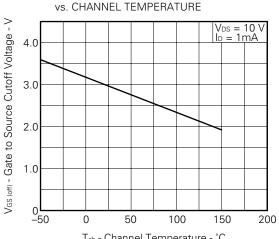
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



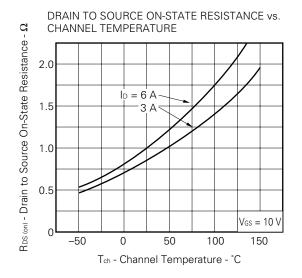
DRAIN TO SOURCE ON-STATE

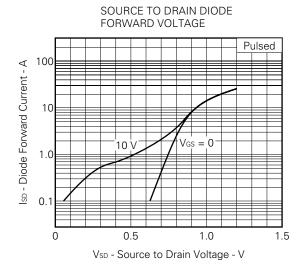


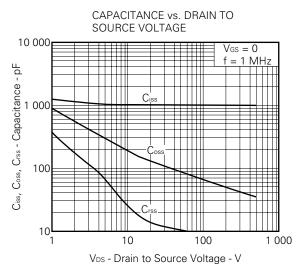
GATE TO SOURCE CUTOFF VOLTAGE

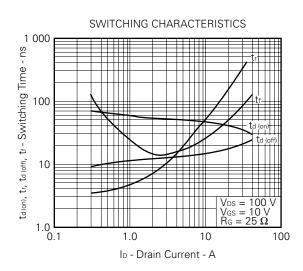


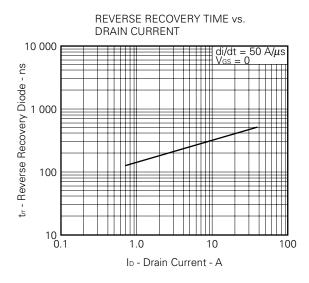
 $\mathsf{T}_\mathsf{ch}$  - Channel Temperature -  ${}^\circ\mathsf{C}$ 

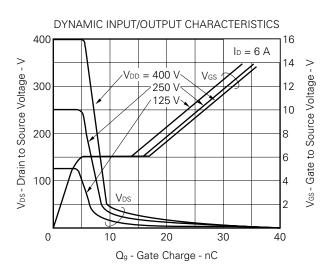




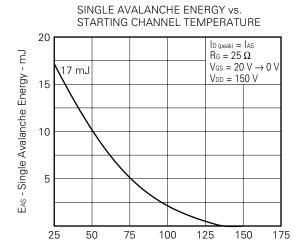




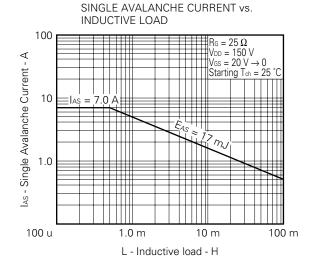








Starting Tch-Starting Channel Temperature - °C



## **REFERENCE**

Document Name	Document No.
NEC semiconductor device reliability/quality control system.	TEI-1202
Quality grade on NEC semiconductor devices.	IEI-1209
Semiconductor device mounting technology manual.	IEI-1207
Semiconductor device package manual.	IEI-1213
Guide to quality assurance for semiconductor devices.	MEI-1202
Semiconductor selection guide.	MF-1134
Power MOS FET features and application switching power supply.	TEA-1034
Application circuits using Power MOS FET.	TEA-1035
Safe operating area of Power MOS FET.	TEA-1037

The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device is actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

7

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