

No.4555

N-Channel MOS Silicon FET Very High-Speed Switching Applications

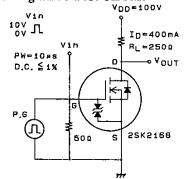
Features

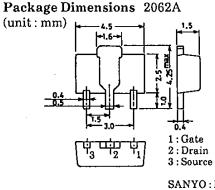
- · Low ON resistance.
- · Very high-speed switching.
- · Low-voltage drive.

Absolute Maximum Ratings at Ta = 25°C unit						
Drain-to-Source Voltage	V_{DSS}		,	250	v	
Gate-to-Source Voltage	V_{GSS}		Ⅎ	±20	V	
Drain Current(DC)	ID			800	mA	
Drain Current(Pulse)	I_{DP}	PW≤10μs, duty cycle≤1%		3.2	Α	
Allowable Power Dissipation	P_{D}	Tc = 25°C		3.5	W	
·	· -	Mounted on ceramic board $(250 \text{mm}^2 \times 0.8 \text{mm})$		1.5	W	
Channel Temperature	Tch	(150		°C	
Storage Temperature	Tstg		-55 to +150		°C	
Electrical Characteristics at Ta = 25°C				tun	max	unit
D-S Breakdown Voltage		$I_D = 1 \text{mA}, V_{GS} = 0$	min 250	typ	max	V
Zero Gate Voltage	IDSS	$V_{DS} = 250 \text{V}, V_{GS} = 0$	200		100	$\mu { m A}$
Drain Current	*D22	*DS-200*,*GS-0			100	μπ
Gate-to-Source Leakage Current	I_{GSS}	$V_{CS} = \pm 18V, V_{DS} = 0$			±10	μ A
Cutoff Voltage		$V_{\rm DS} = 10 \text{V}, I_{\rm D} = 1 \text{mA}$	1.5		2.5	v
Forward Transfer Admittance	$ \mathbf{y}_{\mathrm{fs}} $	$V_{DS} = 10V, I_D = 400 \text{mA}$	0.6	0.9		S
Static Drain-to-Source	R _{DS(on)}	$I_D = 400 \text{ mA}, V_{GS} = 10 \text{ V}$		3.5	5	Ω
ON State Resistance	20(011)	2 . 43				
Input Capacitance	Ciss	$V_{DS} = 20V, f = 1MHz$		80		pF
Output Capacitance	Coss	$V_{DS} = 20V, f = 1MHz$		20		pF
Reverse Transfer Capacitance	Crss	$V_{DS} = 20V, f = 1MHz$		8		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		10		ns
Rise Time	t_r	"		10		ns
Turn-OFF Delay Time	$t_{d(off)}$	"		50		ns
Fall Time	t_f	"		30		ns
Diode Forward Voltage	V_{SD}	$I_{S} = 800 \text{mA}, V_{GS} = 0$		1.0		V

Marking: KL

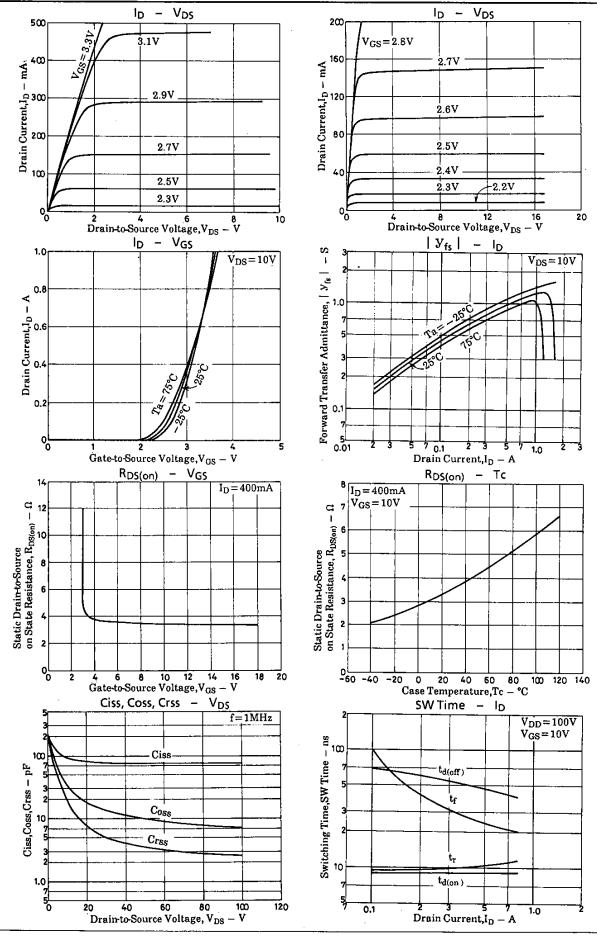
Switching Time Test Circuit

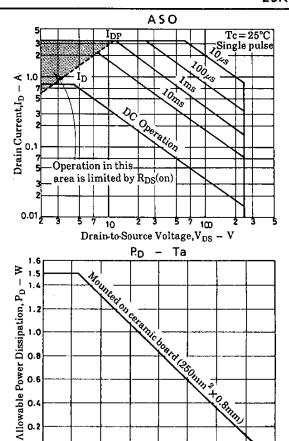




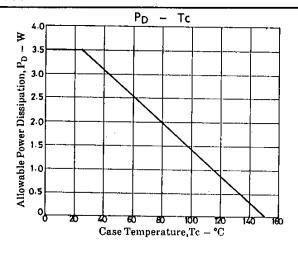
SANYO: PCP (Bottom View)

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40 60 60 00 120 Ambient Temperature, Ta — °C



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