
2SK1163, 2SK1164

Silicon N-Channel MOS FET

HITACHI

Application

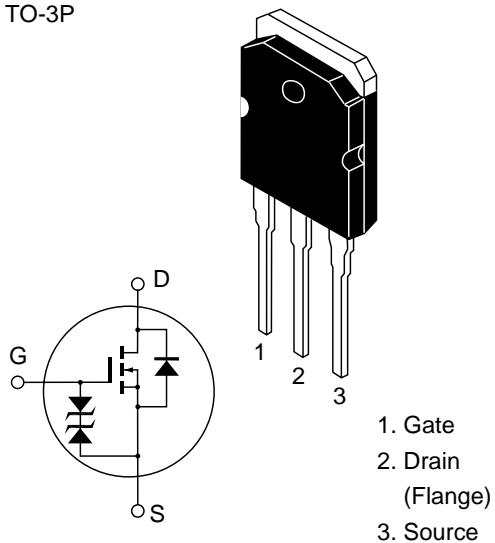
High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

Outline

TO-3P



2SK1163, 2SK1164

Absolute Maximum Ratings (Ta = 25°C)

| Item | | Symbol | Ratings | Unit |
|---|---------|---------------------|-------------|------|
| Drain to source voltage | 2SK1163 | V_{DSS} | 450 | V |
| | 2SK1164 | | 500 | |
| Gate to source voltage | | V_{GSS} | ±30 | V |
| Drain current | | I_D | 11 | A |
| Drain peak current | | $I_{D(pulse)}^{*1}$ | 40 | A |
| Body to drain diode reverse drain current | | I_{DR} | 11 | A |
| Channel dissipation | | P_{ch}^{*2} | 100 | W |
| Channel temperature | | T_{ch} | 150 | °C |
| Storage temperature | | T_{stg} | −55 to +150 | °C |

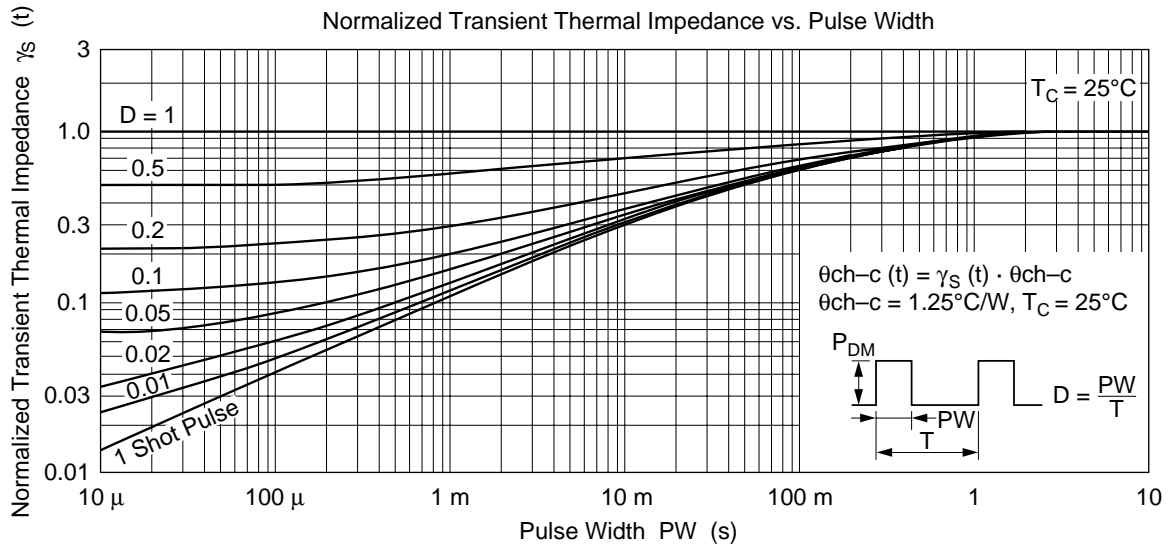
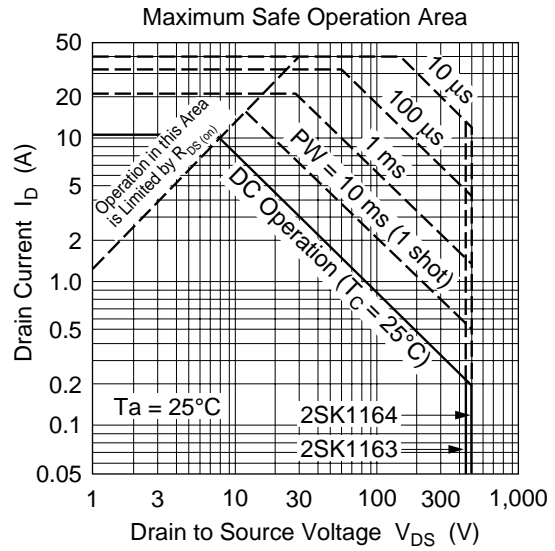
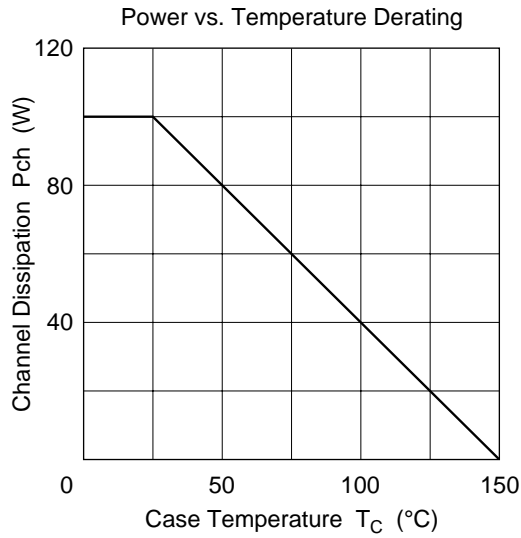
Notes: 1. $PW \leq 10 \mu s$, duty cycle $\leq 1\%$
2. Value at $T_c = 25^\circ C$

Electrical Characteristics (Ta = 25°C)

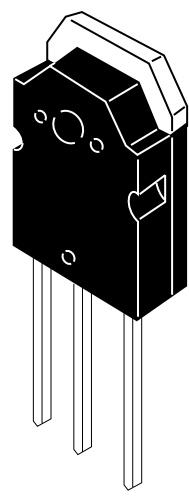
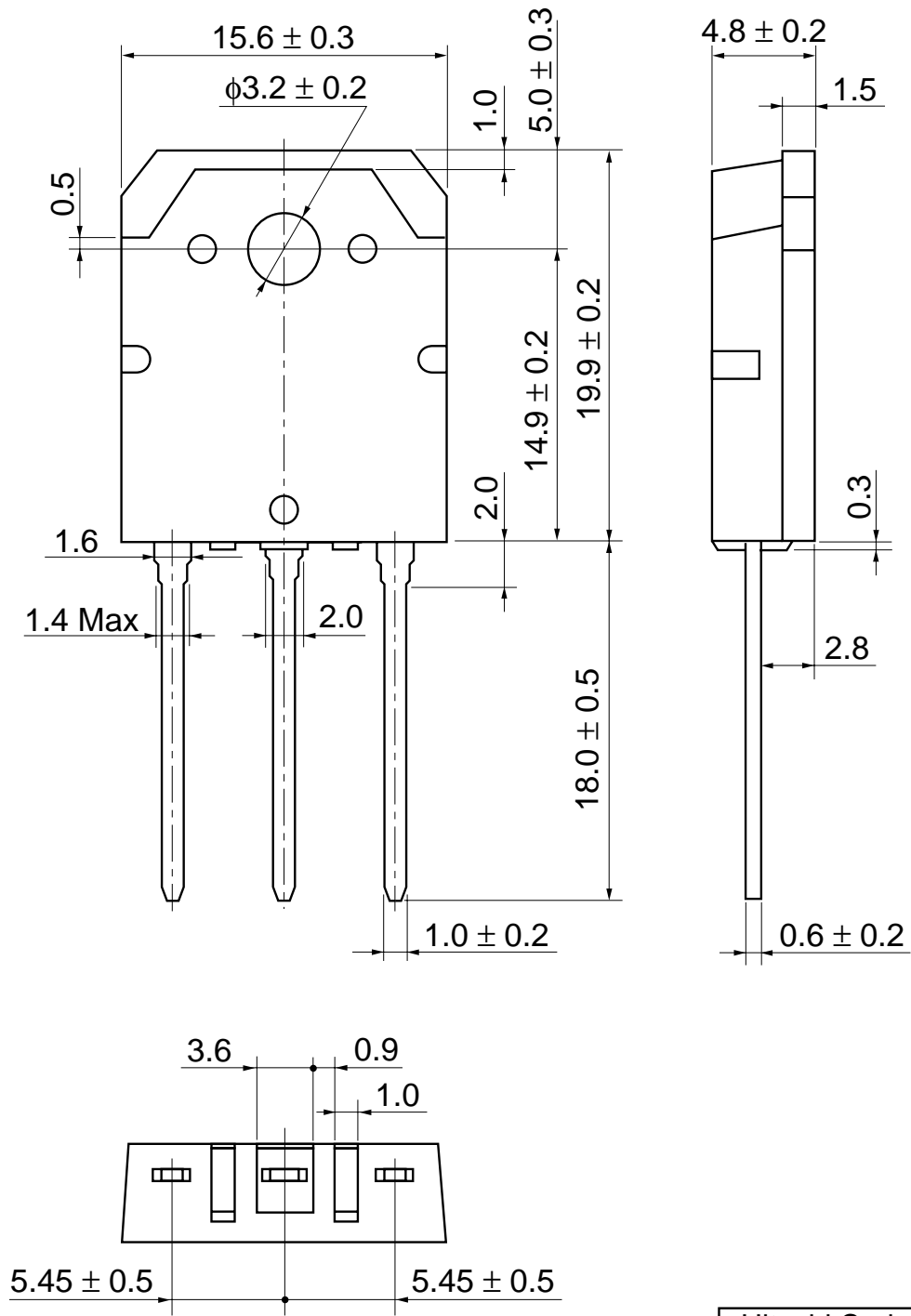
| Item | | Symbol | Min | Typ | Max | Unit | Test conditions |
|--|--------------------|---------------|------------|--------------|------------|------|--|
| Drain to source breakdown voltage | 2SK1163 2SK1164 | $V_{(BR)DSS}$ | 450 500 | — | — | V | $I_D = 10 \text{ mA}$, $V_{GS} = 0$ |
| Gate to source breakdown voltage | | $V_{(BR)GSS}$ | ±30 | — | — | V | $I_G = \pm 100 \mu\text{A}$, $V_{DS} = 0$ |
| Gate to source leak current | | I_{GSS} | — | — | ±10 | μA | $V_{GS} = \pm 25 \text{ V}$, $V_{DS} = 0$ |
| Zero gate voltage drain current | 2SK1163 2SK1164 | I_{DSS} | — | — | 250 | μA | $V_{DS} = 360 \text{ V}$, $V_{GS} = 0$ $V_{DS} = 400 \text{ V}$, $V_{GS} = 0$ |
| Gate to source cutoff voltage | | $V_{GS(off)}$ | 2.0 | — | 3.0 | V | $I_D = 1 \text{ mA}$, $V_{DS} = 10 \text{ V}$ |
| Static Drain to source on state resistance | 2SK1163 2SK1164 | $R_{DS(on)}$ | — — | 0.55 0.60 | 0.7 0.8 | Ω | $I_D = 5 \text{ A}$, $V_{GS} = 10 \text{ V}^{*1}$ |
| Forward transfer admittance | | $ y_{fs} $ | 5.0 | 8.0 | — | S | $I_D = 5 \text{ A}$, $V_{DS} = 10 \text{ V}^{*1}$ |
| Input capacitance | | C_{iss} | — | 1150 | — | pF | $V_{DS} = 10 \text{ V}$, $V_{GS} = 0$, $f = 1 \text{ MHz}$ |
| Output capacitance | | C_{oss} | — | 340 | — | pF | |
| Reverse transfer capacitance | | C_{rss} | — | 55 | — | pF | |
| Turn-on delay time | | $t_{d(on)}$ | — | 17 | — | ns | $I_D = 5 \text{ A}$, $V_{GS} = 10 \text{ V}$, $R_L = 6 \Omega$ |
| Rise time | | t_r | — | 60 | — | ns | |
| Turn-off delay time | | $t_{d(off)}$ | — | 95 | — | ns | |
| Fall time | | t_f | — | 50 | — | ns | |
| Body to drain diode forward voltage | | V_{DF} | — | 1.0 | — | V | $I_F = 11 \text{ A}$, $V_{GS} = 0$ |
| Body to drain diode reverse recovery time | | t_{rr} | — | 400 | — | ns | $I_F = 11 \text{ A}$, $V_{GS} = 0$, $di_F/dt = 100 \text{ A}/\mu\text{s}$ |

Note: 1. Pulse test

See characteristic curves of 2SK1159, 2SK1160.



Unit: mm



| | |
|--------------------------|----------|
| Hitachi Code | TO-3P |
| JEDEC | — |
| EIAJ | Conforms |
| Weight (reference value) | 5.0 g |

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