# 2SK0301 (2SK301)

## Silicon N-Channel Junction FET

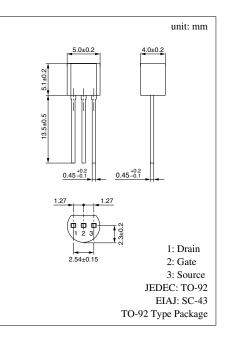
For low-frequency amplification For switching

#### Features

• Low noies, high gain

• High gate to drain voltage  $V_{GDO}$ 

Absolute Maximum Ratings (Ta = $25^{\circ}$ C)						
Parameter	Symbol	Ratings	Unit			
Drain to Source voltage	V <sub>DSX</sub>	55	V			
Gate to Drain voltage	V <sub>GDO</sub>	-55	V			
Gate to Source voltage	V <sub>GSO</sub>	-55	V			
Drain current	I <sub>D</sub>	±30	mA			
Gate current	I <sub>G</sub>	10	mA			
Allowable power dissipation	P <sub>D</sub>	250	mW			
Junction temperature	Tj	125	°C			
Storage temperature	T <sub>stg</sub>	-55 to +125	°C			



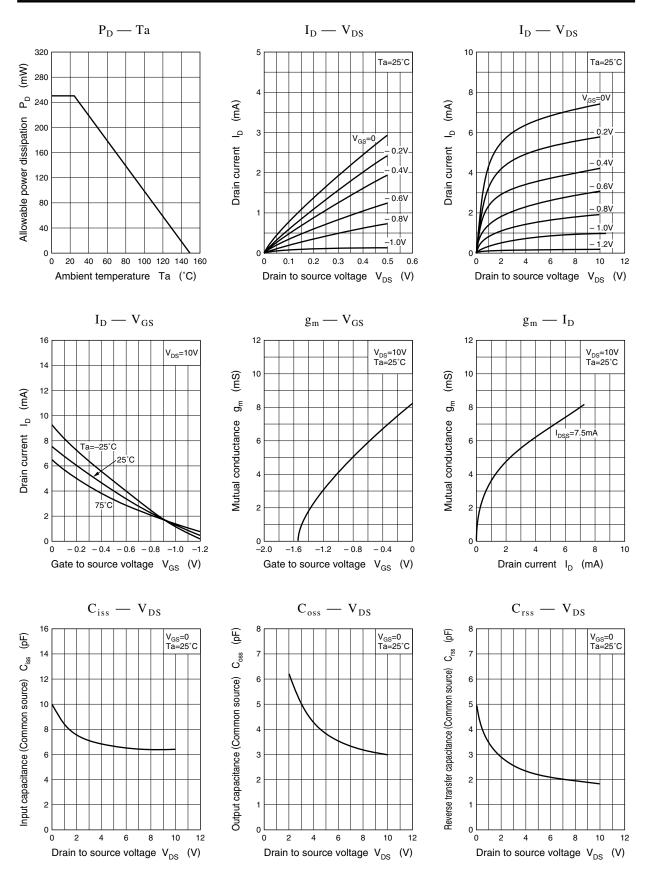
## Electrical Characteristics ( $Ta = 25^{\circ}C$ )

Parameter	Symbol	Conditions	min	typ	max	Unit
Drain to Source cut-off current	I <sub>DSS</sub> *	$V_{DS} = 10V, V_{GS} = 0$	1		20	mA
Gate to Source leakage current	I <sub>GSS</sub>	$V_{GS} = -30V, V_{DS} = 0$			-10	nA
Gate to Drain voltage	V <sub>GDC</sub>	$I_{G} = -100 \mu A, V_{DS} = 0$	-55	-80		V
Gate to Source cut-off voltage	V <sub>GSC</sub>	$V_{\rm DS} = 10V, I_{\rm D} = 10\mu A$			-5	V
Mutual conductance	g <sub>m</sub>	$V_{DS} = 10V, V_{GS} = 0, f = 1kHz$	2.5	7.5		mS
Input capacitance (Common Source)	C <sub>iss</sub>			6.5		pF
Reverse transfer capacitance (Common Source)	C <sub>rss</sub>	$V_{DS} = 10V, V_{GS} = 0, f = 1MHz$		1.9		pF
Noise figure	NF	$V_{DS} = 10V, V_{GS} = 0, R_g = 100k\Omega$ f = 100Hz		0.5		dB

#### \* I<sub>DSS</sub> rank classification

Runk	Р	Q	R	S
I <sub>DSS</sub> (mA)	1 to 3	2 to 6.5	5 to 12	10 to 20

Note) The part number in the parenthesis shows conventional part number.



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