



Vishay Semiconductors

Ultralow V_F Ultrafast Rectifier, 15 A FRED Pt[®]



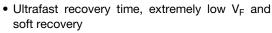


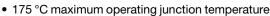
D-PAK	(TO-252AA)
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01 Anode	3 O Anode

PRODUCT SUMMARY				
Package	D-PAK (TO-252AA)			
I _{F(AV)}	15 A			
V_{R}	600 V			
V _F at I _F	1.05 V			
t _{rr} (typ.)	60 ns			
T _J max.	175 °C			
Diode variation	Single die			

FEATURES





• For PFC DCM operation

· Low leakage current

Compliant to RoHS Directive 2002/95/EC

• Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

Ph

RoHS

DESCRIPTION/APPLICATIONS

State of the art, ultralow V_F , soft-switching hyperfast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	V_{RRM}		600	V	
Average rectified forward current	I _{F(AV)}	T _C = 148 °C	15		
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	180	Α	
Peak repetitive forward current	I _{FM}	$T_C = 148 ^{\circ}\text{C}, f = 20 \text{kHz}, d = 50 \%$	30		
Operating junction and storage temperatures	T_J , T_{Stg}		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-		
Forward voltage V _F	V	I _F = 15 A	-	0.99	1.05	V	
	I _F = 15 A, T _J = 150 °C	-	0.85	0.92			
Reverse leakage current	I_	$V_R = V_R$ rated	-	-	10	^	
neverse leakage current	I _R	T _J = 150 °C, V _R = V _R rated	-	-	120	μA	
Junction capacitance	C _T	V _R = 600 V		11	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body		8	-	nH	

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1 A, dI_F/dt = 10$	$I_F = 1 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		60	120	
Reverse recovery time	+	$I_F = 15 \text{ A}, dI_F/dt = 100$	$I_F = 15 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		190	-	ns
neverse recovery time	t _{rr}	T _J = 25 °C		-	220	-	115
		T _J = 125 °C		-	290	-	
Dook roomer ourrent	Bud was a said	T _J = 25 °C	I _F = 15 A dI _F /dt = 200 A/μs	-	21	-	Α
Peak recovery current I _{RRM}	T _J = 125 °C	$V_{\rm R} = 390 \text{ V}$	-	25	-	A	
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	2.6	-	µС
	T _J = 125 °C		-	4	-	μΟ	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}		-	1.4	1.8	°C/W
Thermal resistance, junction to ambient per leg	R _{thJA}		-	-	70	C/VV
Approximate weight				0.3		g
Approximate weight			0.01		OZ.	
Marking device		Case style D-PAK (TO-252AA)		15AW	L06FN	•





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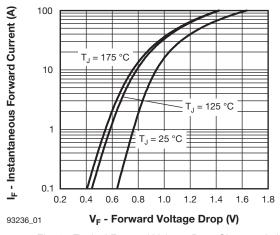


Fig. 1 - Typical Forward Voltage Drop Characteristics

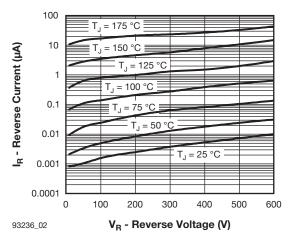


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

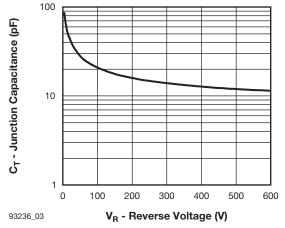


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

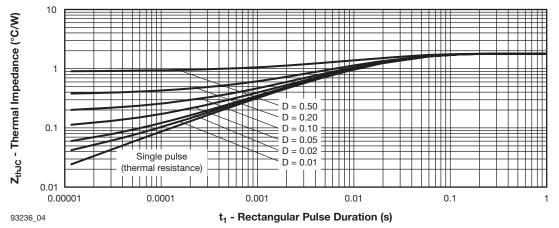


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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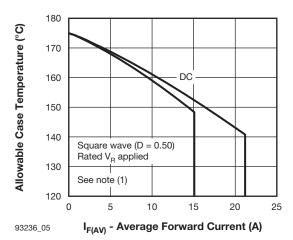


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

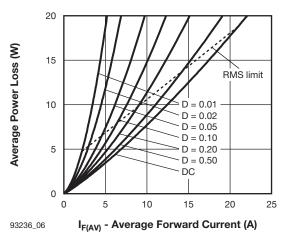


Fig. 6 - Forward Power Loss Characteristics

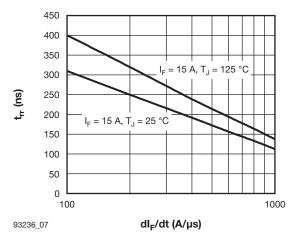


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

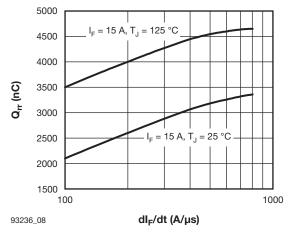


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \mbox{(1)} & \mbox{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \mbox{Forward power loss} = I_{F(AV)} \times V_{FM} \mbox{ at } (I_{F(AV)}/D) \mbox{ (see fig. 6)}; \\ \end{array}$ Pd_{REV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = Rated V_R



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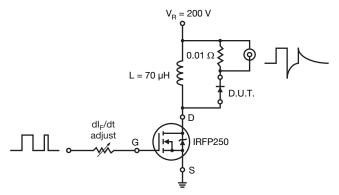
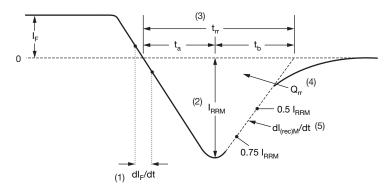


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dI_E/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (4) Q_{rr} area under curve defined by t_{rr} and I_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

VS-15AWL06FN-E3

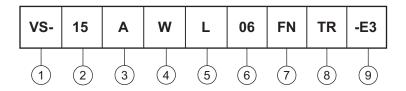
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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductor product

2 - Current rating (15 = 15 A)

Circuit configuration:

A = Single diode (2 anodes)

Package identifier:

W = D-PAK

5 - L = Hyperfast rectifier

6 - Voltage rating (06 = 600 V)

7 - FN = TO-252AA

8 - TR = Tape and reel

9 - Environmental digit:

-E3 = RoHS compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-15AWL06FNTR-E3	2000	2000	13" diameter reel		

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95016				
Part marking information	www.vishay.com/doc?95176				
Packaging information (TR only)	www.vishay.com/doc?95033				
SPICE model	www.vishay.com/doc?95372				

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