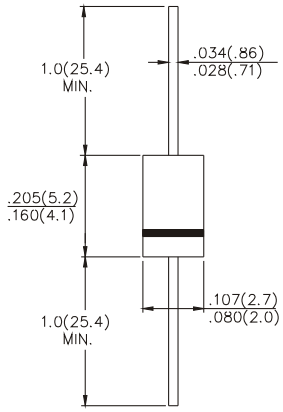
RGPP10 SERIES

GLASS PASSIVATED FAST SWITCHING RECTIFIER

CHENG-YI ELECTRONIC

VOLTAGE RANGE 50 TO 1000 Volts
CURRENT 1.0 Amperes

DO-41



Dimensions in inches and (millimeters)

FEATURE

- High voltage
- High current capability
- Low leakage current
- High surge capability
- Low cost

MECHANICAL DATA

Case: Mold plastic use UL 94V-0 recognized flame retardant epoxy
Terminals: Axial leads, solderable per MIL-STD-202, method 208
Polarity: Color band denotes cathode
Mounting Position: Any

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.
Signle phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

	RGPP10A	RGPP10B	RGPP10D	RGPP10G	RGPP10J	RGPP10K	RGPP10M	UNITS
Maximum Recurrent Peak Reverse Voltage	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current, .375", (9.5mm) Lead Length at T_A =55°C	1.0							A
Peak Forward Surge Current 8.3 ms single half sine-wave	50							A
Maximum Forward Voltage at 1.0A Peak	1.2					1.3		V
Maximum Reverse Current, Rated DC Full Cycle Average, .375", (9.5mm) Lead Length at T_A =55°C	30							μA
Maximum DC Reverse Current, at Rated DC Blocking Voltage	5.0							μA
Maximum Reverse Recovery Time (Note 1)	150	150	150	150	250	500	500	nS
Typical Junction Capacitance (Note 2)	15							pF
Operating and Storage Temperature Range	-65 to +175							°C

Notes : 1. Reverse Recovery Test Conditions : IF=.5A, IR=1A, Irr=.25A
2. Measured at 1.0MHz and applied reverse voltage of 4.0 Volts

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RATING AND CHARACTERISTICS CURVES
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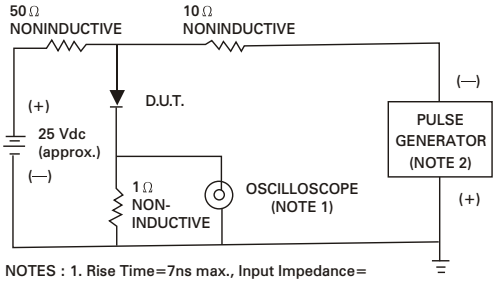
Fig. 1 - REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM



NOTES : 1. Rise Time=7ns max., Input Impedance= 1 megohm, 22pF.
2. Rise Time=10ns max., Source Impedance= 50 ohms.

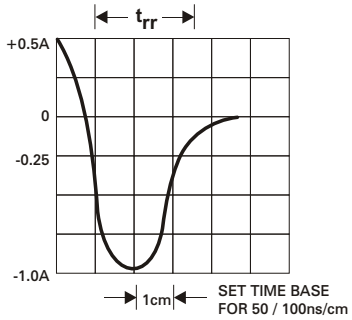


Fig. 2 - FORWARD CURRENT DERATING CURVE

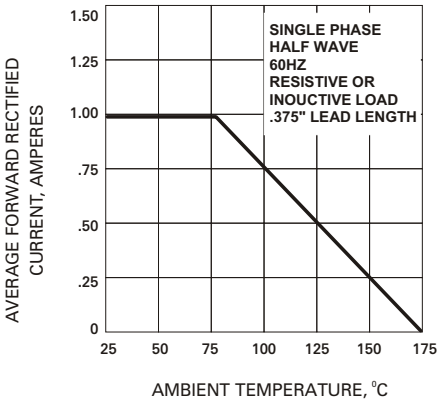


Fig. 3 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

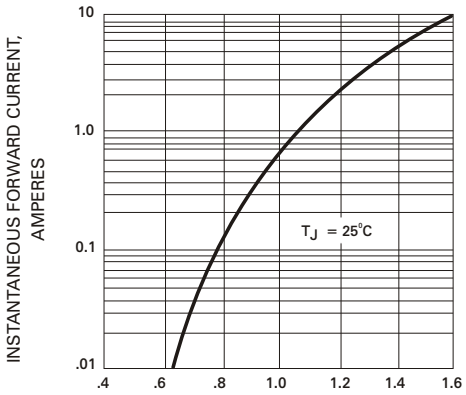


Fig. 4 - TYPICAL JUNCTION CAPACITANCE

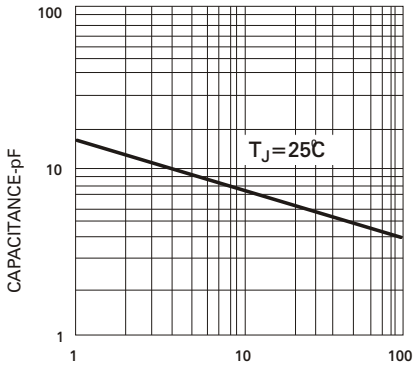


Fig. 5 - PEAK FORWARD SURGE CURRENT