

# Silicon Controlled Rectifier

## Reverse Blocking Triode Thyristor

... designed for industrial and consumer applications such as power supplies, battery chargers, temperature, motor, light and welder controls.

- Supplied in Either Pressfit or Stud Package
- High Surge Current Rating —  $I_{TSM} = 240$  Amps
- Low On-State Voltage — 1.2 V (Typ) @  $I_{TM} = 20$  Amps
- Practical Level Triggering and Holding Characteristics — 40 mA (Max) and 50 mA (Max) @  $T_C = 25^\circ\text{C}$

**MCR3818 Series**  
**MCR3918 Series**

**SCRs**  
**25 AMPERES RMS**  
**50 thru 800 VOLTS**



### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Forward and Reverse Voltage, Note 1 MCR3818, MCR3918-2	$V_{DRM}$ or $V_{RRM}$	50 100 200 400 600 800	Volts
Non-Repetitive Reverse Blocking Voltage MCR3818, MCR3918-2	$V_{RSM}$	75 150 300 500 700 900	Volts
On-State Current RMS	$I_{T(RMS)}$	20	Amps
Average On-State Current ( $T_C = 67^\circ\text{C}$ )	$I_{T(AV)}$	13	Amps
Circuit Fusing ( $T_J = -40$ to $+100^\circ\text{C}$ , $t \leq 8.3$ ms)	$I^2t$	235	$\text{A}^2\text{s}$
Peak Non-Repetitive Surge Current (One Cycle, 60 Hz, $T_J = -40$ to $+100^\circ\text{C}$ )	$I_{TSM}$	240	Amps
Peak Gate Power (Maximum Pulse Width = 10 $\mu\text{s}$ )	$P_{GM}$	5	Watts
Average Gate Power	$P_{G(AV)}$	0.5	Watt
Peak Forward Gate Current (Maximum Pulse Width = 10 $\mu\text{s}$ )	$I_{GM}$	2	Amps

Note 1.  $V_{DRM}$  for all types can be applied on a continuous dc basis without incurring damage. Ratings apply for zero or negative gate voltage. These devices should not be tested with a constant current source for forward or reverse blocking capability such that the voltage applied exceeds the rated blocking voltage.



**CASE 174-04**  
**(TO-203)**  
**STYLE 1**  
**MCR3818 Series**



**CASE 175-03**  
**STYLE 1**  
**MCR3918 Series**

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**MAXIMUM RATINGS — continued**

Rating	Symbol	Value	Unit
Peak Gate Voltage	V <sub>GM</sub>	10	Volts
Operating Junction Temperature Range	T <sub>J</sub>	-40 to +125	°C
Storage Temperature Range	T <sub>stg</sub>	-40 to +150	°C
Stud Torque		30	in. lb.

**THERMAL CHARACTERISTICS**

Characteristic	Symbol	Typ	Max	Unit
Thermal Resistance, Junction to Case Pressfit Package	R <sub>θJC</sub>	1	1.5	°C/W
Stud Package		1.1	1.6	

**ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted.)**

Characteristic	Symbol	Min	Max	Unit
Peak Forward or Reverse Blocking Current (Rated V <sub>DRM</sub> or V <sub>RRM</sub> , gate open) T <sub>J</sub> = 25°C T <sub>J</sub> = 100°C	I <sub>DRM</sub> , I <sub>RRM</sub>	— —	10 5	μA mA
Gate Trigger Current (Continuous dc) (V <sub>D</sub> = 7 Vdc, R <sub>L</sub> = 100 Ω) (V <sub>D</sub> = 7 Vdc, R <sub>L</sub> = 100 Ω, T <sub>C</sub> = -40°C)	I <sub>GT</sub>	— —	40 75	mA
Gate Trigger Voltage (Continuous dc) (V <sub>D</sub> = 7 Vdc, gate open) (V <sub>D</sub> = 7 Vdc, R <sub>L</sub> = 100 Ω, T <sub>C</sub> = -40°C) (V <sub>D</sub> = Rated V <sub>DRM</sub> , R <sub>L</sub> = 100 Ω, T <sub>J</sub> = 100°C)	V <sub>GT</sub>	— — 0.2	1.5 2.5 —	Volts
Peak On-State Voltage (Pulse Width = 1 ms max, duty cycle ≤ 1%) (I <sub>TM</sub> = 20 A) (I <sub>TM</sub> = 41 A)	V <sub>TM</sub>	— —	1.5 1.7	Volts
Holding Current (V <sub>D</sub> = 7 Vdc, gate open) (V <sub>D</sub> = 7 Vdc, gate open, T <sub>C</sub> = -40°C)	I <sub>H</sub>	— —	50 90	mA
Gate Controlled Turn-On Time (t <sub>d</sub> + t <sub>r</sub> ) (I <sub>TM</sub> = 20 A, I <sub>GT</sub> = 40 mAdc, V <sub>D</sub> = Rated V <sub>DRM</sub> )	t <sub>gt</sub>	Typical		μs
		1		
Circuit Commutated Turn-Off Time (I <sub>TM</sub> = 10 A, I <sub>R</sub> = 10 A) (I <sub>TM</sub> = 10 A, I <sub>R</sub> = 10 A, T <sub>J</sub> = 100°C) (V <sub>D</sub> = V <sub>DRM</sub> = rated voltage) (dv/dt = 30 V/μs)	t <sub>q</sub>	20 30		μs
Critical Rate of Rise of Off-State Voltage (V <sub>D</sub> = Rated V <sub>DRM</sub> , Exponential Wave Form, Gate open, T <sub>J</sub> = 100°C)	dv/dt	50		V/μs

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EFFECT OF TEMPERATURE UPON TYPICAL TRIGGER CHARACTERISTICS

FIGURE 1 – GATE TRIGGER CURRENT

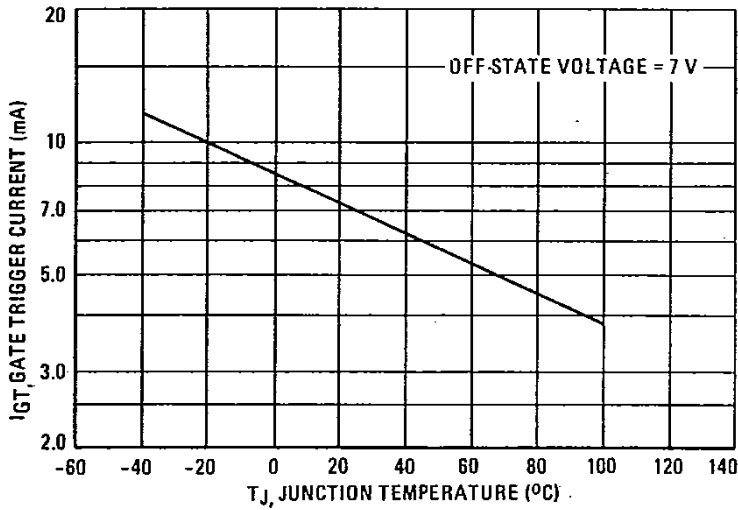
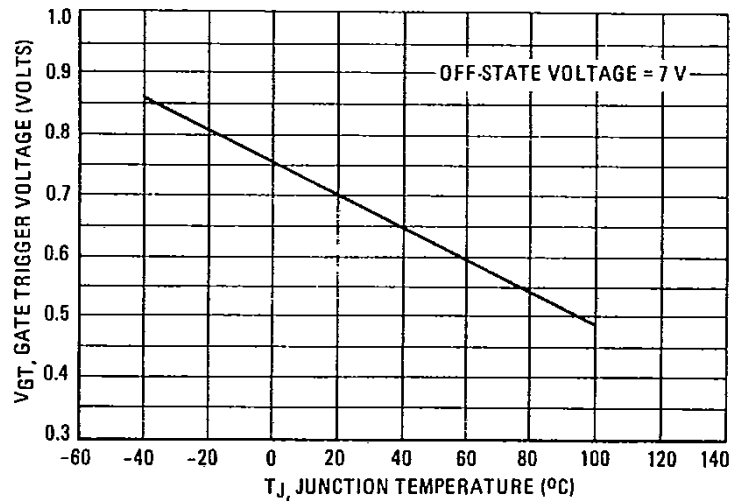


FIGURE 2 – GATE TRIGGER VOLTAGE



MAXIMUM ALLOWABLE NON-REPETITIVE SURGE CURRENT

FIGURE 3 – 60 Hz SURGES

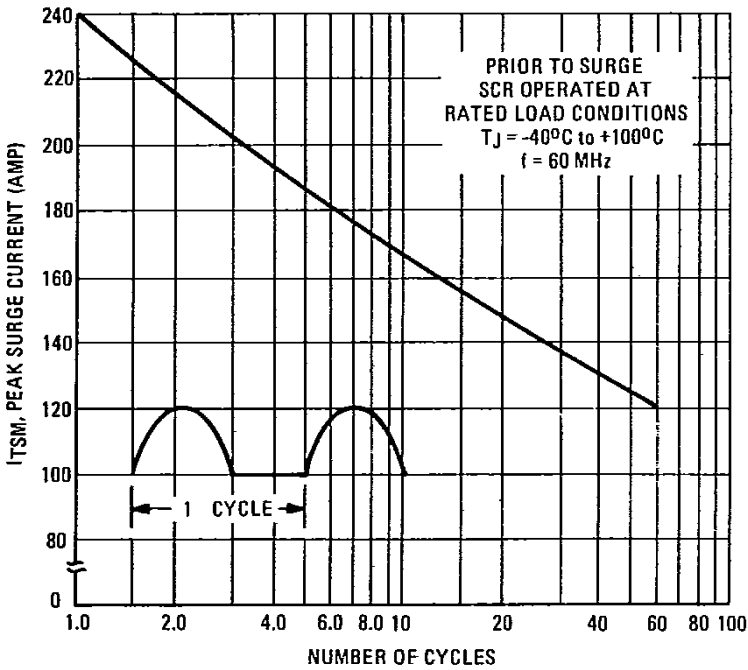
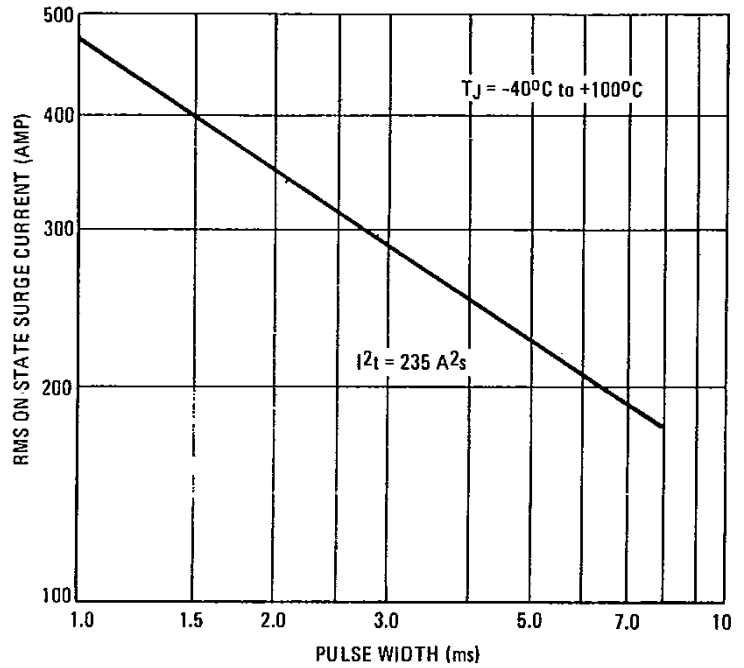


FIGURE 4 – SUB-CYCLE SURGES



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FIGURE 5 – GATE TRIGGER CHARACTERISTICS

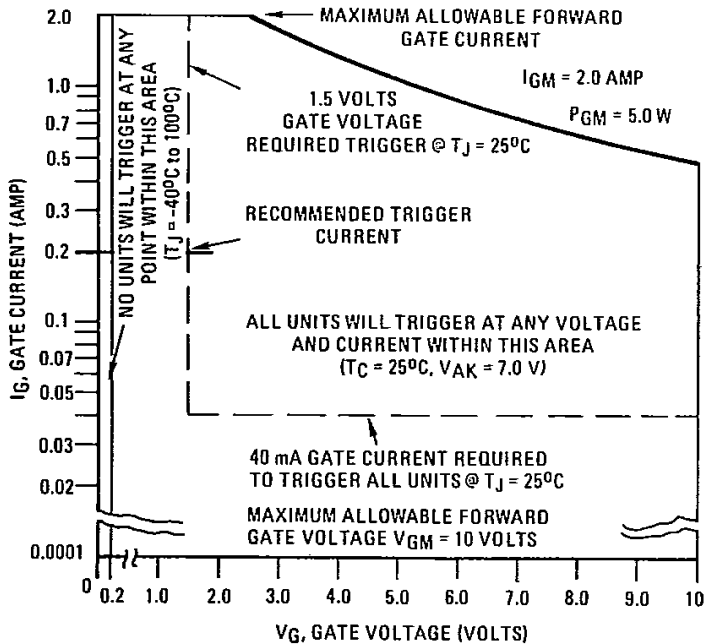
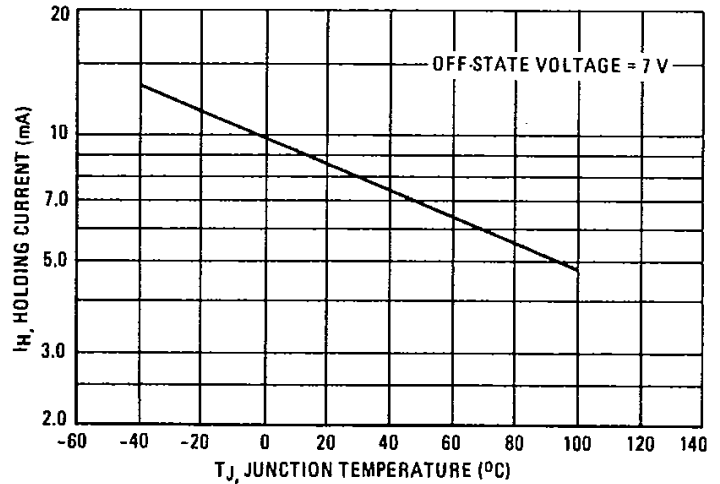


FIGURE 6 – EFFECT OF TEMPERATURE ON TYPICAL HOLDING CURRENT



DERATING AND DISSIPATION FOR RESISTIVE AND INDUCTIVE LOADS ( $f = 60$  to  $400$  Hz, SINE WAVE)

FIGURE 7 – AVERAGE CURRENT DERATING

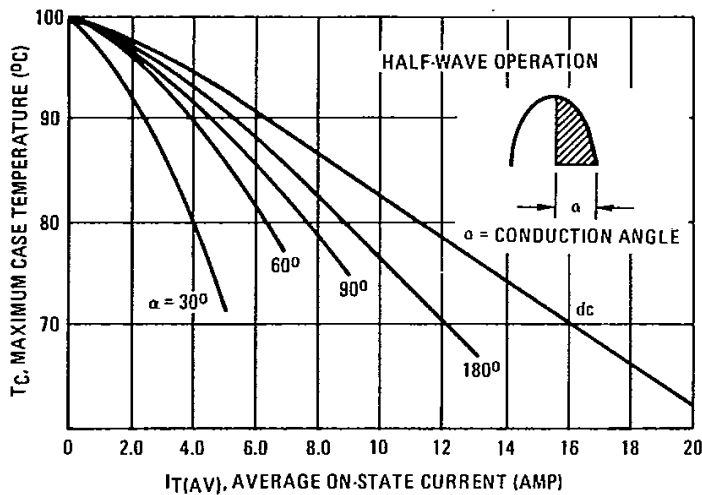
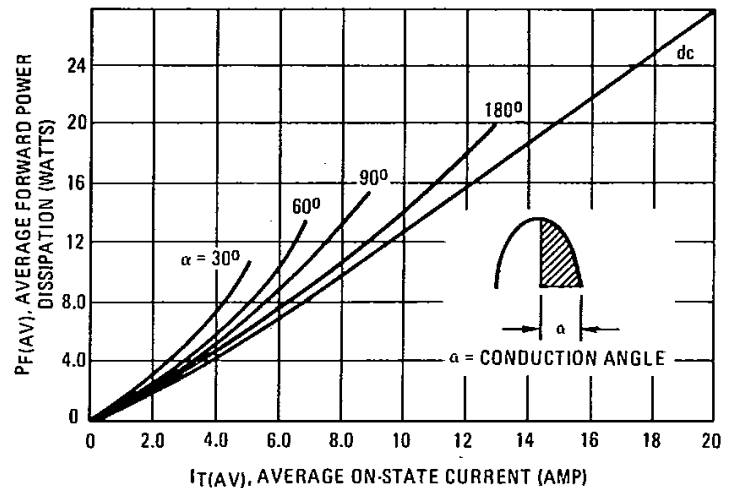
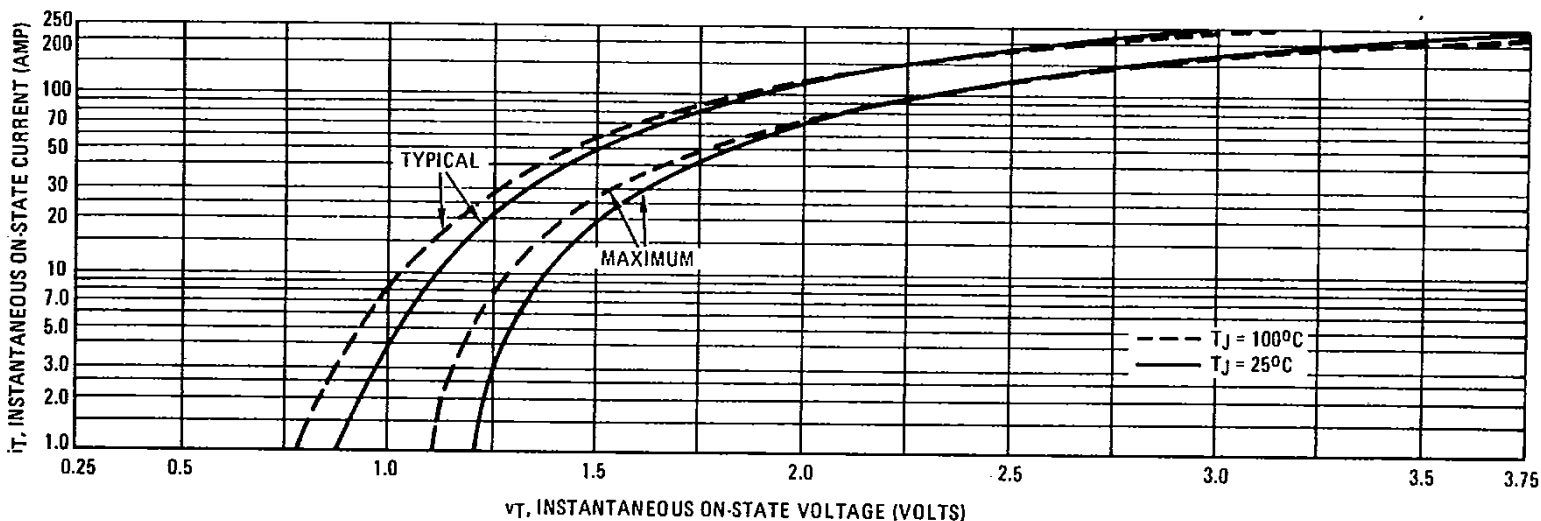


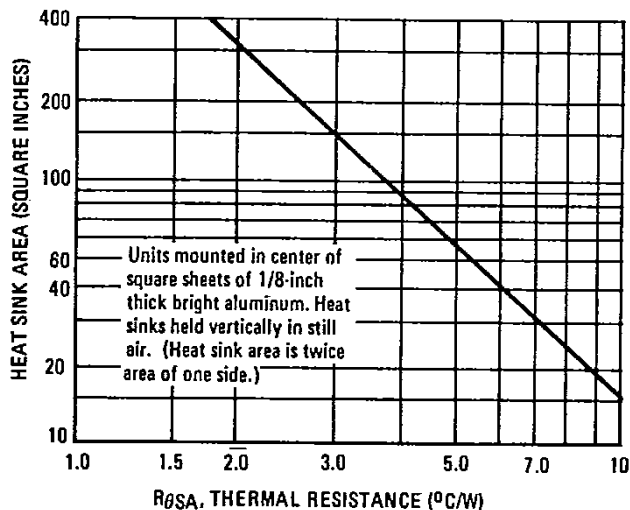
FIGURE 8 – ON-STATE POWER DISSIPATION



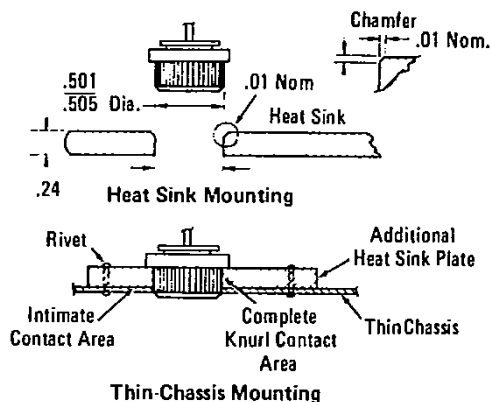
**FIGURE 9 – ON-STATE CHARACTERISTICS**



**FIGURE 10 – TYPICAL THERMAL RESISTANCE OF PLATES**



**FIGURE 11 – MOUNTING DETAILS FOR PRESSFIT THYRISTORS**



The hole edge must be chamfered as shown to prevent shearing off the knurled edge of the rectifier during press-in. The pressing force should be applied evenly on the shoulder ring to avoid tilting or canting of the rectifier case in the hole during the pressing operation. Also, the use of a thermal joint compound will be of considerable aid. The pressing force will vary from 250 to 1000 pounds, depending upon the heat sink material. Recommended hardnesses are: copper – less than 50 on the Rockwell F scale; aluminum – less than 65 on the Brinell scale. A heat sink as thin as 1/8" may be used, but the interface thermal resistance will increase in proportion to the reduction of contact area. A thin chassis requires the addition of a back-up plate.