MB3A

BISS to PC-LPT ADAPTER



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ORDERING INFORMATION

Type Order Designation Description / Options

BiSS to PC-LPT Adapter MB3A Protocol converter for connecting BiSS/SSI sensors to the LPT port on a PC

Scope of delivery: ready-to-use adapter; LPT device driver (Win 98/ME, 2000/XP) and BiSS master PC software for downloading

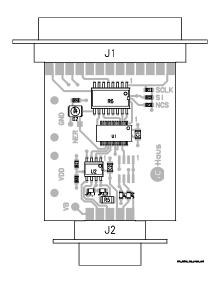
DESCRIPTION

The BiSS to PC-LPT Adapter enables BiSS or SSI sensors to be connected to a PC easily and at low cost.

The parallel printer port is used as hardware link, whereas by software an SPI compatible data communication is emulated, granting a speedy measurement data transmission and the configuration of the adapter. Regarding the sensor side BiSS/SSI communication is carried out via differential RS422 lines, optionally via single ended signals when removing a jumper.

Technical Data

- Synchronously triggered measurement data collection from up to 3 BiSS sensors or one SSI encoder
- BiSS data rates up to 10 Mbit/sec., SSI up to 4 Mbit/sec.
- ◆ Protocol compatible with BiSS Master iC-MB3
- ♦ Connection to the PC parallel printer port
- Operates from a single 5 V voltage supplied via the sensor connection or solder terminals



Size approx. 50 mm x 70 mm

BiSS Plug Pin Configuration J2 (9-pole SUB-D)

Pin	Name	Function
1	VB	not wired
2	MA +	Clock output P
3	MA -	Clock output N
4	VDD	+5 V Adapter supply voltage* (supply current ca. 90 mA)
5	MO -	+5 V High-level signal (Optional: master data output N)
6	GND	Ground (0V)
7	SL +	Data input P
8	SL -	Data input N
9	MO +	0 V Low-level signal (Optional: master data output P)

^{*)} This operating voltage is supplied by iC-Haus demo boards.

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PC Plug Pin Configuration J1 (25-pole SUB-D)

LPT Name nStrobe Data0	Signal ALE n/a	Function Address latch enable Input
 Data7 nAck	 n/a n/a	
Busy	DB1-SO	SPI data output
PaperOut	EOT	End-of-transmission output
Select	NER	Error message (active low)
LineFeed	DB0-SI	SPI data input
nError	n/a	
nInitialize	NCS	Chip select input
nSelect	n/a	
GND	GND	Ground
 GND	 GND	 Ground
	nStrobe Data0 Data7 nAck Busy PaperOut Select LineFeed nError nInitialize nSelect GND	nStrobe ALE Data0 n/a Data7 n/a nAck n/a Busy DB1-SO PaperOut EOT Select NER LineFeed DB0-SI nError n/a nInitialize NCS nSelect n/a GND GND

Wiring circuit

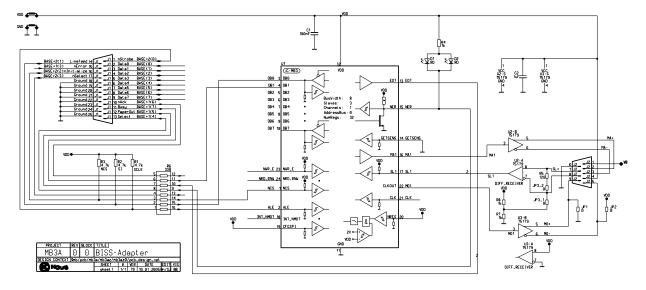


Fig. 1: MB3A schematic diagram. Jumper JP3_2 can be removed to enable single-ended SL line communication. (U3 is not assembled).

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