



# **Powerline N-Channel Single Switch IGBT Module**

**Preliminary Information** 

DS5360-1.1 May 2000

The GP2400ESM12 is a single switch 1200V, robust n channel enhancement mode insulated gate bipolar transistor (IGBT) module. Designed for low power loss, the module is suitable for a variety of high voltage applications in motor drives and power conversion. The high impedance gate simplifies gate drive considerations enabling operation directly from low power control circuitry.

Fast switching times allow high frequency operation making the device suitable for the latest drive designs employing pwm and high frequency switching. The IGBT has a wide reverse bias safe operating area (RBSOA) for ultimate reliability in demanding applications.

These modules incorporate electrically isolated base plates and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

The powerline range of high power modules includes dual and single switch configurations with a range of current and voltage capabilities to match customer system demands.

This device is optimised for traction drives and other applications requiring high thermal cycling capability.

#### **FEATURES**

- n Channel Enhancement Mode
- Non Punch Through Silicon
- High Gate Input Impedance
- Optimised For High Power High Frequency Operation
- Isolated MMC Base with AIN
- 1200V Rating
- 2400A Per Module

#### **APPLICATIONS**

- High Power Switching
- Motor Control
- Inverters
- Traction Drives

 $\begin{array}{c} \text{KEY PARAMETERS} \\ \text{V}_{\text{CES}} & 1200\text{V} \\ \text{V}_{\text{CE(sat)}} & \text{(typ)} & 2.7\text{V} \\ \text{I}_{\text{c}} & \text{(max)} & 2400\text{A} \\ \text{I}_{\text{cros}} & \text{(max)} & 4800\text{A} \end{array}$ 

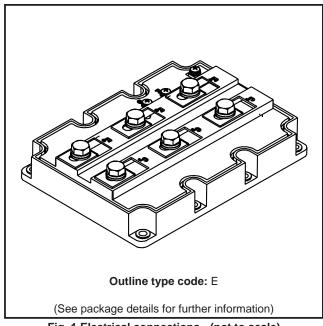


Fig. 1 Electrical connections - (not to scale)

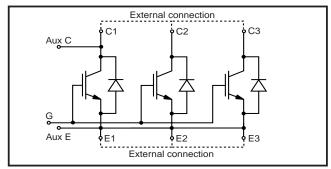


Fig.2 Single switch circuit diagram

#### ORDERING INFORMATION

Order As: GP2400ESM12

Note: When ordering, please use the whole part number.

## **ABSOLUTE MAXIMUM RATINGS**

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to Absolute Maximum Ratings for extended periods may affect device reliability.

T<sub>case</sub> = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Max.	Units
V <sub>CES</sub>	Collector-emitter voltage	$V_{GE} = 0V$	1200	V
V <sub>GES</sub>	Gate-emitter voltage	-	±20	V
I <sub>c</sub>	Continuous collector current	DC, T <sub>case</sub> = 75°C, T <sub>j</sub> = 125°C	2400	А
I <sub>C(PK)</sub>	Peak collector current	1ms, $T_{case} = 75^{\circ}C$ , $T_{j} = 125^{\circ}C$	4800	А
P <sub>max</sub>	Max. power dissipation	$T_{case} = 25^{\circ}C$ (Transistor), $T_{j} = 150^{\circ}C$	20.8	kW
V <sub>isol</sub>	Isolation voltage	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	2500	V

## THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
R <sub>th(j-c)</sub>	Thermal resistance - transistor	DC junction to case	-	6	°C/kW
R <sub>th(j-c)</sub>	Thermal resistance - diode	DC junction to case	-	13.3	°C/kW
R <sub>th(c-h)</sub>	Thermal resistance - case to heatsink (per module)	Mounting torque 5Nm	-	6	°C/kW
		(with mounting grease)			
T <sub>j</sub>	Junction temperature	Transistor	-	150	°C
		Diode	-	125	°C
T <sub>stg</sub>	Storage temperature range	-	-40	125	°C
-	Screw torque	Mounting - M6	-	5	Nm
		Electrical connections - M4	-	2	Nm
		Electrical connections - M8	-	10	Nm

## **ELECTRICAL CHARACTERISTICS**

T<sub>case</sub> = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
I <sub>CES</sub>	Collector cut-off current	$V_{GE} = 0V, V_{CE} = V_{CES}$	-	-	3	mA
		$V_{GE} = 0V$ , $V_{CE} = V_{CES}$ , $T_{case} = 125$ °C	-	-	100	mA
I <sub>GES</sub>	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$	-	-	12	μА
V <sub>GE(TH)</sub>	Gate threshold voltage	$I_{\rm C}$ = 120mA, $V_{\rm GE}$ = $V_{\rm CE}$	4	-	7.5	V
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 2400A	-	2.7	3.5	V
		$V_{GE} = 15V, I_{C} = 2400A, T_{case} = 125^{\circ}C$	-	3.2	4.0	V
I <sub>F</sub>	Diode forward current	DC, T <sub>case</sub> = 50°C, T <sub>j</sub> = 125°C	-	-	2400	Α
I <sub>FM</sub>	Diode maximum forward current	$t_p = 1 \text{ms}, T_j = 125^{\circ}\text{C}$	-	-	4800	А
V <sub>F</sub>	Diode forward voltage	I <sub>F</sub> = 2400A	-	2.2	2.4	V
		I <sub>F</sub> = 2400A, T <sub>case</sub> = 125°C	-	2.3	2.5	V
C <sub>ies</sub>	Input capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz	-	270	-	nF
L <sub>M</sub>	Module inductance	-	-	10	-	nH

# **ELECTRICAL CHARACTERISTICS**

For definition of switching waveforms, refer to figure 3 and 4.

 $T_{case} = 25$ °C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 2400A	-	2300	-	ns
t <sub>f</sub>	Fall time	$V_{GE} = \pm 15V$	-	400	-	ns
E <sub>OFF</sub>	Turn-off energy loss	V <sub>CE</sub> = 600V	-	820	-	mJ
t <sub>d(on)</sub>	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 3.3\Omega$	-	2600	-	ns
t <sub>r</sub>	Rise time	L ~ 80nH	-	1100	-	ns
E <sub>on</sub>	Turn-on energy loss		-	490	-	mJ
Q <sub>rr</sub>	Diode reverse recovery charge	$I_F = 2400A$ , $V_R = 50\% V_{CES}$ ,	-	200	-	μС
		$dI_F/dt = 2000A/\mu s$				

# T<sub>case</sub> = 125°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 2400A	-	2570	-	ns
t <sub>f</sub>	Fall time	$V_{GE} = \pm 15V$	-	400	-	ns
E <sub>OFF</sub>	Turn-off energy loss	V <sub>CE</sub> = 600V	-	980	-	mJ
t <sub>d(on)</sub>	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 3.3\Omega$	-	2650	-	ns
t,	Rise time	L ~ 80nH	-	1000	-	ns
E <sub>on</sub>	Turn-on energy loss		-	620	-	mJ
Q <sub>rr</sub>	Diode reverse recovery charge	$I_F = 2400A, V_R = 50\% V_{CES},$	-	400	-	μС
		$dI_F/dt = 2000A/\mu s$				

## **SWITCHING DEFINITIONS**

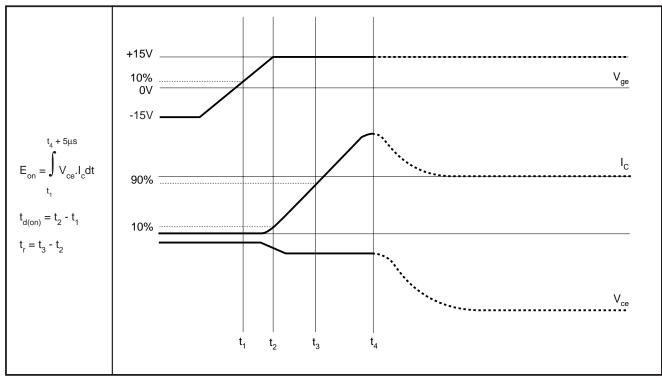


Fig.3 Definition of turn-on switching times

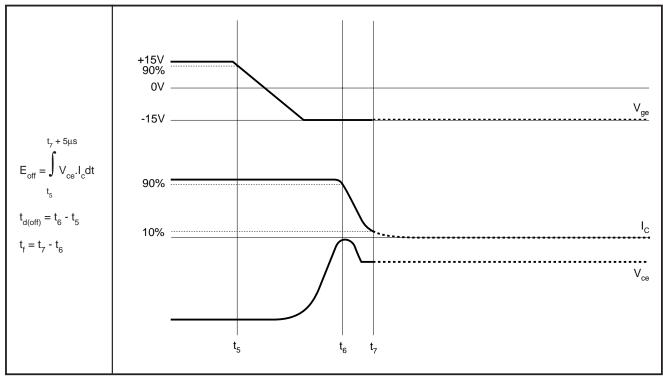
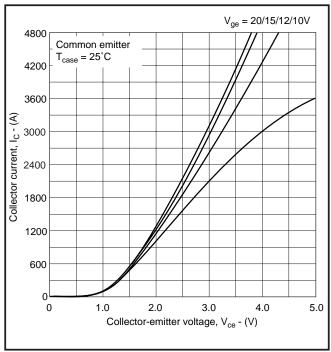


Fig.4 Definition of turn-off switching times

## TYPICAL CHARACTERISTICS



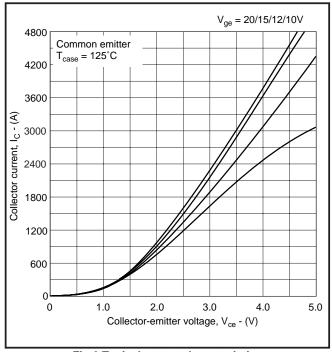


Fig.5 Typical output characteristics



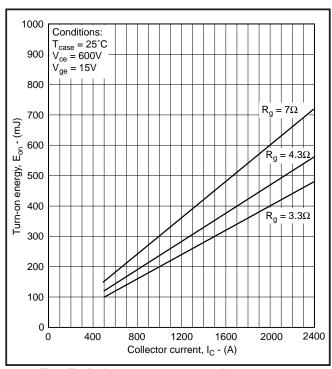


Fig.7 Typical turn-on energy vs collector current

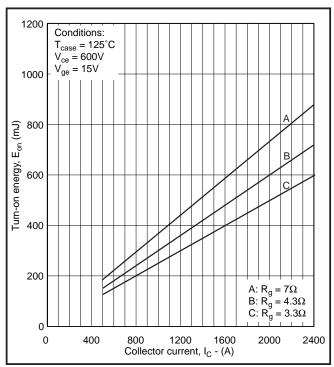
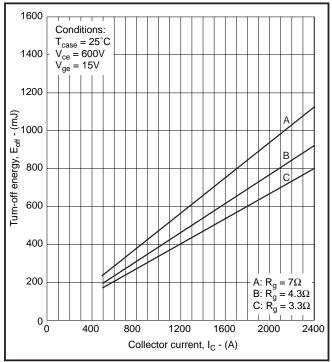


Fig.8 Typical turn-on energy vs collector current



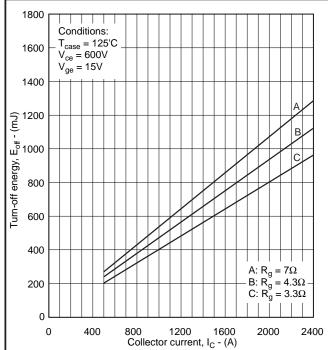
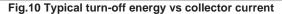
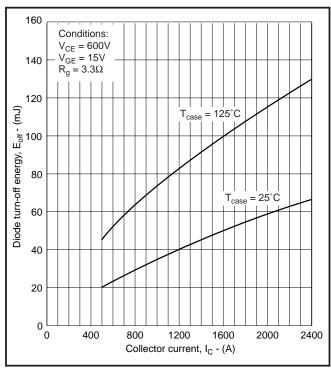
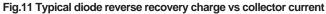


Fig.9 Typical turn-off energy vs collector current







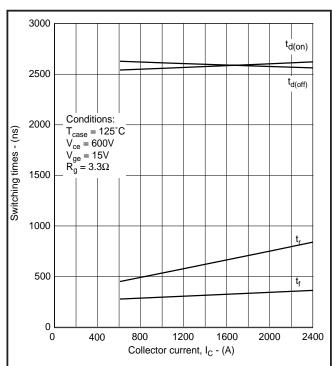
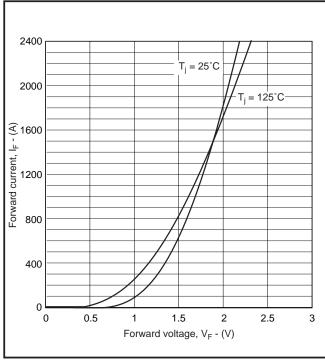


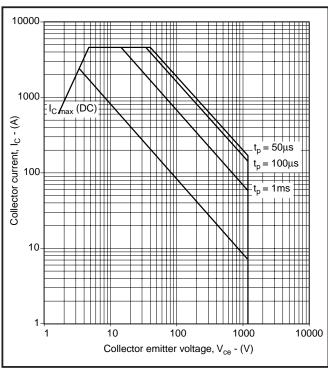
Fig.12 Typical switching characteristics



3500 3000 2500 Collector current, I<sub>C</sub> - (A) 120000 12000 1000 Conditions: 500  $T_{case} = 125$ 'C  $V_{ge} = 15$  $R_g = 3.3 \text{ ohms}$ 200 400 1000 1200 1400 600 800 Collector emitter voltage, V<sub>ce</sub> - (V)

Fig.13 Diode typical forward characteristics

Fig.14 Reverse bias safe operating area



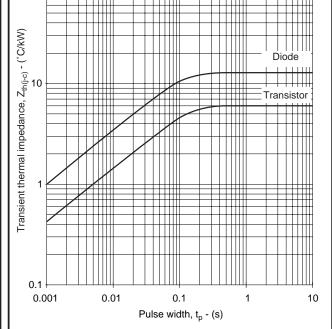
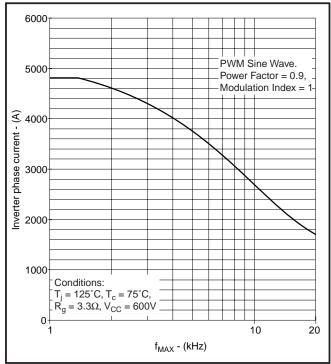


Fig.15 Forward bias safe operating area

Fig.16 Transient thermal impedance

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures.

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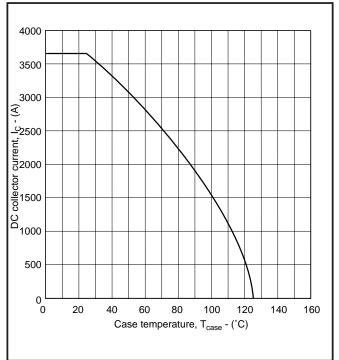
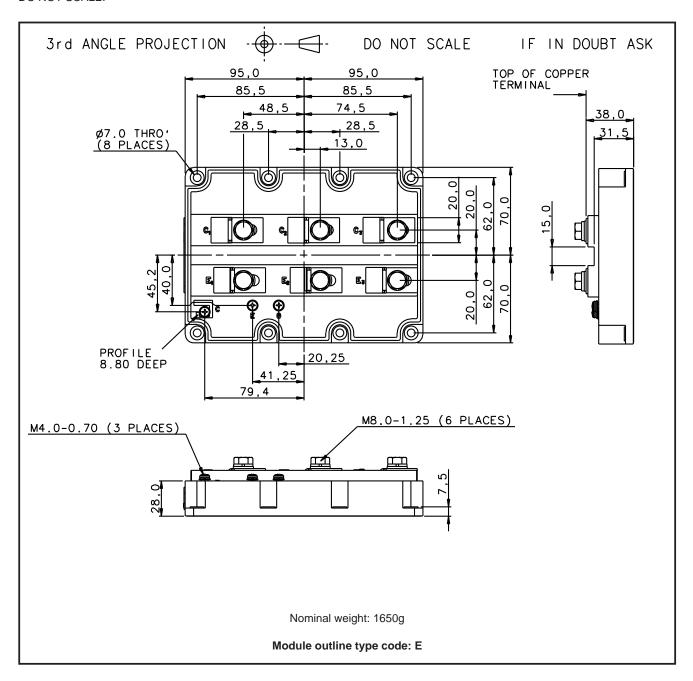


Fig.18 3-Phase inverter operating frequency

Fig.19 DC current rating vs case temperature

## **PACKAGE DETAILS**

For further package information, please contact your local Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



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#### **ASSOCIATED PUBLICATIONS**

Title	Application Note	
	Number	
Electrostatic handling precautions	AN4502	
An introduction to IGBTs	AN4503	
IGBT ratings and characteristics	AN4504	
Heatsink requirements for IGBT modules	AN4505	
Calculating the junction temperature of power semiconductors	AN4506	
Gate drive considerations to maximise IGBT efficiency	AN4507	
Parallel operation of IGBTs – punch through vs non-punch through characteristics	AN4508	
Guidance notes for formulating technical enquiries	AN4869	
Principle of rating parallel connected IGBT modules	AN5000	
Short circuit withstand capability in IGBTs	AN5167	
Driving high power IGBTs with concept gate drivers	AN5190	

#### POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than a basic semiconductor switch, and has developed a flexible range of heatsink / clamping systems in line with advances in device types and the voltage and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group continues to offer high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete solution (PACs).

#### **HEATSINKS**

Power Assembly has its own proprietary range of extruded aluminium heatsinks. They have been designed to optimise the performance or our semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest Sales Representative or the factory.



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