# Memory FRAM **CMOS**

# 1 M Bit (128 $K \times 8$ )

# MB85R1001

#### **■ DESCRIPTIONS**

The MB85R1001 is an FRAM (Ferroelectric Random Access Memory) chip in a configuration of 131,072 words x 8 bits, using the ferroelectric process and silicon gate CMOS process technologies for forming the nonvolatile memory cells.

Unlike SRAM, MB85R1001 is able to retain data without back-up battery.

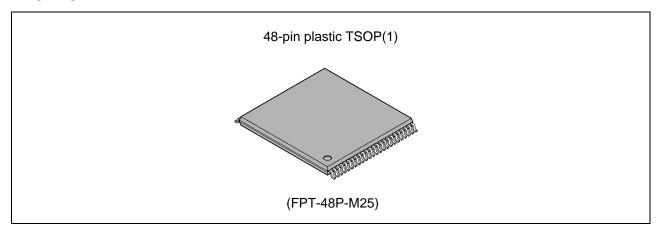
The memory cells used for the MB85R1001 has improved at least 1010 times of read/write access, significantly outperforming FLASH memory and E<sup>2</sup>PROM in endurance.

The MB85R1001 used a pseudo - SRAM interface compatible with conventional asynchronous SRAM.

#### **■ FEATURES**

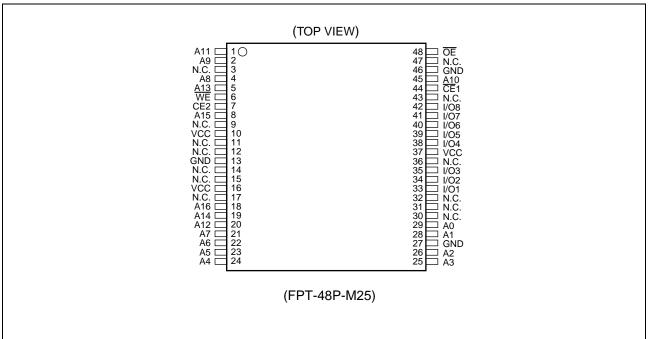
- Bit configuration: 131,072 words x 8bits
- Read/write endurance : 10<sup>10</sup> times
- Operating power supply voltage: 3.0 V to 3.6 V
- Operating temperature range : -20 °C to +85 °C
- 48-pin, TSOP (1) plastic package

#### **■ PACKAGE**





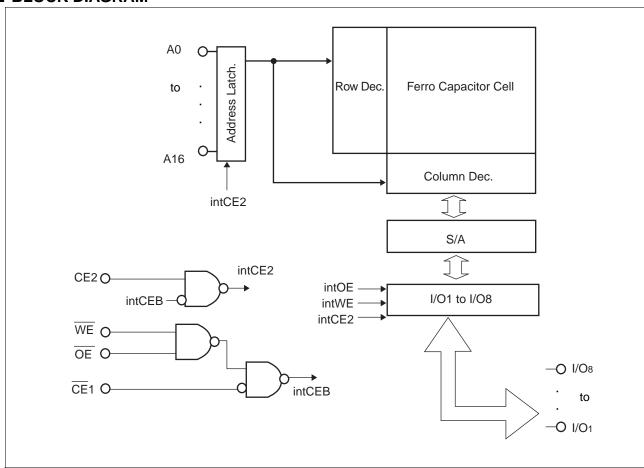
#### **■ PIN ASSIGNMENTS**



#### **■ PIN DESCRIPTIONS**

Pin name	Function
A0 to A16	Address In
I/O1 to I/O8	Data Input/Output
CE1	Chip Enable 1 in
CE2	Chip Enable 2 in
WE	Write Enable in
ŌĒ	Output Enable in
VCC	Power Supply
GND	Ground

#### **■ BLOCK DIAGRAM**



#### **■ FUNCTION TRUTH TABLE**

Operation Mode	CE1	CE2	WE	ŌĒ	I/O <sub>1</sub> to I/O <sub>8</sub>	Supply Current
	Н	X	Х	Х		0(
Standby Pre-charge	X	L	X	X	High-Z	Standby (IsB)
	X	X	Н	Н		(100)
Read	<b>7-1 L</b>	H	Ι	L	Dout	
Read (Pseudo SRAM, <del>OE</del> control)	L	Н	Н	¥		
Write	٦ <u>ـ</u>	H	L	Н	Din	Operation (Icc)
Write (Pseudo SRAM, WE control)	L	Н	FL	Н		
Output Disable	L	Н	Н	Н	High-Z	

 $\begin{array}{l} L=V_{IL},\,H=V_{IH},\,X\,\,can\,\,be\,\,either\,\,V_{IL}\,\,or\,\,V_{IH},\,High-Z=High\,\,Impedance\\ & \qquad \qquad \\ & \qquad \qquad \\ & \qquad \qquad \\ :\,Latch\,\,address\,\,at\,\,rising\,\,edge \end{array}$ 

#### ■ ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Rat	Rating		
raiailietei	Symbol	Min	Max	Unit	
Supply Voltage	Vcc	-0.5	+4.0	V	
Input Voltage	Vin	-0.5	Vcc+0.5	V	
Output Voltage	Vouт	-0.5	Vcc+0.5	V	
Ambient Temperature	TA	-20	+85	°C	
Storage Temperature	Tstg	-40	+125	°C	

WARNING: Semiconductor devices can be permanently damaged by application of stress (voltage, current, temperature, etc.) in excess of absolute maximum ratings. Do not exceed these ratings.

#### ■ RECOMMENDED OPERATING CONDITIONS

 $(Vcc = 3.0 V to 3.6 V, T_A = -20 °C to +85 °C)$ 

Parameter	Symbol		l lni4		
Farameter	Symbol	Min	Тур	Max	Unit
Supply Voltage	Vcc	3.0	3.3	3.6	V
Input Voltage (high)	Vıн	Vcc x 0.8		Vcc + 0.5	V
Input Voltage (low)	VıL	-0.5		0.8	V
Operating Temperature	TA	-20	_	+85	°C

WARNING: The recommended operating conditions are required in order to ensure the normal operation of the semiconductor device. All of the device's electrical characteristics are warranted when the device is operated within these ranges.

> Always use semiconductor devices within their recommended operating condition ranges. Operation outside these ranges may adversely affect reliability and could result in device failure.

> No warranty is made with respect to uses, operating conditions, or combinations not represented on the data sheet. Users considering application outside the listed conditions are advised to contact their FUJITSU representatives beforehand.

#### **■ ELECTRICAL CHARACTERISTICS**

#### 1. DC CHARACTERISTICS

(Vcc = 3.0 V to 3.6 V,  $T_A = -20$  °C to +85 °C)

Parameter	Symbol	Symbol Test Condition		Value			
Farameter	Syllibol	rest Condition	Min	Тур	Max	Unit	
Input Leakage Current	lu	V <sub>IN</sub> = 0 V to V <sub>CC</sub>	_	_	10	μΑ	
Output Leakage Current	<b> </b> LO	$V_{OUT} = 0 \text{ V to } V_{CC},$ $\overline{CE}1 = V_{IH} \text{ or } \overline{OE} = V_{IH}$	_	_	10	μΑ	
Supply Current	Icc	CE1 = 0.2 V, CE2 = Vcc-0.2 V, Iout = 0 mA*1	_	_	10	mA	
Standby Current	Іѕв	$\overline{CE}1 \ge V_{CC}-0.2 \text{ V}$ $CE2 \le 0.2 \text{ V*}^2$ $\overline{OE} \ge V_{CC}-0.2 \text{ V}, \overline{WE} \ge V_{CC}-0.2 \text{ V*}^2$	_	10	100	μΑ	
Output Voltage (high)	Vон	$I_{OH} = -2.0 \text{ mA}$	0.8 x Vcc		_	V	
Output Voltage (low)	Vol	IoL = 2.0 mA			0.4	V	

<sup>\*1 :</sup> Iout : Output current

<sup>\*2 :</sup> All other inputs ( $\overline{CE}1$ , CE2,  $\overline{OE}$ ,  $\overline{WE}$ ) should be at CMOS levels, i.e., H  $\geq$  Vcc - 0.2 V, L  $\leq$  0.2 V.

#### 2. AC CHARACTERISTICS

#### • AC TEST CONDITIONS

Supply Voltage : 3.0 V to 3.6 VOperating Temperature : -20 °C to +85 °CInput Voltage Amplitude : 0.3 V to 2.7 V

Input Rising Time : 10 ns
Input Falling Time : 10 ns
Input Evaluation Level : 2.0 V / 0.8 V
Output Evaluation Level : 2.0 V / 0.8 V
Output Impedance : 50 pF

#### (1) Read Operation

(Vcc = 3.0 V to 3.6 V,  $T_A = -20$  °C to +85 °C)

Parameter	Symbol	Va	Unit	
Farameter	Зуньы	Min	Max	Offic
Read Cycle Time	<b>t</b> RC	250	_	ns
CE1 Active Time	t <sub>CA1</sub>	210	2,000	ns
OE Active Time	<b>t</b> RP	210	2,000	ns
Pre-charge Time	<b>t</b> PC	40		ns
Address Setup Time	<b>t</b> as	10	_	ns
Address Hold Time	<b>t</b> AH	50	_	ns
OE Setup Time	<b>t</b> es	10		ns
CE1 Access Time	t <sub>CE1</sub>	_	100	ns
CE2 Access Time	t <sub>CE2</sub>	_	100	ns
OE Access Time	<b>t</b> oe	_	100	ns
OE Output Floating Time	tонz	_	25	ns

#### (2) Write Operation

(Vcc = 3.0 V to 3.6 V,  $T_A = -20$  °C to +85 °C)

Parameter	Symbol	Val	Unit	
i didilicitoi	Symbol	Min	Max	Oilit
Write Cycle Time	twc	250	_	ns
CE1 Active Time	t <sub>CA1</sub>	210	2,000	ns
CE2 Active Time	t <sub>CA2</sub>	210	2,000	ns
Pre-charge Time	<b>t</b> PC	40	_	ns
Address Setup Time	tas	10	_	ns
Address Hold Time	tан	50	_	ns
Write Pulse Width	<b>t</b> wp	210	_	ns
Data Setup Time	tos	10	_	ns
Data Hold Time	tон	50	_	ns
Write Setup Time	<b>t</b> ws	0	_	ns

#### (3) Power ON/OFF Sequence

Parameter			Units		
rai ametei	bol	Min	Тур	Max	Ullita
CE1 LEVEL holding time in Power OFF	<b>t</b> pd	85	_	_	ns
CE1 LEVEL holding time in Power ON	<b>t</b> pu	85	_	_	ns
Power interval *	<b>t</b> pi	0.5	_	_	S

<sup>\*:</sup> Condition for power detection circuit to function

### 3. Pin Capacitance

 $(f = 1 \text{ MHz}, T_A = +25 \, ^{\circ}\text{C})$ 

Parameter	Symbol	Test Condition		Value		Unit
Farameter	Syllibol	rest Condition	Min	Тур	Max	Oilit
Input Capacitance	Cin	VIN = GND	_	_	10	pF
Output Capacitance	Соит	Vout = GND	_	_	10	pF

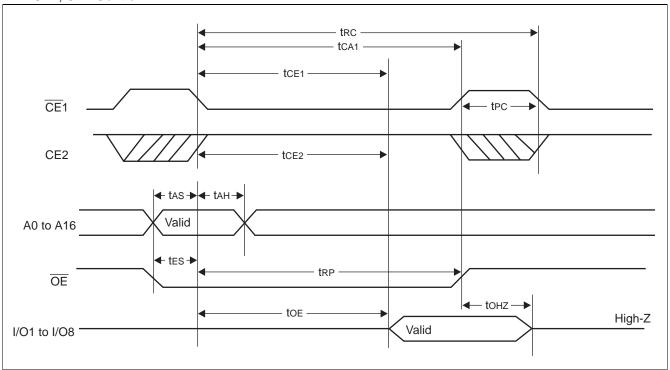
### 4. Reliability

Data retention 10 years ( $T_A = 0$  °C to +55 °C) Access endurance 10<sup>10</sup> times ( $T_A = -20$  °C to +85 °C)

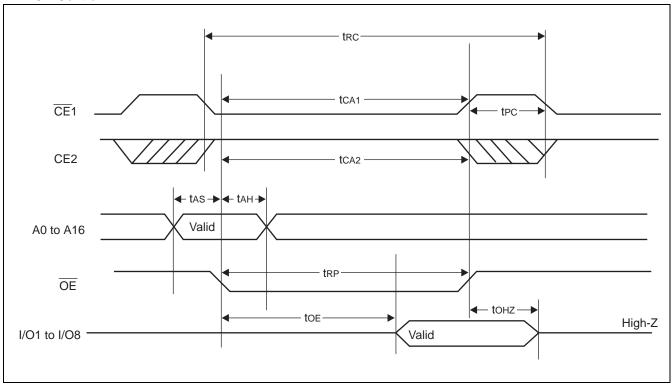
#### **■ TIMING DIAGRAMS**

### 1. Read Cycle Timing

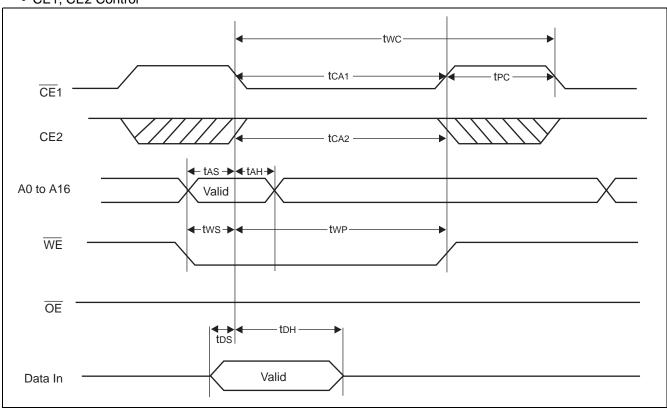
• CE1, CE2 Control



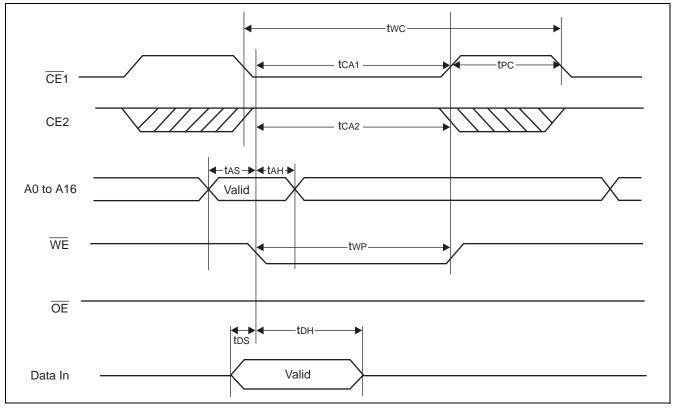
### • OE Control



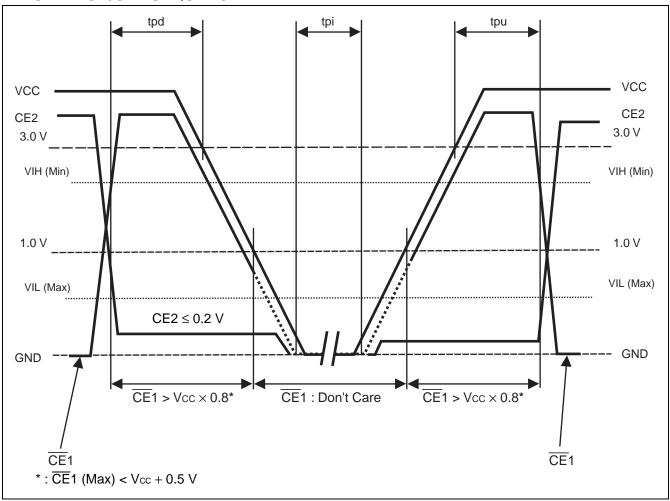
# 2. Write Cycle Timing • CE1, CE2 Control



### • WE Control



### **■ POWER ON/OFF SEQUENCE**



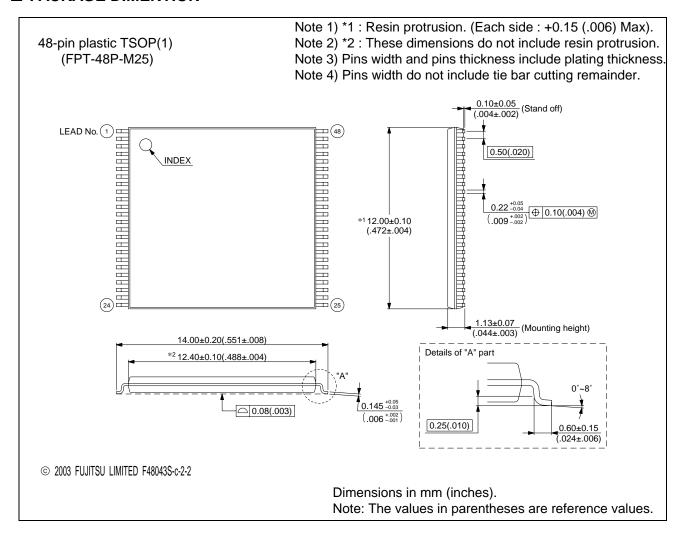
#### **■ NOTES ON USE**

After IR reflow, the hold of data that was written before IR reflow is not guaranteed.

#### **■** ORDERING INFOMATION

Part number	Package	Remarks
MB85R1001PFTN	48-pin plastic TSOP(1) (FPT-48P-M25)	

#### **■ PACKAGE DIMENTION**



## **FUJITSU LIMITED**

All Rights Reserved.

The contents of this document are subject to change without notice. Customers are advised to consult with FUJITSU sales representatives before ordering.

The information, such as descriptions of function and application circuit examples, in this document are presented solely for the purpose of reference to show examples of operations and uses of Fujitsu semiconductor device; Fujitsu does not warrant proper operation of the device with respect to use based on such information. When you develop equipment incorporating the device based on such information, you must assume any responsibility arising out of such use of the information. Fujitsu assumes no liability for any damages whatsoever arising out of the use of the information.

Any information in this document, including descriptions of function and schematic diagrams, shall not be construed as license of the use or exercise of any intellectual property right, such as patent right or copyright, or any other right of Fujitsu or any third party or does Fujitsu warrant non-infringement of any third-party's intellectual property right or other right by using such information. Fujitsu assumes no liability for any infringement of the intellectual property rights or other rights of third parties which would result from the use of information contained herein.

The products described in this document are designed, developed and manufactured as contemplated for general use, including without limitation, ordinary industrial use, general office use, personal use, and household use, but are not designed, developed and manufactured as contemplated (1) for use accompanying fatal risks or dangers that, unless extremely high safety is secured, could have a serious effect to the public, and could lead directly to death, personal injury, severe physical damage or other loss (i.e., nuclear reaction control in nuclear facility, aircraft flight control, air traffic control, mass transport control, medical life support system, missile launch control in weapon system), or (2) for use requiring extremely high reliability (i.e., submersible repeater and artificial satellite).

Please note that Fujitsu will not be liable against you and/or any third party for any claims or damages arising in connection with above-mentioned uses of the products.

Any semiconductor devices have an inherent chance of failure. You must protect against injury, damage or loss from such failures by incorporating safety design measures into your facility and equipment such as redundancy, fire protection, and prevention of over-current levels and other abnormal operating conditions.

If any products described in this document represent goods or technologies subject to certain restrictions on export under the Foreign Exchange and Foreign Trade Law of Japan, the prior authorization by Japanese government will be required for export of those products from Japan.

#### F0501

© 2005 FUJITSU LIMITED Printed in Japan