

Silicon NPN Power Transistors

2SC2460

DESCRIPTION

- With TO-3 package
- Complement to type 2SA1050
- Excellent safe operating area

APPLICATIONS

- For audio and general purpose power amplifier applications

PINNING(see Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

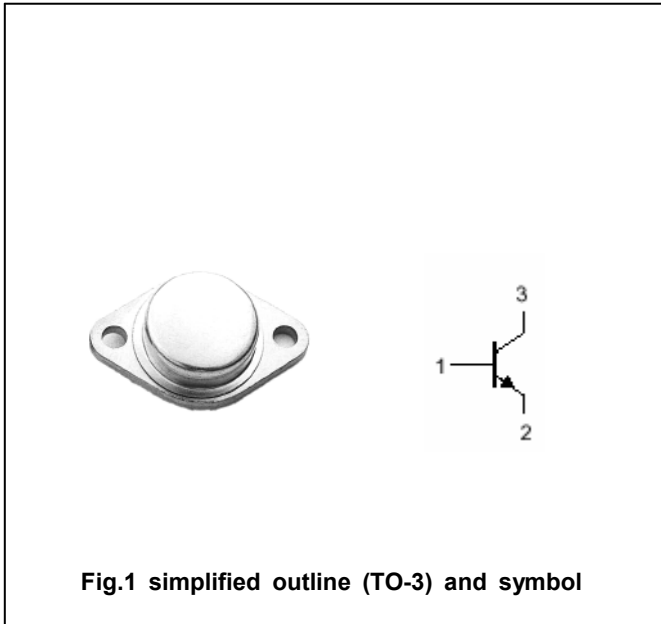


Fig.1 simplified outline (TO-3) and symbol

Absolute maximum ratings(Ta=□)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	140	V
V _{CEO}	Collector-emitter voltage	Open base	140	V
V _{EBO}	Emitter-base voltage	Open collector	5	V
I _C	Collector current		12	A
P _C	Collector power dissipation	T _C =25□	100	W
T _j	Junction temperature		175	□
T _{stg}	Storage temperature		-55~200	□

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CHARACTERISTICS

T_j=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =25mA ; I _B =0	140			V
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =1mA ; I _E =0	140			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =1mA ; I _C =0	5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =8A ; I _B =0.8A			2.5	V
V _{BE}	Base-emitter on voltage	I _C =6A ; V _{CE} =5V			1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =140V ; I _E =0			10	μA
I _{EBO}	Emitter cut-off current	V _{EB} =5V ; I _C =0			10	μA
h _{FE-1}	DC current gain	I _C =1A ; V _{CE} =5V	55		240	
h _{FE-2}	DC current gain	I _C =6A ; V _{CE} =-5V	35			
f _T	Transition frequency	I _C =1A ; V _{CE} =10V		70		MHz

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PACKAGE OUTLINE



Fig.2 outline dimensions (unindicated tolerance:±0.1mm)